

Product Summary

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D $T_A = +25^\circ\text{C}$
60V	$2\Omega @ V_{GS} = 4V$	100mA
	$2.5\Omega @ V_{GS} = 2.5V$	50mA

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **ESD Protected**
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

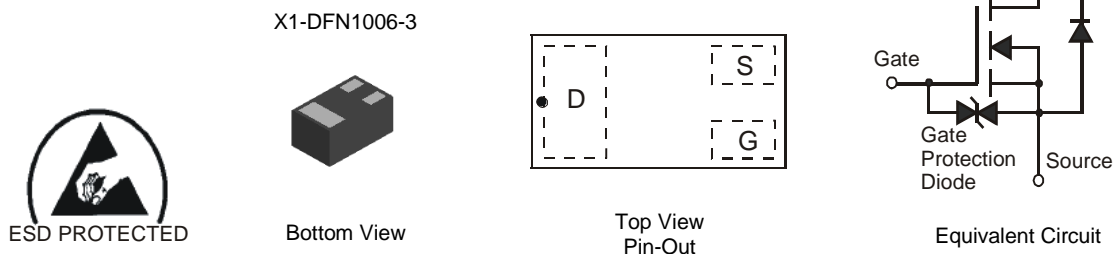
Description and Applications

This new generation MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- DC-DC Converters
- Power Management Functions
- Battery Operated Systems and Solid-State Relays
- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.

Mechanical Data

- Case: X1-DFN1006-3
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish — NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (e4)
- Weight: 0.001 grams (Approximate)



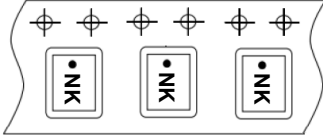
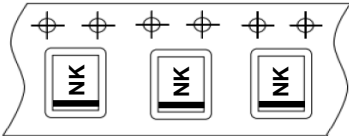

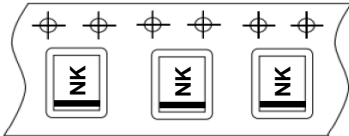


Ordering Information (Note 4)

Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
DMN62D0LFB-7	NK	7	8	3,000
DMN62D0LFB-7B	NK	7	8	10,000

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information

<p>DMN62D0LFB-7</p>	<div style="display: flex; justify-content: space-around; align-items: flex-start;"> <div style="text-align: center;">  <p>Top View Dot Denotes Drain Side</p> </div> <div style="text-align: center;"> <p>From date code 1527 (YYWW), this changes to:</p>  <p>Top View Bar Denotes Gate and Source Side</p> </div> </div> <div style="display: flex; justify-content: space-around; margin-top: 20px;">   </div>
<p>DMN62D0LFB-7B</p>	<div style="text-align: center; margin-bottom: 20px;">  <p>Top View Bar Denotes Gate and Source Side</p> </div> <p style="text-align: right; margin-right: 100px;">NK = Part Marking Code</p> <div style="text-align: center;">  </div>

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	60	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 5) V _{GS} = 4.0V	Steady State	T _A = +25°C	I _D	100	mA
		T _A = +70°C		75	
Pulsed Drain Current (Note 6)			I _{DM}	200	mA

Thermal Characteristics

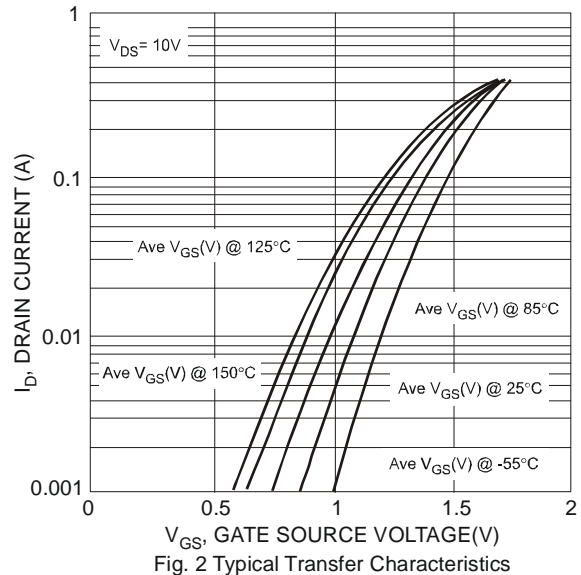
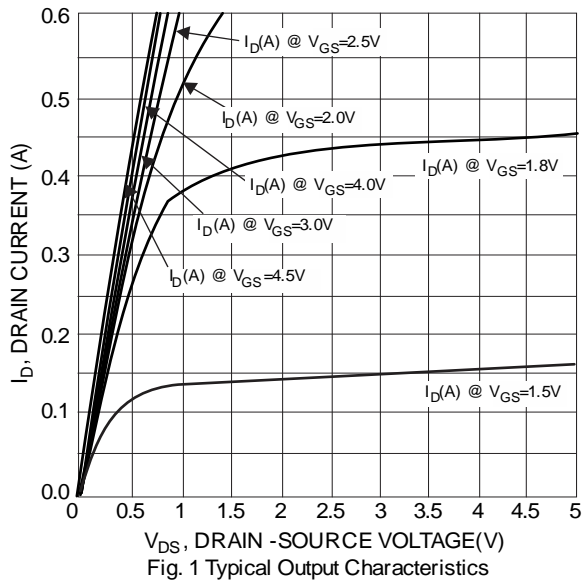
Characteristic	Symbol	Max	Unit
Power Dissipation (Note 5)	P _D	0.47	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 5)	R _{θJA}	258	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

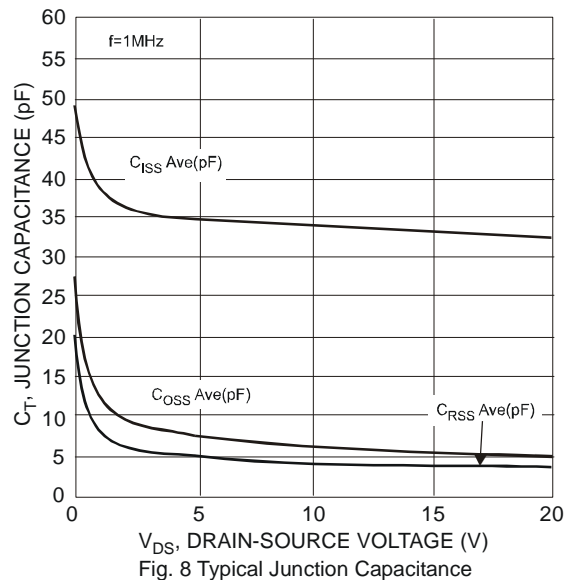
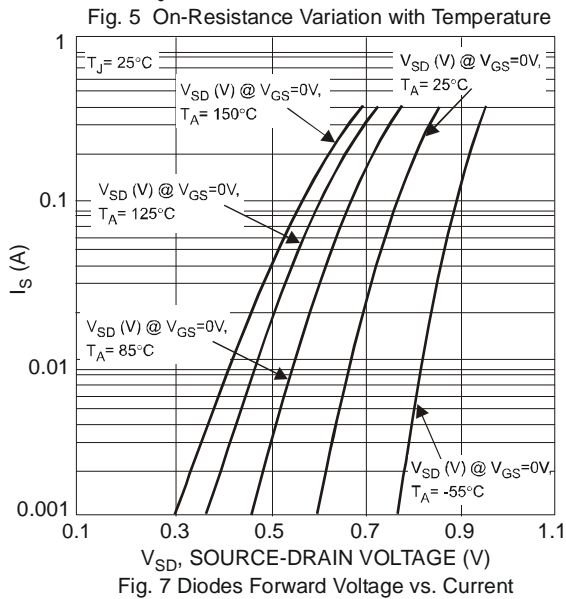
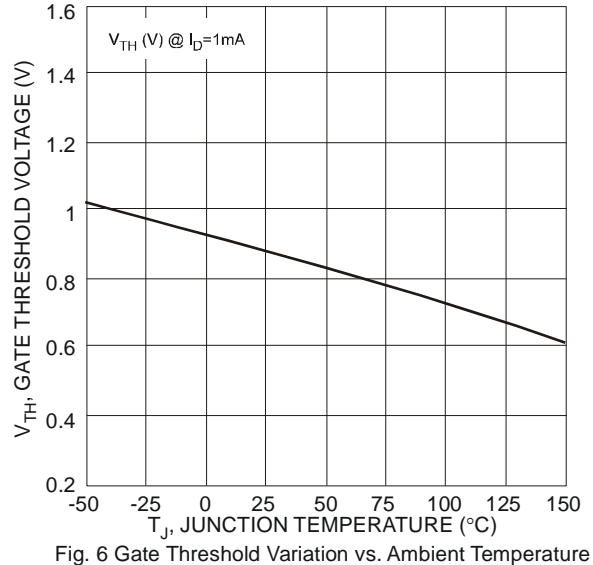
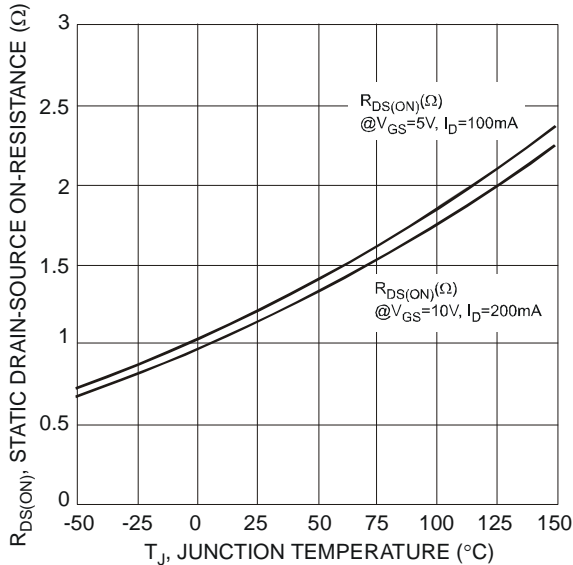
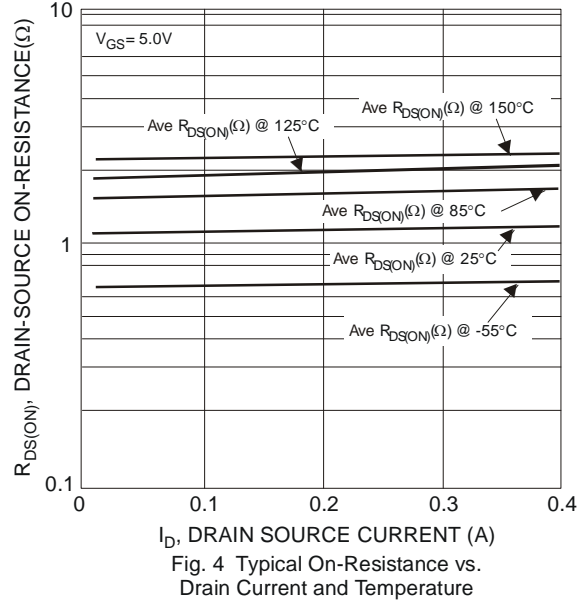
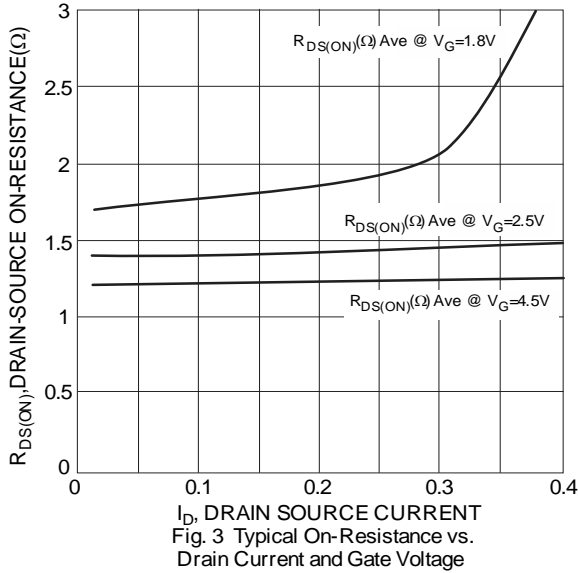
- Notes:
5. Device mounted on FR-4 PCB with minimum recommended pad layout, single sided.
 6. Repetitive rating, pulse width limited by junction temperature.

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise stated.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	60	-	-	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current $T_J = +25^\circ\text{C}$	I_{DSS}	-	-	1.0	μA	$V_{DS} = 60V, V_{GS} = 0V$
Gate-Source Leakage	I_{GSS}	-	-	± 100	nA	$V_{GS} = \pm 5V, V_{DS} = 0V$
		-	-	± 500	nA	$V_{GS} = \pm 10V, V_{DS} = 0V$
		-	-	± 2.0	μA	$V_{GS} = \pm 15V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(TH)}$	0.6	-	1.0	V	$V_{DS} = V_{GS}, I_D = 250\mu A$
Static Drain-Source On-Resistance	$R_{DS(ON)}$	-	1.3	2	Ω	$V_{GS} = 4V, I_D = 100mA$
		-	1.5	2.5		$V_{GS} = 2.5V, I_D = 50mA$
		-	1.9	3		$V_{GS} = 1.8V, I_D = 50mA$
		-	2.6	-		$V_{GS} = 1.5V, I_D = 10mA$
Forward Transfer Admittance	$ Y_{fs} $	-	0.8	-	S	$V_{DS} = 10V, I_D = 200mA$
Diode Forward Voltage	V_{SD}	-	0.9	1.3	V	$V_{GS} = 0V, I_S = 115mA$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C_{iss}	-	32	64	pF	$V_{DS} = 25V, V_{GS} = 0V, f = 1.0MHz$
Output Capacitance	C_{oss}	-	4.4	9		
Reverse Transfer Capacitance	C_{rss}	-	2.9	6		
Gate Resistance	R_g	-	126	250	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge	Q_g	-	0.45	0.9	nC	$V_{GS} = 4.5V, V_{DS} = 10V, I_D = 250mA$
Gate-Source Charge	Q_{gs}	-	0.08	0.2		
Gate-Drain Charge	Q_{gd}	-	0.08	0.2		
Turn-On Delay Time	$t_{D(ON)}$	-	3.4	10	ns	$V_{GS} = 10V, V_{DS} = 30V, R_L = 150\Omega, R_G = 25\Omega, I_D = 200mA$
Turn-On Rise Time	t_R	-	3.4	10		
Turn-Off Delay Time	$t_{D(OFF)}$	-	26.4	45		
Turn-Off Fall Time	t_F	-	16.3	30		

Notes: 7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to production testing.





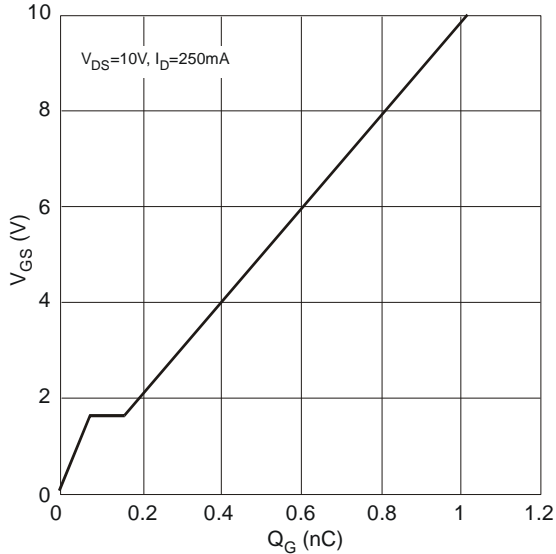


Fig. 9 Gate Charge Characteristics

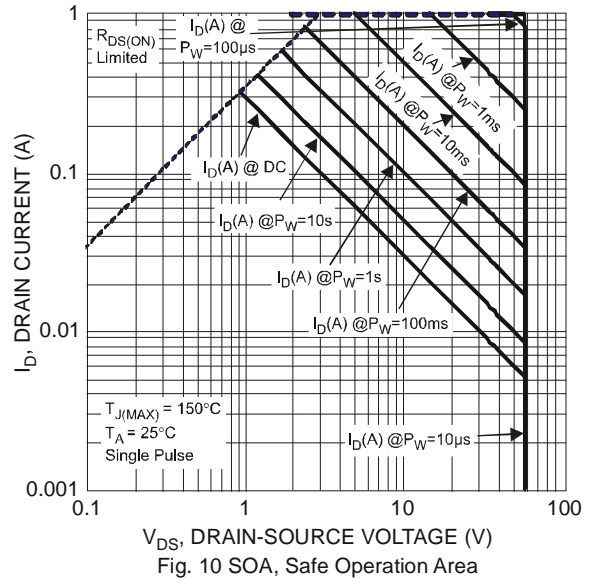


Fig. 10 SOA, Safe Operation Area

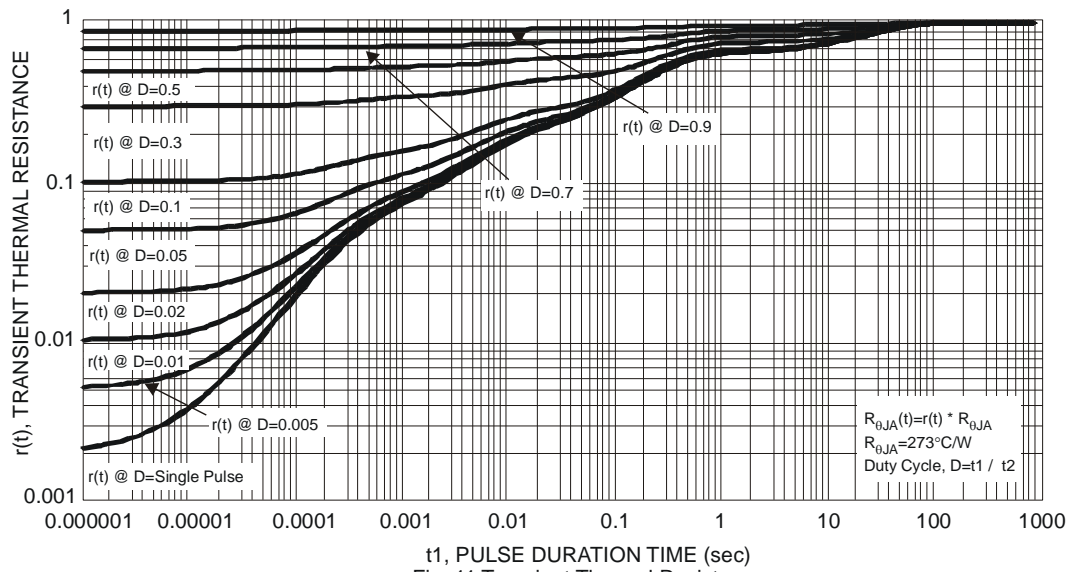
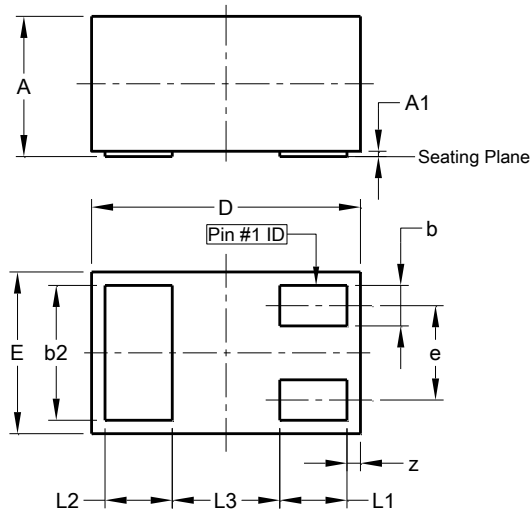


Fig. 11 Transient Thermal Resistance

Package Outline Dimensions

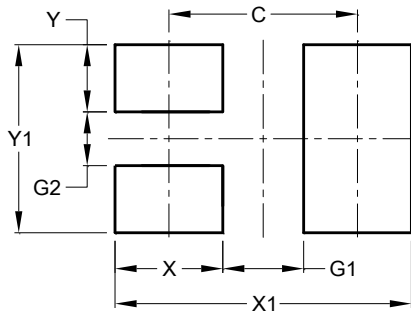
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.



X1-DFN1006-3			
Dim	Min	Max	Typ
A	0.47	0.53	0.50
A1	0.00	0.05	0.03
b	0.10	0.20	0.15
b2	0.45	0.55	0.50
D	0.95	1.075	1.00
E	0.55	0.675	0.60
e	-	-	0.35
L1	0.20	0.30	0.25
L2	0.20	0.30	0.25
L3	-	-	0.40
z	0.02	0.08	0.05
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
C	0.70
G1	0.30
G2	0.20
X	0.40
X1	1.10
Y	0.25
Y1	0.70

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