



N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	Rds(on)	I _D T _A = +25°C
60V	2Ω @ V _{GS} = 4V	310mA
	2.5Ω @ V _{GS} = 2.5V	295mA

Description

This new generation MOSFET has been designed to minimize the onstate resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- DC-DC Converters
- Power management functions
- Battery Operated Systems and Solid-State Relays
- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

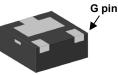
Mechanical Data

- Case: X1-DFN1212-3
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208 @4
- Terminal Connections: See Diagram
- Weight: 0.005 grams (approximate)

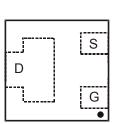


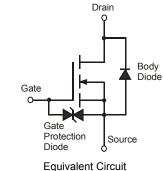


Top View



Bottom View





Pin-Out Top View

Ordering Information (Note 4)

Part Number	Compliance	Case	Packaging
DMN62D0LFD-7	Standard	X1-DFN1212-3	3,000/Tape & Reel
DMN62D0LFD-13	Standard	X1-DFN1212-3	10,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

 See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



K63 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: Y = 2013) M = Month (ex: 9 = September)

Date Code Key				
Year	2007			

Year	2007	2008	2009	2010	201	1 20)12	2013	2014	2015	2016	2017
Code	U	V	W	Х	Y		Z	А	В	С	D	E
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	g Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage		V _{DSS}	60	V
Gate-Source Voltage		V _{GSS}	±20	V
Continuous Drain Current (Note 5) V _{GS} = 4.0V	ID	310 260	mA	
Pulsed Drain Current (Note 6) (10µs pulse, duty cycle = 1%)	I _{DM}	1.0	A	

Thermal Characteristics

Characteristic	Symbol	Max	Unit
Power Dissipation (Note 5)	PD	0.48	W
Thermal Resistance, Junction to Ambient $@T_A = +25^{\circ}C$ (Note 5)	R _{0JA}	265	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)					_		
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	_	V	$V_{GS} = 0V, I_D = 250 \mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	_	—	1.0	μA	V _{DS} = 60V, V _{GS} = 0V	
		_	—	±100	nA	V_{GS} = ±5V, V_{DS} = 0V	
Gate-Source Leakage	I _{GSS}	—	_	±500	nA	$V_{GS} = \pm 10V, V_{DS} = 0V$	
		—	_	±2.0	μA	$V_{GS} = \pm 15 V, V_{DS} = 0 V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(th)}	0.6	—	1.0	V	V_{DS} = V_{GS} , I_D = 250 μ A	
		_	1.3	2		V_{GS} = 4V, I _D = 100mA	
Static Drain-Source On-Resistance	D	—	1.4	2.5	Ω	V _{GS} = 2.5V, I _D = 50mA	
	R _{DS(ON)}	—	1.8	3		V_{GS} = 1.8V, I _D = 50mA	
		_	2.4	_		V _{GS} = 1.5V, I _D = 10mA	
Forward Transfer Admittance	Y _{fs}	_	1.8	_	S	V _{DS} = 10V, I _D = 200mA	
Diode Forward Voltage	V _{SD}	_	0.8	1.3	V	V _{GS} = 0V, I _S = 115mA	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}	—	31	—			
Output Capacitance	C _{oss}	_	4.3	_	pF	V _{DS} = 25V, V _{GS} = 0V, f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	_	3.0	_		1 - 1.00012	
Gate Resistance	R _g	—	99	_	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1MHz	
Total Gate Charge	Qg	—	0.5	_			
Gate-Source Charge	Q _{gs}	_	0.09	_	nC	V_{GS} = 4.5V, V_{DS} = 10V, I _D = 250mA	
Gate-Drain Charge	Q _{gd}	_	0.07	_		ID - 25011A	
Turn-On Delay Time	t _{D(on)}	—	2.6	—	ns		
Turn-On Rise Time	tr	—	2.1		ns	$V_{GS} = 10V, V_{DS} = 30V,$	
Turn-Off Delay Time	t _{D(off)}	_	18	—	ns	$R_{L} = 150\Omega, R_{G} = 25\Omega,$ $I_{D} = 200mA$	
Turn-Off Fall Time	tf	_	8.7	—	ns		

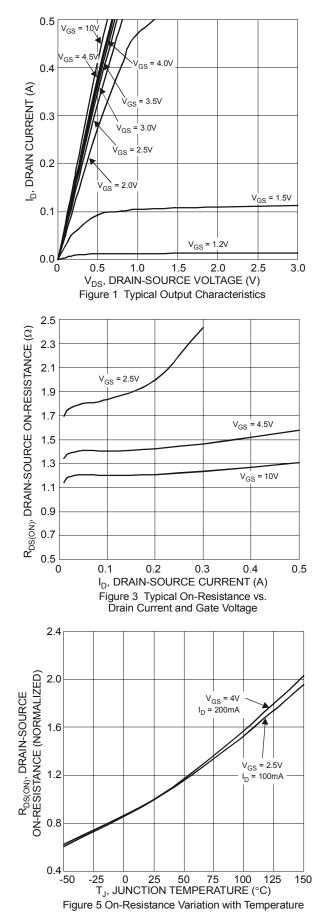
5. Device mounted on FR-4 PCB with minimum recommended pad layout, single sided.

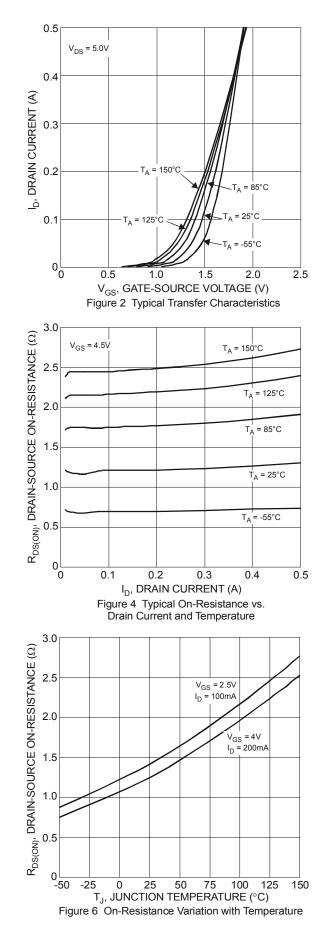
6. Repetitive rating, pulse width limited by junction temperature.

Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to production testing.

Notes:

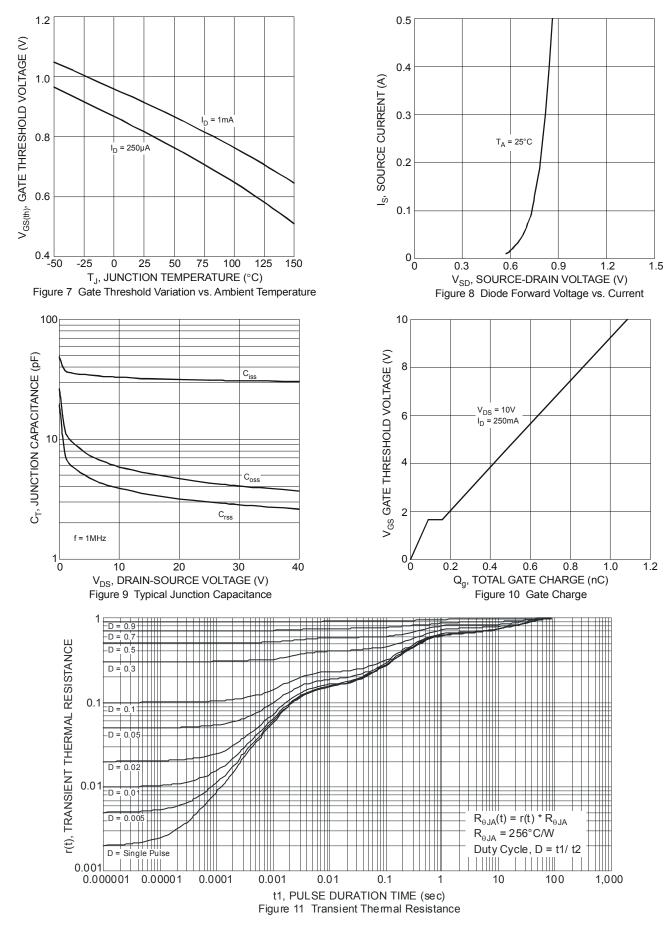






DMN62D0LFD Document number: DS36359 Rev. 2 - 2



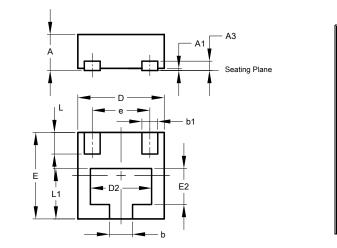


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Package Outline Dimensions

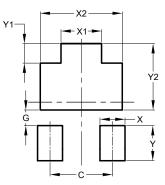
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



U-DFN1212-3								
Туре С								
Dim	Min	Max	Тур					
Α	0.47	0.53	0.50					
A1	0	0.05	0.02					
A3	-	-	0.13					
b	0.27	0.37	0.32					
b1	0.17	0.27	0.22					
D	1.15	1.25	1.20					
D2	0.75	0.95	0.85					
e	-	1	0.80					
Е	1.15	1.25	1.20					
E2	0.40	0.60	0.50					
L	0.25	0.35	0.30					
L1	0.65	0.75	0.70					
All D	imens	ions iı	n mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for latest version.



U-DFN1212-3 Type C				
Dimensions	Value			
С	0.800			
G	0.200			
Х	0.320			
X1	0.520			
X2	1.050			
Y	0.450			
Y1	0.250			
Y2 0.850				
All Dimensi	ions in mm			



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