



DMNH6012SPSQ

60V 175°C N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI

#### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>C</sub> = +25°C
60V	$11m\Omega @ V_{GS} = 10V$	50A

# **Description and Applications**

This MOSFET is designed to meet the stringent requirements of Automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- DC Motor Control
- Solenoid Driving
- Power Management Functions

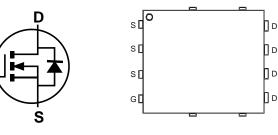
### **Features and Benefits**

- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching Ensures More Reliable and Robust End Application
- Low R<sub>DS(ON)</sub> Minimizes Power Losses
- Low Q<sub>G</sub> Minimizes Switching Losses
- <1.1mm Package Profile Ideal for Thin Applications
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

### **Mechanical Data**

- Case: PowerDl<sup>®</sup>5060-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (@3)
- Terminal Connections: See Diagram Below
- Weight: 0.097 grams (Approximate)





Internal Schematic

Top View Pin Configuration

# Ordering Information (Note 5)

Part Number	Case	Packaging
DMNH6012SPSQ-13	PowerDI5060-8	2,500/Tape & Reel

Notes: 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.

2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

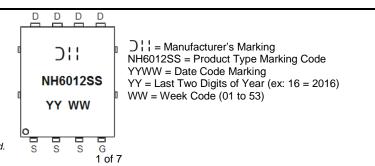
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3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to http://www.diodes.com/quality/product\_compliance\_definitions/.

5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

# **Marking Information**



PowerDI is a registered trademark of Diodes Incorporated. DMNH6012SPSQ Document number: DS38778 Rev. 1 - 2



### **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V <sub>DSS</sub>	60	V	
Gate-Source Voltage	V <sub>GSS</sub>	±20	V	
Continuous Drain Current (Note 7) V <sub>GS</sub> = 10V	T <sub>C</sub> = +25°C T <sub>C</sub> = +100°C	Ι <sub>D</sub>	50 30	A
Pulsed Drain Current (380µs Pulse, Duty Cycle = 1%)	IDM	120	A	
Maximum Continuous Body Diode Forward Current (Note 7)	Is	2.6	А	
Avalanche Current, L = 0.1mH (Note 8)	I <sub>AS</sub>	45	A	
Avalanche Energy, L = 0.1mH (Note 8)	E <sub>AS</sub>	100	mJ	

# **Thermal Characteristics**

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 6)		PD	1.6	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State		93	°C/W
Thermal Resistance, Sunction to Amblent (Note 0)	t<10s	$R_{ extsf{ heta}JA}$	51	
Total Power Dissipation (Note 7)		PD	3.1	W
Thermal Resistance, Junction to Ambient (Note 7)	Steady State	Devi	49	°C/W
Thermal Resistance, Junction to Amblent (Note 7)	t<10s	R <sub>0JA</sub>	26	
Thermal Resistance, Junction to Case	R <sub>0JC</sub>	3.8		
Operating and Storage Temperature Range		TJ. TSTG	-55 to +175	°C

# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 9)	<b>,</b> ,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,		- 71-				
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	—		V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current, T <sub>J</sub> = +25°C	I <sub>DSS</sub>	_	—	1	μA	$V_{DS} = 60V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	_	—	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 9)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	2	—	4	V	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>		8	11	mΩ	$V_{GS} = 10V, I_D = 50A$	
Diode Forward Voltage	V <sub>SD</sub>		0.7	1.2	V	$V_{GS} = 0V, I_{S} = 1.7A$	
DYNAMIC CHARACTERISTICS (Note 10)							
Input Capacitance	CISS		1,926	—	pF	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V, f = 1MHz	
Output Capacitance	C <sub>OSS</sub>		330	—	pF		
Reverse Transfer Capacitance	C <sub>RSS</sub>		112		pF		
Gate Resistance	R <sub>G</sub>		2.0	—	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Q <sub>G</sub>		16.3		nC		
Total Gate Charge (V <sub>GS</sub> = 10V)	Q <sub>G</sub>		35.2		nC	V 20V L 25A	
Gate-Source Charge	Q <sub>GS</sub>	_	7.6	_	nC	$V_{DS} = 30V, I_D = 25A$	
Gate-Drain Charge	Q <sub>GD</sub>	—	6.9	—	nC		
Turn-On Delay Time	t <sub>D(ON)</sub>	_	6.4	—	ns	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 30V,	
Turn-On Rise Time	t <sub>R</sub>	_	11.9		ns		
Turn-Off Delay Time	t <sub>D(OFF)</sub>		16.5		ns	$R_G = 3\Omega$ , $I_D = 25A$	
Turn-Off Fall Time	t <sub>F</sub>		5		ns	7	
Body Diode Reverse Recovery Time	t <sub>RR</sub>		28		ns		
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>		23		nC	$-I_F = 25A, di/dt = 100A/\mu s$	

Notes: 6. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.

7. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.

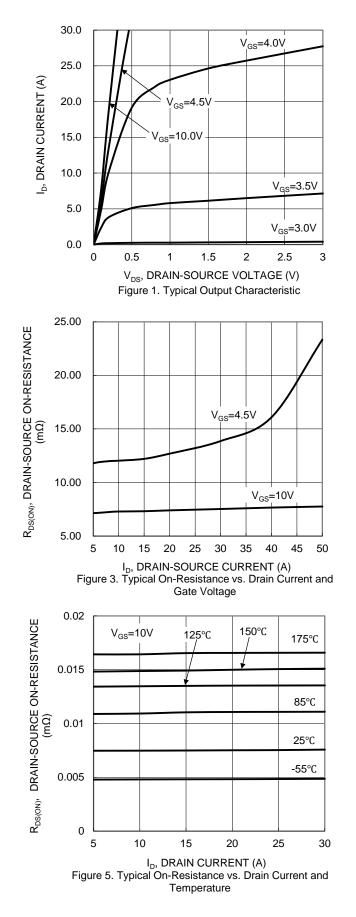
8. I<sub>AS</sub> and E<sub>AS</sub> ratings are based on low frequency and duty cycles to keep  $T_J = +25^{\circ}C$ .

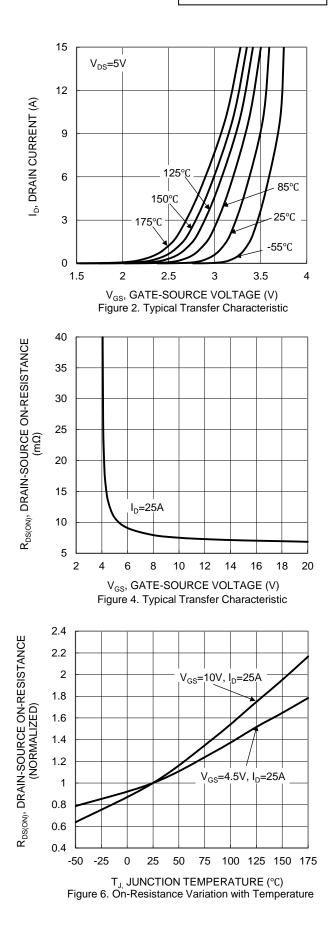
9. Short duration pulse test used to minimize self-heating effect.

10. Guaranteed by design. Not subject to product testing.



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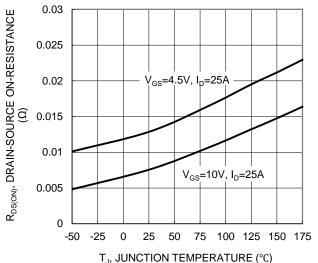




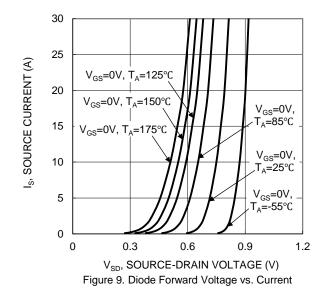
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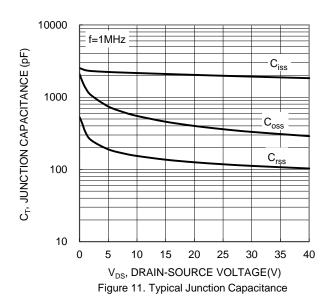


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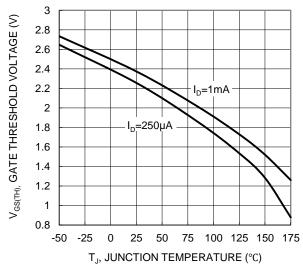
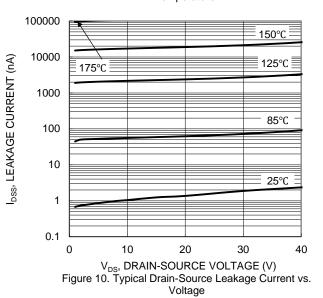
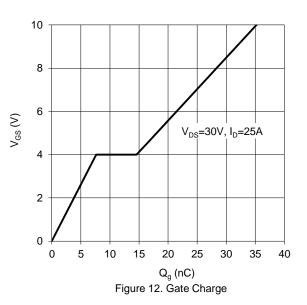
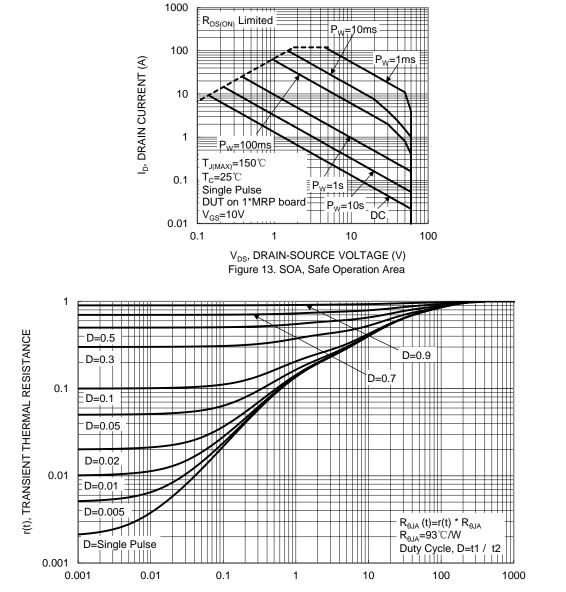


Figure 8. Gate Threshold Variation vs. Junction Temperature







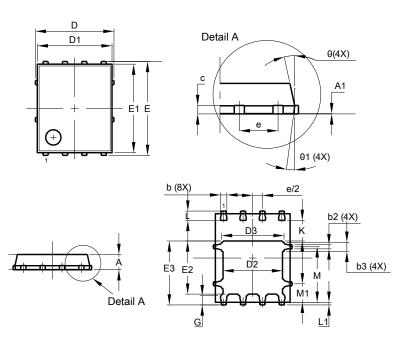


t1, Pulse Duration Time (sec) Figure 14. Transient Thermal Resistance



# Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

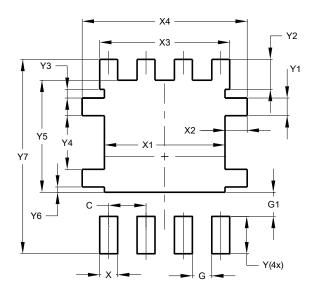


#### PowerDI5060-8

	PowerDI5060-8					
Dim	Min	Max	Тур			
Α	0.90	1.10	1.00			
A1	0.00	0.05	-			
b	0.33	0.51	0.41			
b2	0.200	0.350	0.273			
b3	0.40	0.80	0.60			
c	0.230	0.330	0.277			
D		5.15 BSC	;			
D1	4.70	5.10	4.90			
D2	3.70	4.10	3.90			
D3	3.90	4.30	4.10			
E		6.15 BSC	;			
E1	5.60	6.00	5.80			
E2	3.28	3.68	3.48			
E3	3.99	4.39	4.19			
е		1.27 BSC	;			
G	0.51	0.71	0.61			
K	0.51	-	-			
L	0.51	0.71	0.61			
L1	0.100	0.200	0.175			
Μ	3.235	4.035	3.635			
M1	1.00	1.40	1.21			
Θ	10°	12°	11°			
Θ1	6°	8°	7°			
Al	Dimens	ions in n	nm			

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



#### PowerDI5060-8

Dimensions	Value (in mm)
С	1.270
G	0.660
G1	0.820
Х	0.610
X1	4.100
X2	0.755
X3	4.420
X4	5.610
Y	1.270
Y1	0.600
Y2	1.020
Y3	0.295
Y4	1.825
Y5	3.810
Y6	0.180
Y7	6.610



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