



Product Summary

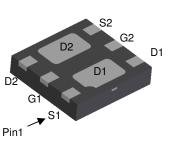
Device	V _{(BR)DSS}	R _{DS(ON)} max	I _{D MAX} T _A = +25℃
		61mΩ @ V _{GS} = -4.5V	-3.8A
P-Channel	-12V	81mΩ @ V _{GS} = -2.5V	-3.3A
		$115m\Omega @ V_{GS} = -1.8V$	-2.8A

Description

This MOSFET is designed to minimize the on-state resistance $(R_{DS(on)})$ and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

- Load Switch
- Power Management Functions
- Portable Power Adaptors



U-DFN2020-6

Bottom View

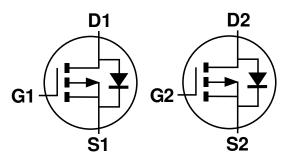
DUAL P-CHANNEL ENHANCEMENT MODE MOSFET

Features

- Low On-Resistance
- Low Input Capacitance
- Low Profile, 0.6mm Max Height
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @
- Terminals Connections: See Diagram Below
- Weight: 0.0065 grams (Approximate)



Internal Schematic

Ordering Information (Note 4)

Part Number	Case	Packaging
DMP1046UFDB -7	U-DFN2020-6	3,000/Tape & Reel
DMP1046UFDB -13	U-DFN2020-6	10,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com.

Marking Information



 $\begin{array}{l} \mathsf{P6} = \mathsf{Product Type Marking Code} \\ \mathsf{YM} = \mathsf{Date Code Marking} \\ \mathsf{Y} = \mathsf{Year} \ (\mathsf{ex: C} = 2015) \\ \mathsf{M} = \mathsf{Month} \ (\mathsf{ex: 9} = \mathsf{September}) \end{array}$

Date Code Key	
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Year	201	5	2016		2017	20	18	2019		2020	2	2021
Code	С		D		E	F	-	G		Н		
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	Ν	D



Maximum Ratings (@T_A = +25 °C, unless otherwise specified.)

Characteristic		Symbol	Value	Units	
Drain-Source Voltage		V _{DSS}	-12	V	
Gate-Source Voltage			V _{GSS}	±8	V
	Steady State	T _A = +25 ℃ T _A = +70 ℃	ID	-3.8 -3.0	А
Continuous Drain Current (Note 5) $V_{GS} = 4.5V$	t < 5s	T _A = +25℃ T _A = +70℃	ID	-5.0 -4.0	А
Maximum Continuous Body Diode Forward Curre	ent (Note 5)		Is	-1	А
Pulsed Drain Current (10µs pulse, duty cycle = 1	%)	I _{DM}	-15	А	
Avalanche Current (L = 0.1mH)		I _{AS}	-12	А	
Avalanche Energy (L = 0.1mH)		Eas	8	mJ	

Thermal Characteristics

Characteristic		Symbol	Value	Units	
Total Power Dissipation (Note 5)	Steady State	Steady State		W	
Total Power Dissipation (Note 5)	t < 5s	PD	2.2	vv	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	D	92		
merinal Resistance, Junction to Ambient (Note 5)	t < 5s	R _{θJA}	55	°C/W	
Thermal Resistance, Junction to Case (Note 5)	$R_{\theta JC}$	20			
Operating and Storage Temperature Range		TJ, TSTG	-55 to 150	°C	

Notes: 5. Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.

Electrical Characteristics (@T_A = +25 °C, unless otherwise specified.)

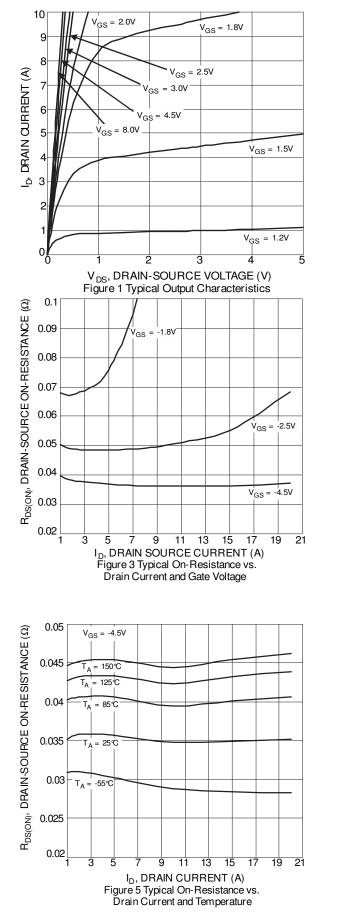
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Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)						
Drain-Source Breakdown Voltage	BV _{DSS}	-12	-	-	V	$V_{GS} = 0V, I_D = -250 \mu A$
Zero Gate Voltage Drain Current T _J = +25 °C	I _{DSS}	-	-	-1.0	μA	$V_{DS} = -12V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	-	-	±100	nA	$V_{GS} = \pm 8V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 6)						
Gate Threshold Voltage	V _{GS(th)}	-0.4	-	-1	V	$V_{DS} = V_{GS}$, $I_D = -250 \mu A$
		-	37	61		$V_{GS} = -4.5V, I_D = -3.6A$
Static Drain-Source On-Resistance	R _{DS (ON)}	-	47	81	mΩ	$V_{GS} = -2.5V, I_D = -3.2A$
	. ,	-	63	115		$V_{GS} = -1.8V, I_D = -1.0A$
Diode Forward Voltage	V _{SD}	-	-0.65	-1.2	V	$V_{GS} = 0V, I_{S} = -4.5A$
DYNAMIC CHARACTERISTICS (Note 7)						
Input Capacitance	C _{iss}	-	915	-	pF	
Output Capacitance	Coss	-	225	-	pF	V _{DS} = -6V, V _{GS} = 0V, f = 1.0MHz
Reverse Transfer Capacitance	Crss	-	183	-	pF	
Gate Resistance	R _g	-	56.9	-	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge (V _{GS} = -4.5V)	0	-	10.7	-	nC	
Total Gate Charge (V _{GS} = -8V)	Qg		17.9		nC	
Gate-Source Charge	Q _{gs}	-	1.7	-	nC	$V_{DS} = -6V, I_D = -4.3A$
Gate-Drain Charge	Q _{gd}	-	3.0	-	nC	
Turn-On Delay Time	t _{D(on)}	-	5.7	-	ns	
Turn-On Rise Time	tr	-	11.5	-	ns	$V_{DD} = -6V, V_{GS} = -4.5V,$
Turn-Off Delay Time	t _{D(off)}	-	27.8	-	ns	$R_L = 1.6\Omega, R_G = 1\Omega$
Turn-Off Fall Time	tf	-	26.4	-	ns]

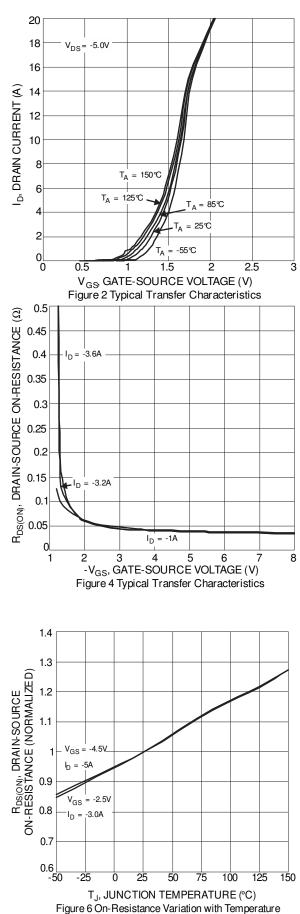
Notes: 6. Short duration pulse test used to minimize self-heating effect.

7. Guaranteed by design. Not subject to product testing.

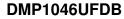


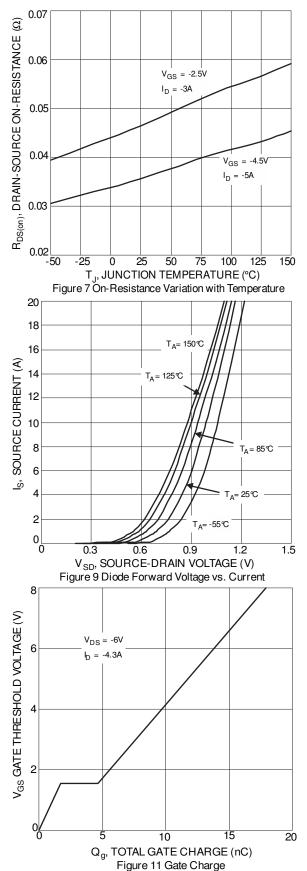
DMP1046UFDB

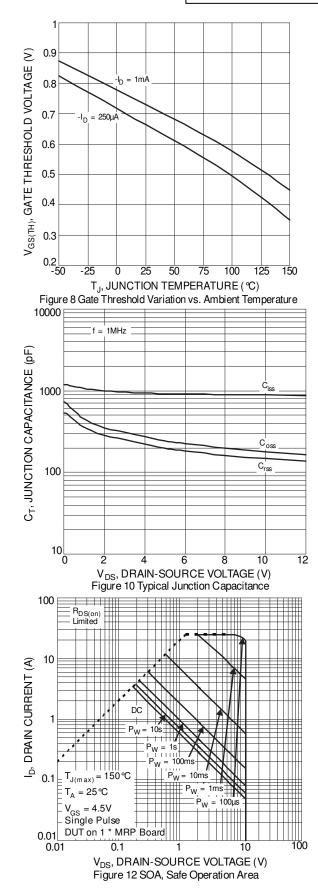




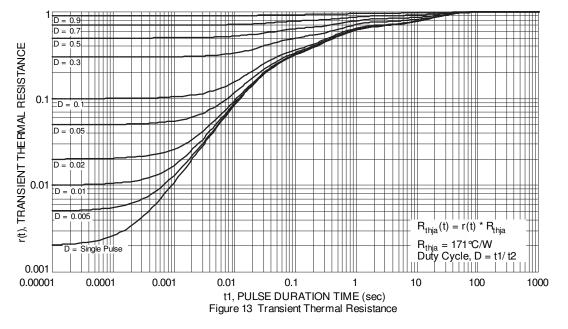






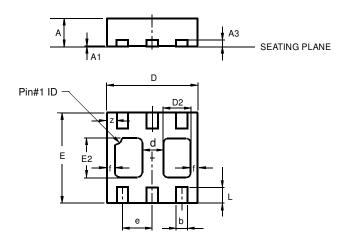






Package Outline Dimensions

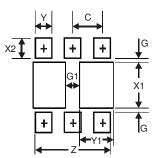
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



U-DFN2020-6								
Туре В								
Dim	Min	Min Max Typ						
Α	0.545	0.605	0.575					
A1	0	0.05	0.02					
A3	_	_	0.13					
b	0.20	0.30	0.25					
D	1.95	2.075	2.00					
d	_	_	0.45					
D2	0.50	0.70	0.60					
е	_	_	0.65					
Е	1.95	2.075	2.00					
E2	0.90	1.10	1.00					
f	_	_	0.15					
L	0.25	0.35	0.30					
z	_	_	0.225					
All								

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	1.67
G	0.20
G1	0.40
X1	1.0
X2	0.45
Y	0.37
Y1	0.70
С	0.65



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