

#### P-CHANNEL ENHANCEMENT MODE MOSFET

## **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(ON) max</sub>	I <sub>D</sub> T <sub>A</sub> = +25°C
-20V	$65m\Omega @V_{GS} = -4.5V$	-4.2A
-20V	100mΩ @ $V_{GS}$ = -2.5 $V$	-3.4A

## **Description**

This MOSFET has been designed to minimize the on-state resistance ( $R_{DS(ON)}$ ) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

## **Applications**

- Backlighting
- Power Management Functions
- DC-DC Converters
- Motor Control

## **Features**

- Low On-Resistance
- Low Input Capacitance
- · Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 standards for High Reliability

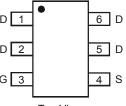
## **Mechanical Data**

- Case: TSOT26
- Case Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish MatteTin annealed over Copper leadframe.
   Solderable per MIL-STD-202, Method 208
- Weight: 0.0013 grams (approximate)

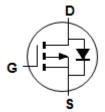




Top View



Top View Pin-Out



Internal Schematic

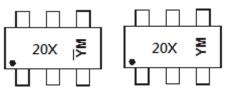
## Ordering Information (Note 4)

Part Number	Case	Packaging		
DMP2033UVT-7	TSOT26	3000/Tape & Reel		
DMP2033UVT -13	TSOT26	10000/Tape & Reel		

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

## **Marking Information**



Chengdu A/T Site Shanghai A/T Site

20X = Product Type Marking Code YM = Date Code Marking for SAT (Shanghai Assembly/ Test site)  $\overline{\gamma}_M$  = Date Code Marking for CAT (Chengdu Assembly/ Test site) Y or  $\overline{\gamma}$  = Year (ex: A = 2013) M = Month (ex: 9 = September)

Date Code Key

Year	201	1	2012		2013	20	14	2015		2016	2	2017
Code	Υ		Z		Α	[	3	С		D		Е
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



## **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characte	eristic		Symbol	Value	Units
Drain-Source Voltage			V <sub>DSS</sub>	-20	V
Gate-Source Voltage		V <sub>GSS</sub>	±8	V	
Continuous Drain Current (Note 6) Steady $T_A = +25^{\circ}C$ State $T_A = +70^{\circ}C$			I <sub>D</sub>	-4.2 -3.4	А
Pulsed Drain Current (Note 6)			I <sub>DM</sub>	-10	Α

## **Thermal Characteristics**

Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 5)		$P_{D}$	1.2	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{ heta JA}$	100	°C/W
Total Power Dissipation (Note 6)		P <sub>D</sub>	1.7	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	74	°C/W
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	°C

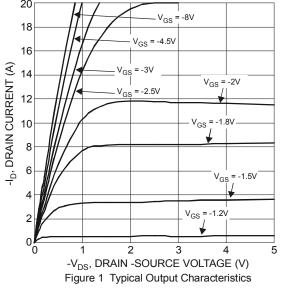
## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

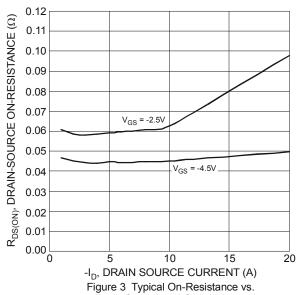
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage		-20	_	_	V	$V_{GS} = 0V$ , $I_D = -250\mu A$
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	I <sub>DSS</sub>	_	_	-1.0	μA	V <sub>DS</sub> = -20V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 8V$ , $V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(th)}$	-0.5	_	-0.9	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
			45	65		V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -4.2A
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	57	100	mΩ	V <sub>GS</sub> = -2.5V, I <sub>D</sub> = -3.4A
			80	200		V <sub>GS</sub> = -1.8V, I <sub>D</sub> = -2A
Forward Transfer Admittance	Y <sub>fs</sub>	_	9	_	S	$V_{DS} = -5V, I_{D} = -4A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C <sub>iss</sub>	_	845		pF	
Output Capacitance	Coss	_	72	_	pF	V <sub>DS</sub> = -15V, V <sub>GS</sub> = 0V f = 1.0MHz
Reverse Transfer Capacitance	C <sub>rss</sub>	_	63	_	pF	1 - 1.0WH2
SWITCHING CHARACTERISTICS (Note 8)						
Total Gate Charge	$Q_g$	_	10.4		nC	V 45V V 0V
Gate-Source Charge	$Q_{gs}$	_	1.5	_	nC	$V_{GS} = -4.5V$ , $V_{DS} = -4V$ , $I_{D} = -3.5A$
Gate-Drain Charge	$Q_{gd}$	_	1.9	_	nC	1D = -3.5A
Turn-On Delay Time	t <sub>D(on)</sub>	_	6.5	_	ns	
Turn-On Rise Time	t <sub>r</sub>	_	13.4	_	ns	V <sub>DS</sub> = -4V, V <sub>GS</sub> = -4.5V,
Turn-Off Delay Time	t <sub>D(off)</sub>	_	51.5	_	ns	$R_G = 6\Omega$ , $I_D = -1A$
Turn-Off Fall Time	t <sub>f</sub>	_	21.8	_	ns	1

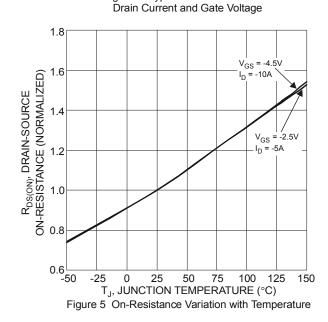
Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

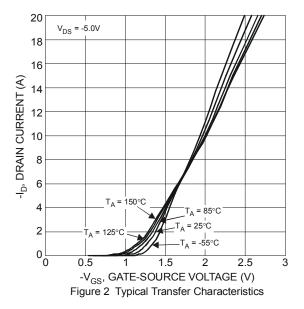
<sup>7.</sup> Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to product testing.

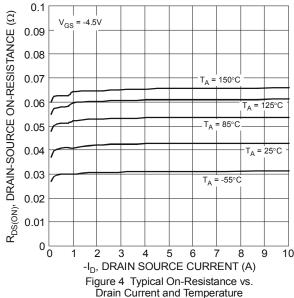


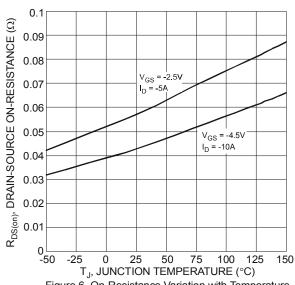














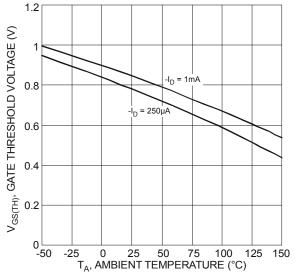
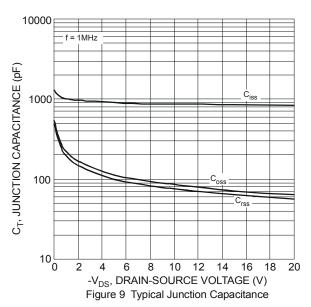
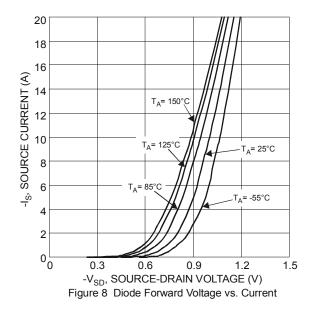
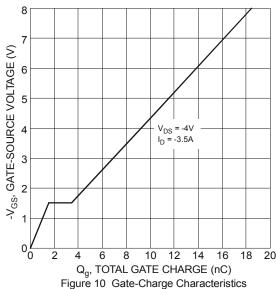


Figure 7 Gate Threshold Variation vs. Ambient Temperature

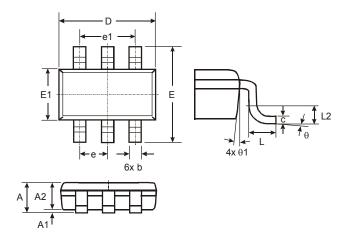






## **Package Outline Dimensions**

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

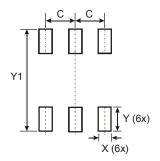


TSOT26							
Dim	Min	Max	Тур				
Α	1	1.00	_				
<b>A</b> 1	0.01	0.10	-				
A2	0.84	0.90	-				
D		_	2.90				
Е	1	-	2.80				
E1		_	1.60				
þ	0.30	0.45	_				
O	0.12	0.20	_				
е	1	-	0.95				
e1	-	_	1.90				
L	0.30	0.50					
L2	ı	_	0.25				
θ	0°	8°	4°				
θ1	4°	12°	_				
All Dimensions in mm							



### **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	0.950
Х	0.700
Υ	1.000
Y1	3.199

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