



DMP2066UFDE

20V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{ds(on)}	Package	I _D T _A = +25°C
-20V	36mΩ @ V _{GS} = -4.5V		-6.2A
	56mΩ @ V _{GS} = -2.5V	U-DFN2020-6 Type E	-5.0A
	75mΩ @ V _{GS} = -1.8V	.,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	-4.2A

Description

This new generation MOSFET has been designed to minimize the onstate resistance (R_{DS(on)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

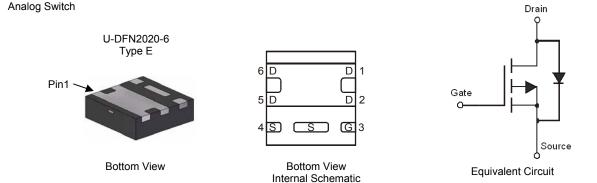
- General Purpose Interfacing Switch
- **Power Management Functions**
- Analog Switch



- 0.6mm profile ideal for low profile applications
- PCB footprint of 4mm²
- Low Gate Threshold Voltage
- Low On-Resistance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: U-DFN2020-6 Type E
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Weight: 0.0065 grams (approximate)



Ordering Information (Note 4)

Part Number	Case	Packaging
DMP2066UFDE-7	U-DFN2020-6 Type E	3,000/Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. Notes:

2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com.

Marking Information

•	PC	ΥM
-		

PC = Product Type Marking Code YM = Date Code Marking Y = Year (ex: Y = 2011) M = Month (ex: 9 = September) Dot Denotes Pin 1

Date Code Key

Date Code Rey												
Year	201 ²	1	2012		2013	20	14	2015		2016	2	2017
Code	Y		Z		А	E	3	С		D		E
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage			V _{DSS}	-20	V
Gate-Source Voltage			V _{GSS}	±12	V
Continuous Drain Current (Nato E) \/ 4 E\/	Steady State	T _A = +25°C T _A = +70°C	ID	-6.2 -4.9	A
Continuous Drain Current (Note 5) V_{GS} = -4.5V	t<5s	T _A = +25°C T _A = +70°C	ID	-7.5 -5.9	А
Continuous Drain Current (Nata E) // 4 0//	Steady State	T _A = +25°C T _A = +70°C	ID	-4.2 -3.4	А
Continuous Drain Current (Note 5) V_{GS} = -1.8V	t<5s	T _A = +25°C T _A = +70°C	۱ _D	-5.2 -4.1	А
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	I _{DM}	-25	А		
Maximum Continuous Body Diode Forward Current	ls	2.5	А		

Thermal Characteristics

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 6)		PD	0.66	W
Thermal Decistence, Junction to Ambient (Note 6)	Steady state	D	189	°C/W
Thermal Resistance, Junction to Ambient (Note 6)	t<5s	$R_{ ext{ heta}}JA$	123	°C/W
Total Power Dissipation (Note 5)		PD	2.03	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady state	D	61	°C/W
Thermal Resistance, sunction to Amblent (Note 5)	t<5s	$R_{ ext{ heta}JA}$	40	°C/W
Thermal Resistance, Junction to Case (Note 5)		$R_{ ext{ heta}Jc}$	9.3	°C/W
Operating and Storage Temperature Range		T _{J.} T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)	Oymbol	WIIII	I IYP	Max	Onit	Test condition
Drain-Source Breakdown Voltage	BV _{DSS}	-20	_	_	V	V _{GS} = 0V, I _D = -250µA
Zero Gate Voltage Drain Current	I _{DSS}		_	-1	μA	$V_{DS} = -20V, V_{GS} = 0V$
Gate-Source Leakage	IGSS		_	±100	nA	V _{GS} = ±12.0V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(th)}	-0.4	_	-1.1	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$
		_	25	36		V _{GS} = -4.5V, I _D = -4.6A
Static Drain-Source On-Resistance	R _{DS (ON)}	_	33	56	mΩ	V _{GS} = -2.5V, I _D = -3.8A
		_	50	75		V _{GS} = -1.8V, I _D = -2.0A
Forward Transfer Admittance	Y _{fs}	_	9	_	S	V _{DS} = -10V, I _D = -4.5A
Diode Forward Voltage	V _{SD}	_	-0.7	-1.2	V	V _{GS} = 0V, I _S = -2.1A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	_	1537	_	pF	
Output Capacitance	Coss	_	146	_	pF	└V _{DS} = -10V, V _{GS} = 0V - f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	127	_	pF	
Gate Resistance	R _g	_	10.4		Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1.0MHz
Total Gate Charge	Qg	_	14.4	_		(- 40)(1)(- 45)(1)
Gate-Source Charge	Q _{gs}	_	2.6	_	nC	V _{DS} = -10V, V _{GS} = -4.5V In = -4.5A
Gate-Drain Charge	Q _{gd}	_	2.7	_		$I_D = -4.5A$
Turn-On Delay Time	t _{D(on)}		13.7			
Turn-On Rise Time	tr	_	14.0	_		$V_{DD} = -10V, V_{GS} = -4.5V, R_G = 6\Omega,$
Turn-Off Delay Time	t _{D(off)}	_	79.1	_	ns	R _L = 10Ω, I _D = -1A
Turn-Off Fall Time	t _f	_	35.5	_		

Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.
Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.

7. Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to production testing.

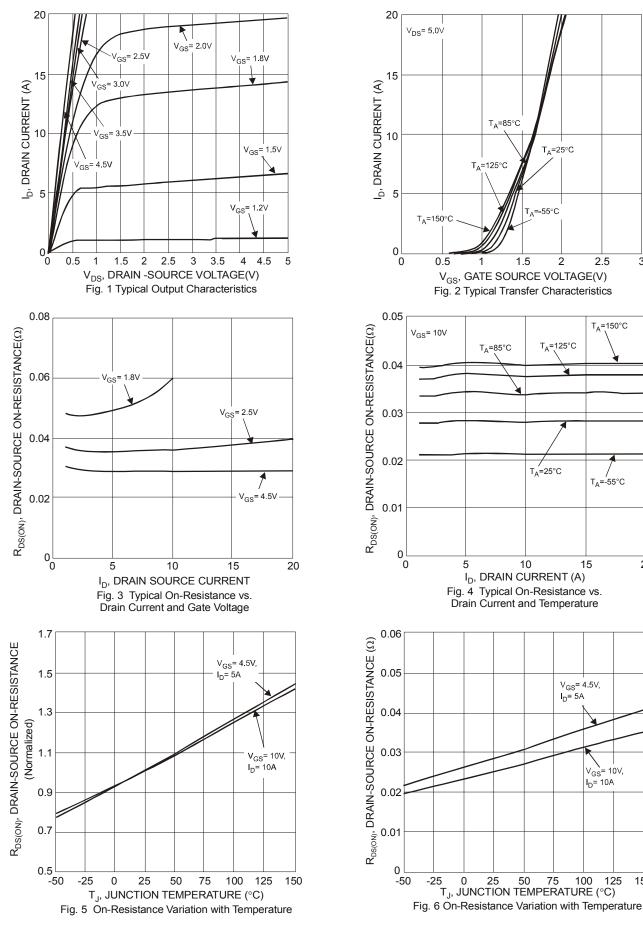
Notes:



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DMP2066UFDE Document number: DS35496 Rev. 5 - 2

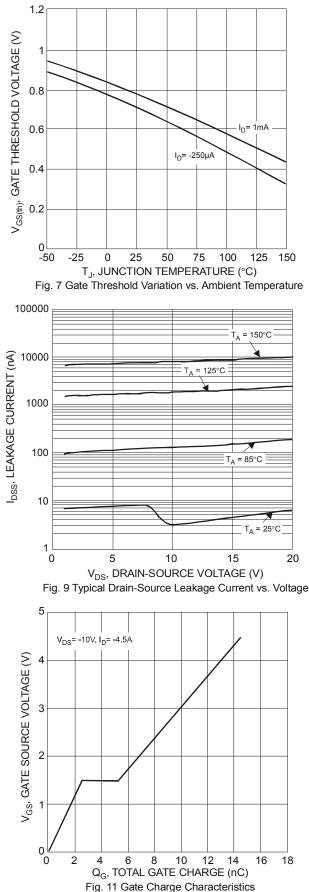
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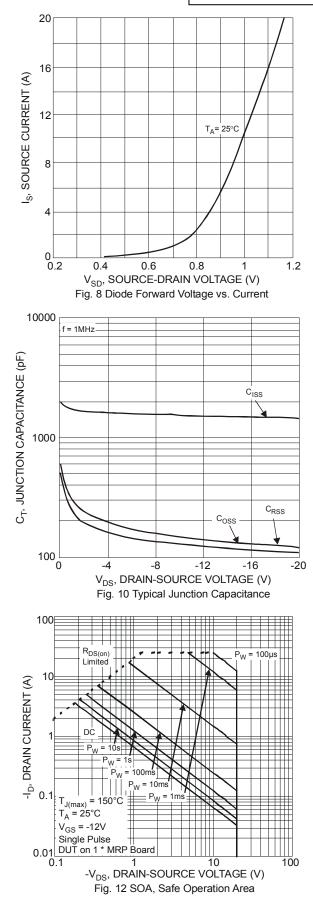
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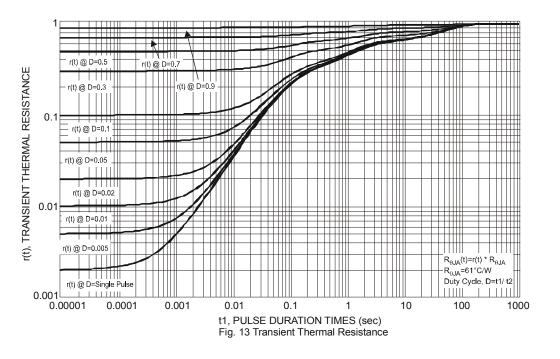




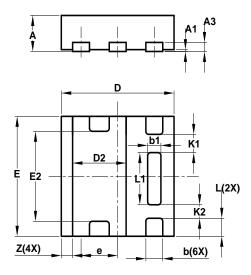








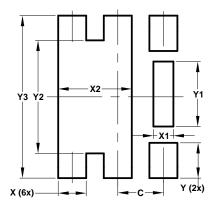
Package Outline Dimensions



U-DFN2020-6 Type E									
Dim	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,								
Α	0.57	0.63	0.60						
A1	0	0.05	0.03						
A3	-	-	0.15						
b	0.25	0.35	0.30						
b1	0.185	0.285	0.235						
D	1.95	2.05	2.00						
D2	0.85	1.05	0.95						
ш	1.95	2.05	2.00						
E2	1.40	1.60	1.50						
e	0.65								
1	0.25	0.35	0.30						
L1	0.82	0.92	0.87						
K1	_	_	0.305						
K2	_	_	0.225						
Z	_	_	0.20						
All	Dimens	ions in	mm						



Suggested Pad Layout



Dimensions	Value			
Dimensions	(in mm)			
С	0.650			
Х	0.400			
X1	0.285			
X2	1.050			
Y	0.500			
Y1	0.920			
Y2	1.600			
Y3	2.300			

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