



DMP21D5UFD

#### P-CHANNEL ENHANCEMENT MODE MOSFET

### **Product Summary**

V <sub>(BR)DSS</sub>	$R_{DS(ON) max}$	Package	Ι <sub>D</sub> T <sub>A</sub> = +25°C		
	1.0Ω @ V <sub>GS</sub> = -4.5V		-600mA		
001/	1.5Ω @ V <sub>GS</sub> = -2.5V		-500mA		
-20V	2.0Ω @ V <sub>GS</sub> = -1.8V	X1-DFN1212-3	-400mA		
	3.0Ω @ V <sub>GS</sub> = -1.5V		-250mA		

# Description

This new generation MOSFET has been designed to minimize the onstate resistance ( $R_{DS(on)}$ ) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

### **Applications**

- DC-DC Converters
- Power Management Functions

#### Features

- Low On-Resistance
- Very Low Gate Threshold Voltage V<sub>GS(TH)</sub>, 1.0V max
- Low Input Capacitance
- Fast Switching Speed
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

### Mechanical Data

- Case: X1-DFN1212-3
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208 @€
- Terminal Connections: See Diagram
- Weight: 0.005 grams (approximate)

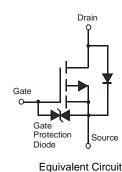


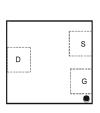


Top View

G pin

Bottom View





Pin-out Top view

### Ordering Information (Note 4)

Part Number	Case	Packaging
DMP21D5UFD-7	X1-DFN1212-3	3000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and</li>

<1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com.

# **Marking Information**

	KP2	
•	YM	

KP2 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: Y = 2011) M = Month (ex: 9 = September)

Balo boad lity												
Year	201	1	2012		2013	20	14	2015		2016	2	2017
Code	Y		Z		А	E	3	С		D		E
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage		V <sub>DSS</sub>	-20	V	
Gate-Source Voltage	V <sub>GSS</sub>	±8	V		
Continuous Drain Current (Note 6) $V_{GS}$ = -4.5V	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	Ι <sub>D</sub>	-600 -500	mA
Continuous Drain Current (Note 6) V <sub>GS</sub> = -1.8V	Ι <sub>D</sub>	-400 -300	mA		
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	I <sub>DM</sub>	-2	А		
Maximum Body Diode continuous Current	Is	-800	mA		

# **Thermal Characteristics**

Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 5)	_	PD	0.4	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady state	$R_{ ext{ heta}JA}$	280	°C/W
Total Power Dissipation (Note 6)		PD	0.8	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady state	$R_{ ext{ heta}JA}$	140	°C/W
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C

# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

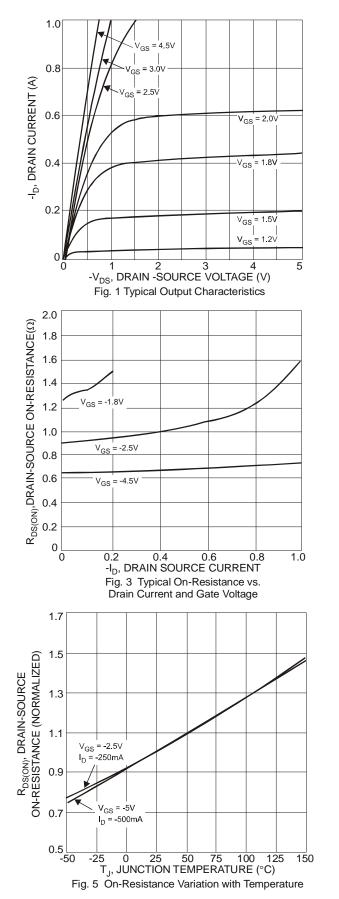
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)				•		÷	
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-20			V	$V_{GS} = 0V, I_D = -1mA$	
Zero Gate Voltage Drain Current $T_J = +25^{\circ}C$	I <sub>DSS</sub>	—	_	-80 -100	nA	$V_{DS} = -4.5V, V_{GS} = 0V$ $V_{DS} = -20V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	_		±10.0	μA	$V_{GS} = \pm 8V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V <sub>GS(th)</sub>	-0.5	_	-1.0	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$	
		_	0.7	1.0		$V_{GS} = -4.5V, I_{D} = -100mA$	
		_	0.9	1.5		$V_{GS} = -2.5V, I_{D} = -80mA$	
Static Drain-Source On-Resistance	R <sub>DS (ON)</sub>	_	1.2	2.0	Ω	$V_{GS} = -1.8V, I_{D} = -40mA$	
	· · ·	_	1.5	3.0		$V_{GS} = -1.5V, I_{D} = -30mA$	
		_	5			$V_{GS} = -1.2V, I_D = -1mA$	
Forward Transfer Admittance	Y <sub>fs</sub>	_	0.7		S	$V_{DS} = -3V, I_{D} = -100mA$	
Diode Forward Voltage	V <sub>SD</sub>	_	-0.75	-1.2	V	$V_{GS} = 0V, I_{S} = -330mA,$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C <sub>iss</sub>	_	46.1	_			
Output Capacitance	C <sub>oss</sub>	_	7.2	_	pF	$V_{DS} = -10V, V_{GS} = 0V,$ f = 1.0MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>	_	4.9	_			
Total Gate Charge V <sub>GS</sub> = -4.5V	Qg	_	0.5	_			
Total Gate Charge V <sub>GS</sub> = -8V	Qg	_	0.8	_		101/1 050-	
Gate-Source Charge	Q <sub>gs</sub>	_	0.1	_	nC	$V_{DS} = -10V, I_{D} = -250mA$	
Gate-Drain Charge	Q <sub>gd</sub>	_	0.1	_			
Turn-On Delay Time	t <sub>D(on)</sub>	_	8.5	—			
Turn-On Rise Time	tr		4.3			$V_{DD} = -3V, V_{GS} = -2.5V,$	
Turn-Off Delay Time	t <sub>D(off)</sub>	_	20.2	—	ns	$R_{\rm L} = 300\Omega, R_{\rm G} = 25\Omega,$	
Turn-Off Fall Time	tf	_	19.2	_	1	I <sub>D</sub> = -100mA	

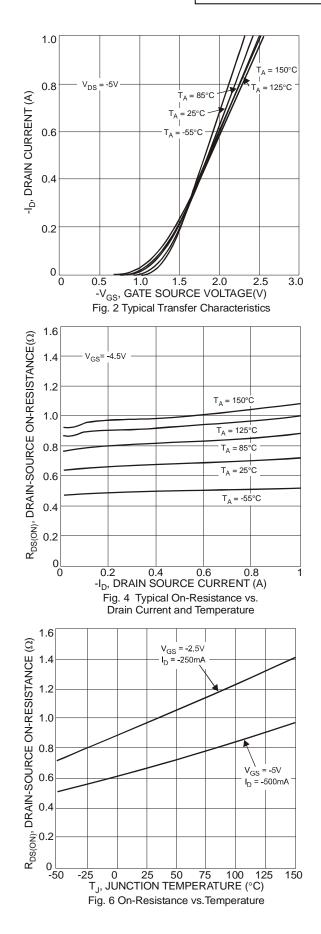
5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout. Notes:

Botter mounted on FR-4 substrate PC board, 202 copper, with 1inch square copper plate.
Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing.



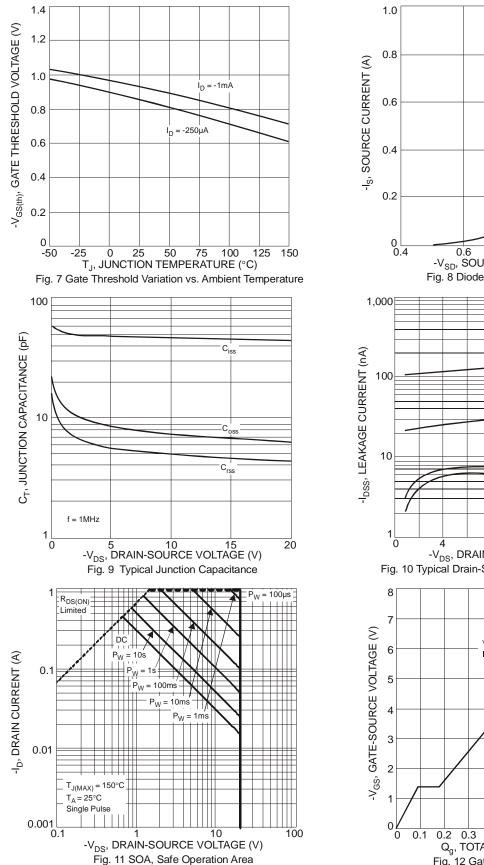
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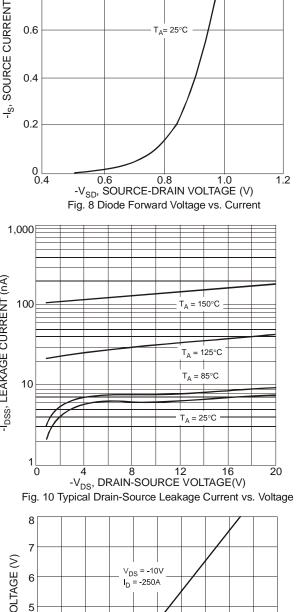






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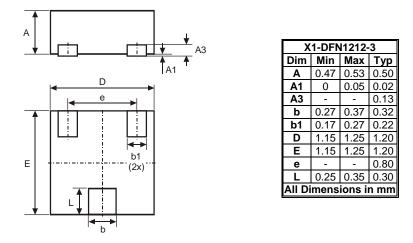






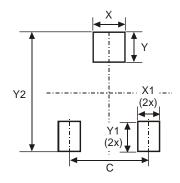
# Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



# Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	0.80
Х	0.42
X1	0.32
Y	0.50
Y1	0.50
Y2	1.50



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