



#### P-CHANNEL ENHANCEMENT MODE MOSFET

### **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub> MAX	Package	I <sub>D</sub> T <sub>A</sub> = +25°C
-30V	70mΩ @V <sub>GS</sub> = -10V	SO-8	-3.9A
-307	95mΩ @V <sub>GS</sub> = -4.5V	30-6	-3.3A

### Description

This MOSFET has been designed to minimize the on-state resistance  $(R_{DS(ON)})$  and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

#### **Applications**

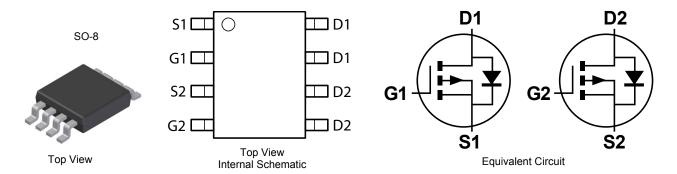
- Backlighting
- Power Management Functions
- DC-DC Converters

#### **Features**

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 standards for High Reliability

#### **Mechanical Data**

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound
- UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- · Terminal Connections Indicator: See diagram
- Terminals: Finish Matte Tin annealed over Copper lead frame Solderable per MIL-STD-202, Method 208 <a>@3</a>
- Weight: 0.074 grams (approximate)



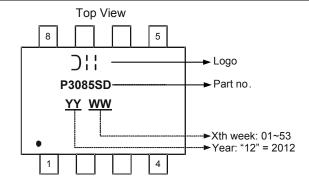
## Ordering Information (Note 4)

Part Number	Case	Packaging
DMP3085LSD-13	SO-8	2500/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant
- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com.

### **Marking Information**





## **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			$V_{DSS}$	-30	V
Gate-Source Voltage			$V_{GSS}$	±20	V
Continuous Drain Current (Note 6) \/ = 10\/	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	Ι <sub>D</sub>	-3.9 -3.1	А
Continuous Drain Current (Note 6) V <sub>GS</sub> = -10V	t<10s	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	I <sub>D</sub>	-4.9 -3.9	Α
Maximum Continuous Body Diode Forward Current (Note 6)			I <sub>S</sub>	-2.5	Α
Pulsed Drain Current (10µs pulse, duty cycle = 1%)			I <sub>DM</sub>	20	Α

## **Thermal Characteristics**

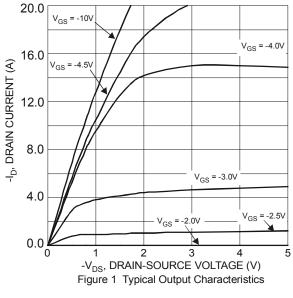
Characteristic	Symbol	Value	Units	
Total Dawar Dissination (Note 5)	T <sub>A</sub> = +25°C	ם	1.1	W
Total Power Dissipation (Note 5)	T <sub>A</sub> = +70°C	$P_{D}$	0.7	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	D	107	°C/W
Thermal Resistance, Junction to Ambient (Note 3)	t<10s	$R_{\theta JA}$	70	
Total Power Dissipation (Note 6)	T <sub>A</sub> = +25°C	$P_{\mathrm{D}}$	1.7	W
Total Fower Dissipation (Note o)	$T_A = +70^{\circ}C$	FD	1.1	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	D	75	°C/W
Thermal Resistance, Junction to Ambient (Note o)	t<10s	$R_{\theta JA}$	50	
Thermal Resistance, Junction to Case	$R_{ heta JC}$	14.5		
Operating and Storage Temperature Range		T <sub>J.</sub> T <sub>STG</sub>	-55 to +150	°C

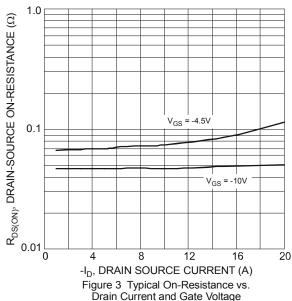
## **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-30	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	-1	μA	V <sub>DS</sub> =-30V, V <sub>GS</sub> = 0V	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	$V_{GS(th)}$	-1	_	-3	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
Static Drain-Source On-Resistance		-	50	70	mΩ	$V_{GS} = -10V, I_D = -5.3A$	
Static Diain-Source Off-Resistance	R <sub>DS</sub> (ON)		75	95	11122	$V_{GS} = -4.5V$ , $I_D = -4.2A$	
Forward Transfer Admittance	Y <sub>fs</sub>	-	5.8	_	S	$V_{DS} = -5V$ , $I_{D} = -5.3A$	
Diode Forward Voltage	$V_{SD}$	_	-0.7	-1.2	V	$V_{GS} = 0V, I_{S} = -1A$	
DYNAMIC CHARACTERISTICS (Note 9)						_	
Input Capacitance	C <sub>iss</sub>	-	563	_		V <sub>DS</sub> = -25V, V <sub>GS</sub> = 0V, f = 1.0MHz	
Output Capacitance	Coss	_	48	_	pF		
Reverse Transfer Capacitance	C <sub>rss</sub>	-	41	_			
Gate Resistance	R <sub>G</sub>	_	10.3	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V <sub>GS</sub> = -4.5V)	Qg	-	5.2	_			
Total Gate Charge (V <sub>GS</sub> = -10V)	$Q_{g}$	_	11	_	nC	V <sub>DS</sub> = -15V, I <sub>D</sub> = -3.8A	
Gate-Source Charge	Q <sub>gs</sub>	_	1.7	_	IIC		
Gate-Drain Charge	$Q_{gd}$	_	1.9	_			
Turn-On Delay Time	t <sub>D(on)</sub>	-	4.8			V <sub>DS</sub> = -15V, V <sub>GS</sub> = -10V, I <sub>D</sub> = -1A, R <sub>G</sub> = 6.0Ω	
Turn-On Rise Time	t <sub>r</sub>	_	5	_	nS		
Turn-Off Delay Time	t <sub>D(off)</sub>	_	31	_	113		
Turn-Off Fall Time	t <sub>f</sub>	_	14.6	_			

- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
   Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
- I<sub>AR</sub> and E<sub>AR</sub> rating are based on low frequency and duty cycles to keep T<sub>J</sub> = 25°C.
   Short duration pulse test used to minimize self-heating effect.
- 9. Guaranteed by design. Not subject to product testing.







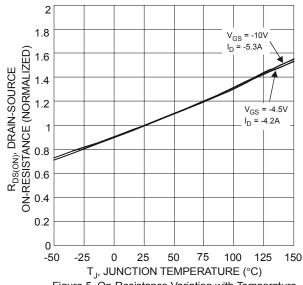
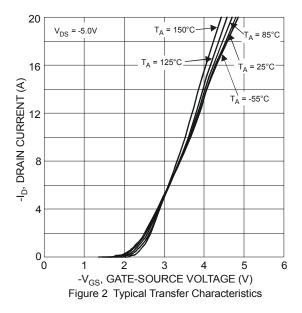
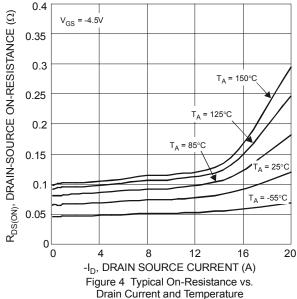
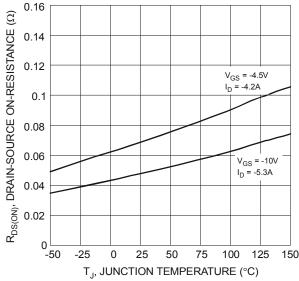


Figure 5 On-Resistance Variation with Temperature









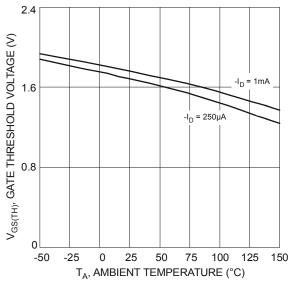
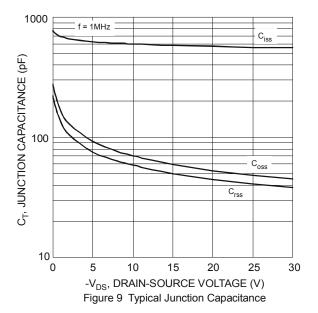
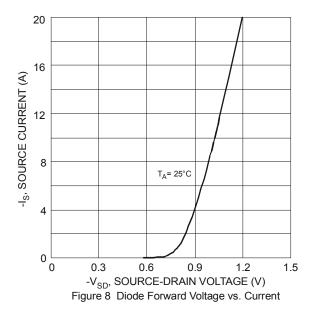
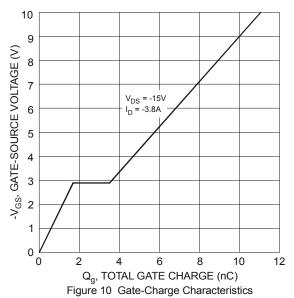


Figure 7 Gate Threshold Variation vs. Ambient Temperature



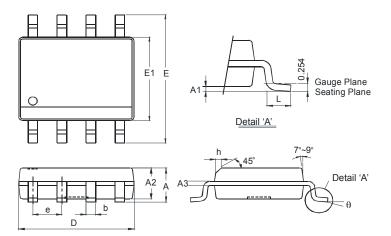






## **Package Outline Dimensions**

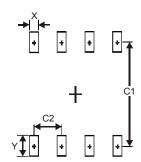
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SO-8					
Dim	Min	Max			
Α	-	1.75			
A1	0.10	0.20			
A2	1.30	1.50			
A3	0.15	0.25			
b	0.3	0.5			
D	4.85	4.95			
Е	5.90	6.10			
E1	3.85	3.95			
е	1.27 Typ				
h	-	0.35			
L	0.62	0.82			
θ	0°	8°			
All Dimensions in mm					

## **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)			
Х	0.60			
Υ	1.55			
C1	5.4			
C2	1.27			



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