



Product Summary

V _{(BR)DSS}	R _{DS(ON)} max	I _D max T _A = +25°C
60V	18mΩ @ V _{GS} = 10V	9.2 A
000	$28m\Omega @ V_{GS} = 4.5V$	7.5 A

Description and Applications

This MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$ and maintain superior switching performance, making it ideal for high efficiency power management applications.

- Load Switch
- Adaptor Switch
- Notebook PC

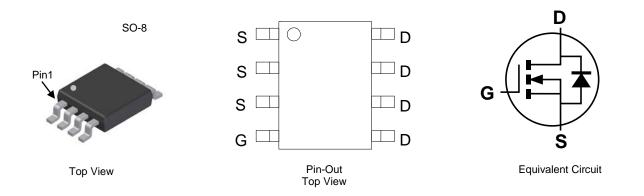
60V N-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See diagram
- Terminals: Finish Matte Tin annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 ©3
- Weight: 0.076 grams (approximate)



Ordering Information (Note 4)

Part Number	Case	Packaging
DMT6016LSS-13	SO-8	2500/Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

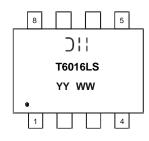
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

Notes:



 $\begin{array}{l} \label{eq:constraint} \exists Manufacturer's Marking \\ T6016LS = Product Type Marking Code \\ YYWW = Date Code Marking \\ YY or \overline{YY} = Year (ex: 14 = 2014) \\ WW = Week (01 - 53) \end{array}$



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	60	V
Gate-Source Voltage			V _{GSS}	±20	V
	Steady State	T _A = +25°C T _A = +70°C	Ι _D	9.2 7.4	А
Continuous Drain Current (Note 6) $V_{GS} = 10V$	t<10s	T _A = +25°C T _A = +70°C	Ι _D	11.9 9.5	A
Continuous Drain Current (Note 6) V_{GS} = 4.5V	Steady State	T _A = +25°C T _A = +70°C	ID	7.5 6.0	А
	t<10s	T _A = +25°C T _A = +70°C	ID	9.7 7.7	А
Pulsed Drain Current (10µs pulse, duty cycle = 1%)			I _{DM}	60	А
Maximum Continuous Body Diode Forward Current (Note 6)			ls	2	А
Avalanche Current (Note 7) L = 0.1mH			IAS	15.3	А
Avalanche Energy (Note 7) L = 0.1mH			E _{AS}	11.7	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)		PD	1.5	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State		85	°C/W
merma Resistance, Junction to Ambient (Note 5)	t<10s	$R_{ extsf{ heta}JA}$	45	°C/W
Total Power Dissipation (Note 6)		PD	2.1	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	D	74	°C/W
mermai Resistance, Junction to Ambient (Note 6)	t<10s	$R_{ extsf{ heta}JA}$	37	°C/W
Thermal Resistance, Junction to Case		$R_{\theta JC}$	13	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to 150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	60		—	V	$V_{GS} = 0V, I_D = 250 \mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	—	1	μA	$V_{DS} = 48V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(th)}	1	_	2.5	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	
Static Drain-Source On-Resistance	Proven	—	—	18	mΩ	$V_{GS} = 10V, I_D = 10A$	
	R _{DS (ON)}	_	—	28	11152	VGS = 4.5V, ID = 6A	
Diode Forward Voltage (Note 7)	V _{SD}	_	0.7	1.2	V	$V_{GS} = 0V, I_{S} = 1A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C _{iss}	_	864	—	pF	$V_{DS} = 30V, V_{GS} = 0V,$ f = 1MHz	
Output Capacitance	Coss	_	282	—			
Reverse Transfer Capacitance	C _{rss}	_	27	—			
Gate resistance	Rg	_	1.3	—	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge ($V_{GS} = 4.5V$)	Qg	_	8.4	—		V _{DS} = 30V, I _D = 10A	
Total Gate Charge (V _{GS} = 10V)	Qg	_	17	—	nC		
Gate-Source Charge	Q _{gs}	_	3.1	—	nc		
Gate-Drain Charge	Q _{gd}	_	4.3	—			
Turn-On Delay Time	t _{D(on)}	_	3.4	—		$V_{GS} = 10V, V_{DS} = 30V,$ $R_{G} = 6\Omega, I_{D} = 10A$	
Turn-On Rise Time	tr	_	5.2	—	ns		
Turn-Off Delay Time	t _{D(off)}	_	13	—			
Turn-Off Fall Time	t _f	_	7	—	1		
Reverse Recovery Time	T _{rr}	_	22	—	ns		
Reverse Recovery Charge	Q _{rr}	_	11	—	nC	- I _F = 10A, di/dt = 100A/μs	

Notes:

Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.

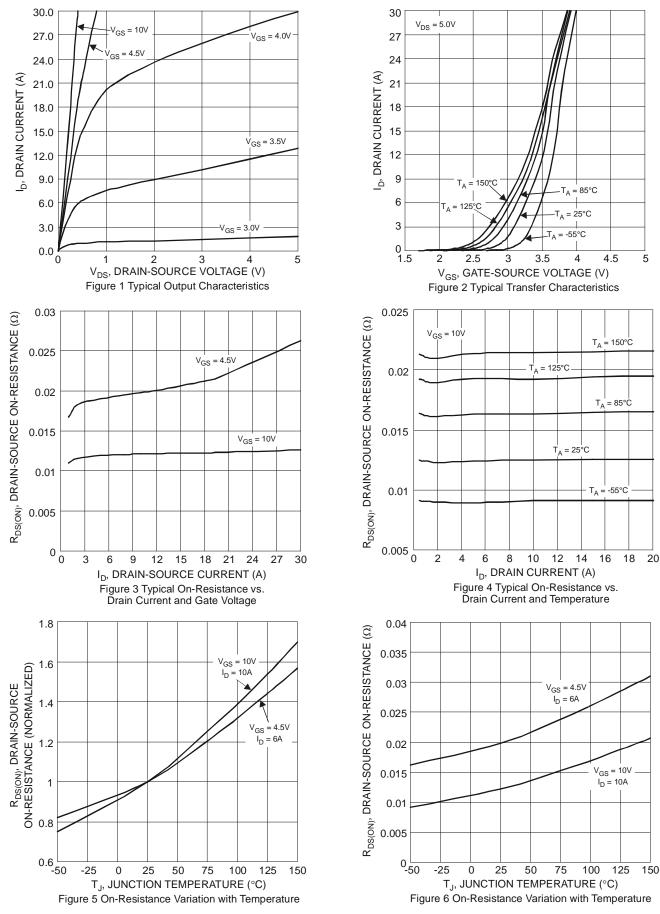
7. I_{AS} and E_{AS} rating are based on low frequency and duty cycles to keep $T_J = +25^{\circ}C$.

8. Short duration pulse test used to minimize self-heating effect.

9. Guaranteed by design. Not subject to product testing.

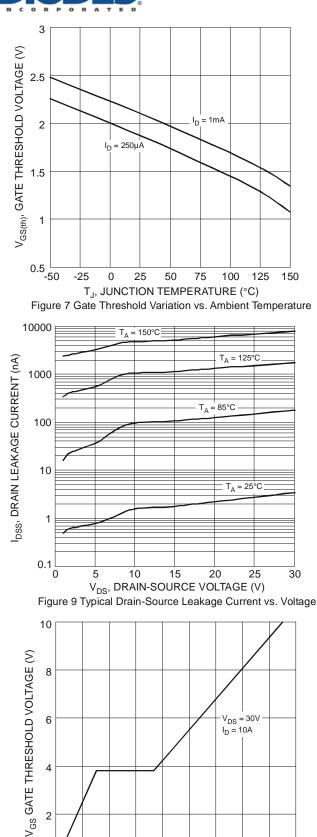


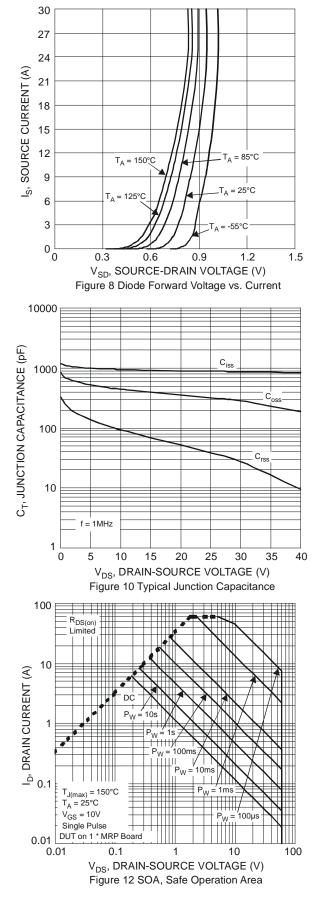
DMT6016LSS



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6

4

8 10 12

Q_g, TOTAL GATE CHARGE (nC)

Figure 11 Gate Charge

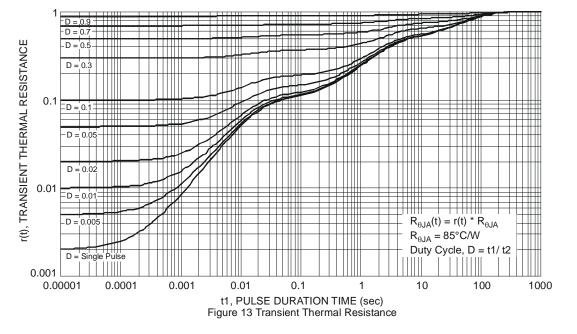
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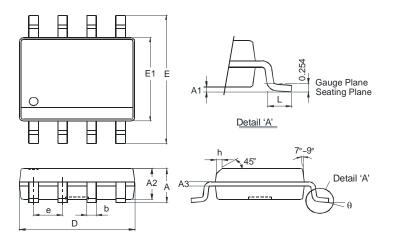
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Package Outline Dimensions

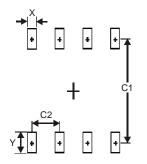
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



SO-8					
Dim	Min	Max			
Α	-	1.75			
A1	0.10	0.20			
A2	1.30	1.50			
A3	0.15	0.25			
b	0.3	0.5			
D	4.85	4.95			
Е	5.90	6.10			
E1	3.85	3.95			
е	1.27 Typ				
h	-	0.35			
L	0.62	0.82			
θ	0° 8°				
All Dimensions in mm					

Suggested Pad Layout

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



Dimensions	Value (in mm)
Х	0.60
Y	1.55
C1	5.4
C2	1.27



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