



DMT8012LPS

#### 80V N-CHANNEL ENHANCEMENT MODE MOSFET

### **Product Summary**

BV <sub>DSS</sub>	R <sub>ds(on)</sub>	Ι <sub>D</sub> T <sub>C</sub> = +25°C
80V	17mΩ @ V <sub>GS</sub> = 10V	65A
000	21mΩ @ V <sub>GS</sub> = 4.5V	59A

#### **Description and Applications**

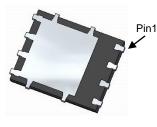
This MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- Synchronous Rectifier
- Backlighting
- **Power Management Functions**
- **DC-DC Converters**

#### POWERDI®5060-8



Top View



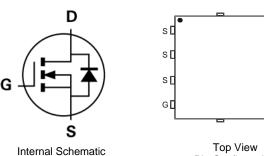
Bottom View

#### Features

- High Conversion Efficiency
- Low RDS(ON) Minimizes On State Losses
- Low Input Capacitance
- Fast Switching Speed
- <1.1mm Package Profile Ideal for Thin Applications
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

#### **Mechanical Data**

- Case: POWERDI®5060-8 •
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe; Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.097 grams (Approximate)



Pin Configuration

ΠD

D D

Πр

#### Ordering Information (Note 4)

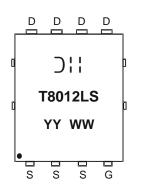
	Part Number	Case	Packaging		
DMT8012LPS-13		POWERDI®5060-8	2,500 / Tape & Reel		
Notes:	Notes: 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.				

2. See http://www.diodes.com/quality/lead free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green"

and Lead-free. 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and

<1000ppm antimony compounds. 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

#### **Marking Information**



) | | =Manufacturer's Marking T8012LS = Product Type Marking Code YYWW = Date Code Marking YY = Last Digit of Year (ex: 14 = 2014) WW = Week Code (01 to 53)



## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Drain-Source Voltage		V <sub>DSS</sub>	80	V
Gate-Source Voltage		V <sub>GSS</sub>	±20	V
Continuous Drain Current, V <sub>GS</sub> = 10V (Note 5)	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	9 7.2	A
Continuous Drain Current, $V_{GS} = 10V$ (Note 6) $T_C = +25^{\circ}C$ $T_C = +70^{\circ}C$		۱ <sub>D</sub>	65 51	А
Maximum Continuous Body Diode Forward Current (Note 6	ls	80	А	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	80	А	
Avalanche Current, L=0.1mH		I <sub>AS</sub>	11.6	А
Avalanche Energy, L=0.1mH		E <sub>AS</sub>	10.2	mJ

#### **Thermal Characteristics**

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)	T <sub>A</sub> = +25°C	PD	2.1	W
Thermal Resistance, Junction to Ambient (Note 5)		R <sub>θJA</sub>	56	°C/W
Total Power Dissipation (Note 6)	T <sub>C</sub> = +25°C	PD	113	W
Thermal Resistance, Junction to Case (Note 6)		R <sub>0JC</sub>	1.1	°C/W
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C

## **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)	Cymbol		.,,,,	max	0		
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	80	-	-	V	$V_{GS} = 0V, I_D = 1mA$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	-	-	1	μA	$V_{DS} = 64V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	-	-	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)						-	
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1	1.3	3	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Statia Praia Source On Registeres		-	14	17	mΩ	$V_{GS} = 10V, I_D = 12A$	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	-	16.5	21		$V_{GS} = 4.5V, I_D = 6A$	
Diode Forward Voltage	V <sub>SD</sub>	-	0.9	1.2	V	$V_{GS} = 0V, I_{S} = 20A$	
DYNAMIC CHARACTERISTICS (Note 8)						-	
Input Capacitance	C <sub>iss</sub>	-	1,949	-		$V_{DS} = 40V, V_{GS} = 0V,$ f = 1MHz	
Output Capacitance	C <sub>oss</sub>	-	177	-	pF		
Reverse Transfer Capacitance	C <sub>rss</sub>	-	10	-			
Gate Resistance	Rg	-	0.7	-	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Qg	-	15	-			
Total Gate Charge (V <sub>GS</sub> = 10V)	Qg	-	34	-	nC	$V_{DS} = 40V, I_D = 12A$	
Gate-Source Charge	Q <sub>gs</sub>	-	6	-	nc		
Gate-Drain Charge	Q <sub>gd</sub>	-	4.5	-			
Turn-On Delay Time	t <sub>D(ON)</sub>	-	4.9	-		$V_{DD} = 40V, V_{GS} = 10V,$ $I_D = 12A, R_G = 1.6\Omega$	
Turn-On Rise Time	t <sub>R</sub>	-	3.8	-	ns		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	-	16.5	-			
Turn-Off Fall Time	t <sub>F</sub>	-	3.5	-			
Body Diode Reverse Recovery Time	t <sub>RR</sub>	-	30.2	-	ns		
Body Diode Reverse Recovery Charge	Qrr	-	34.6	-	nC	I <sub>F</sub> = 12A, di/dt = 100A/μs	

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.

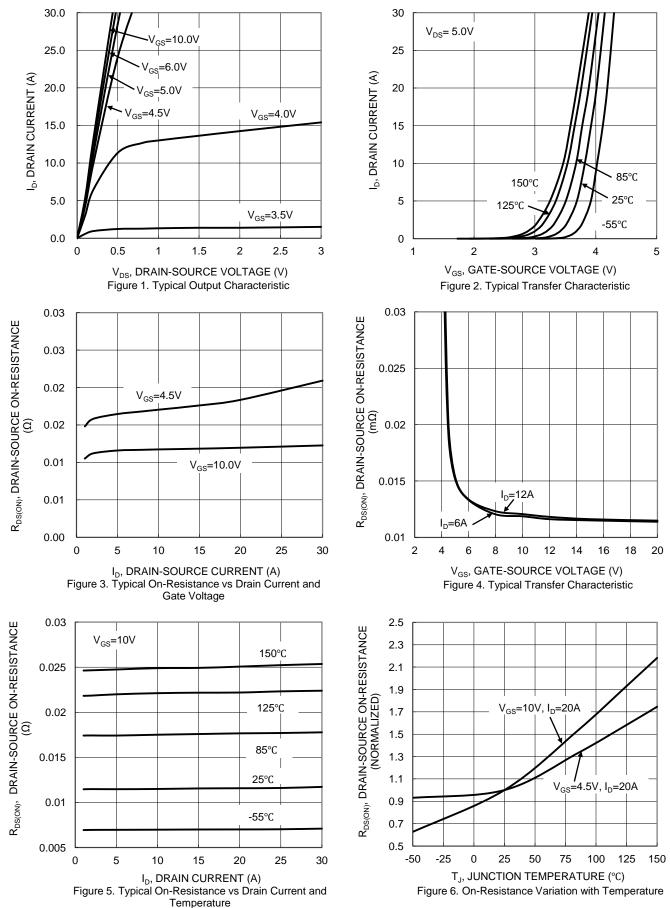
6. Thermal resistance from junction to soldering point (on the exposed drain pad).

7. Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.

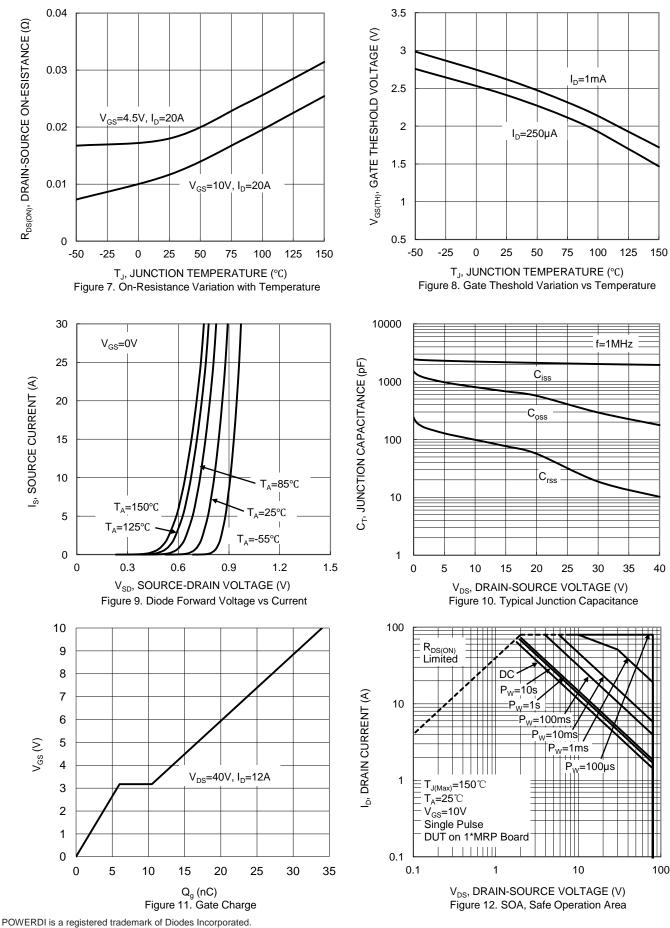


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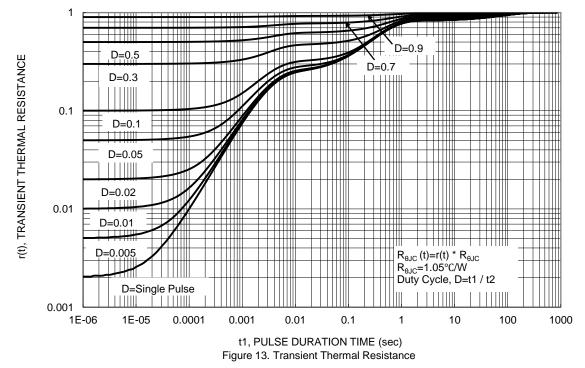




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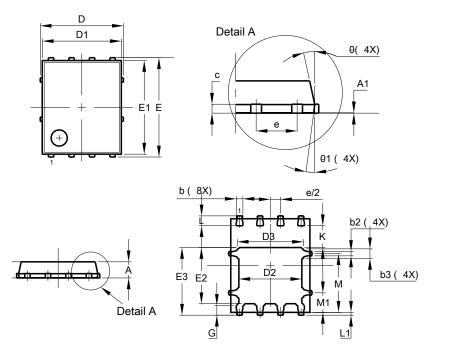






## Package Outline

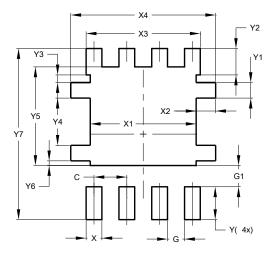
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



	POWERDI <sup>®</sup> 5060-8					
Dim	Min	Max	Тур			
Α	0.90	1.10	1.00			
A1	0.00	0.05	-			
b	0.33	0.51	0.41			
b2	0.200	0.350	0.273			
b3	0.40	0.80	0.60			
С	0.230	0.330	0.277			
D		5.15 BSC				
D1	4.70	5.10	4.90			
D2	3.70	4.10	3.90			
D3	3.90	4.30	4.10			
E	(	6.15 BSC	;			
E1	5.60	6.00	5.80			
E2	3.28	3.68	3.48			
E3	3.99	4.39	4.19			
е		1.27 BSC				
G	0.51	0.71	0.61			
K	0.51	-	-			
L	0.51	0.71	0.61			
L1	0.100	0.200	0.175			
М	3.235	4.035	3.635			
M1	1.00	1.40	1.21			
θ	10°	12°	11°			
θ1	6°	8°	7°			
Al	All Dimensions in mm					

## **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
C	1.270
G	0.660
G1	0.820
Х	0.610
X1	4.100
X2	0.755
X3	4.420
X4	5.610
Y	1.270
Y1	0.600
Y2	1.020
Y3	0.295
Y4	1.825
Y5	3.810
Y6	0.180
Y7	6.610



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