

## 40V 175°C N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI

# **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> T <sub>C</sub> = +25°C
40V	$6.5 \text{m}\Omega$ @ $V_{GS} = 10 \text{V}$	100A
	$9.8 \text{m}\Omega$ @ $V_{GS} = 4.5 \text{V}$	80A

# **Features**

- Thermally Efficient Package Cooler Running Applications
- High Conversion Efficiency
- Low R<sub>DS(ON)</sub> Minimizes On-State Losses
- Low Input Capacitance
- · Fast Switching Speed
- <1.1mm Package Profile Ideal for Thin Applications</li>
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

# **Description and Applications**

This MOSFET is designed to meet the stringent requirements of Automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Motor Control
- DC-DC Converters
- Loadswitch

# **Mechanical Data**

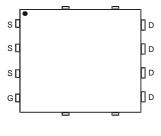
- Case: PowerDI<sup>®</sup>5060-8
- Case Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe.
   Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.097 grams (Approximate)



Top View

Pin1 G

G S



Top View Pin Configuration

# Ordering Information (Note 5)

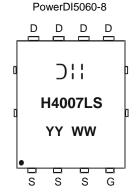
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	Part Number	Case	Packaging
	DMTH4007LPSQ-13	PowerDI5060-8	2,500/Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to http://www.diodes.com/quality/product\_compliance\_definitions/.
- 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

**Bottom View** 

# Marking Information



Oll = Manufacturer's Marking
H4007LS = Product Type Marking Code
YYWW = Date Code Marking
YY = Last Digit of Year (ex: 14 = 2014)
WW = Week Code (01 to 53)

PowerDI is a registered trademark of Diodes Incorporated.



# **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Drain-Source Voltage		$V_{DSS}$	40	V
Gate-Source Voltage		$V_{GSS}$	±20	V
Continuous Drain Current, V <sub>GS</sub> = 10V (Note 6)	$T_A = +25$ °C $T_A = +70$ °C	I <sub>D</sub>	15.5 13	А
Continuous Drain Current, V <sub>GS</sub> = 10V (Note 7)	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	Ι <sub>D</sub>	100 80	А
Maximum Continuous Body Diode Forward Current (Note 7)	Is	100	Α	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I <sub>DM</sub>	160	Α
Avalanche Current, L = 0.1mH		I <sub>AS</sub>	20	Α
Avalanche Energy, L = 0.1mH		Eas	20	mJ

# **Thermal Characteristics**

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 6)	$T_A = +25$ °C	$P_{D}$	2.7	W
Thermal Resistance, Junction to Ambient (Note 6)		$R_{\theta JA}$	55	°C/W
Total Power Dissipation (Note 7)	$T_C = +25^{\circ}C$	P <sub>D</sub>	150	W
Thermal Resistance, Junction to Case (Note 7)		R <sub>eJC</sub>	1	°C/W
Operating and Storage Temperature Range		$T_{J_{i}}T_{STG}$	-55 to +175	°C

# **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)				,			
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	40	_	_	V	$V_{GS} = 0V, I_D = 1mA$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	1	μΑ	$V_{DS} = 32V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)	·						
Gate Threshold Voltage	V <sub>GS(th)</sub>	1	_	3	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance	-		5.4	6.5	mΩ	$V_{GS} = 10V, I_D = 20A$	
Static Diain-Source On-Resistance	R <sub>DS(ON)</sub>	_	8.4	9.8	11122	$V_{GS} = 4.5V, I_D = 20A$	
Diode Forward Voltage	V <sub>SD</sub>	_	_	1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 20A	
DYNAMIC CHARACTERISTICS (Note 9)	·						
Input Capacitance	C <sub>iss</sub>		1,895	_		V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V, f = 1MHz	
Output Capacitance	Coss	l	485	_	pF		
Reverse Transfer Capacitance	C <sub>rss</sub>		20.9	_			
Gate Resistance	Rg	0.1	0.62	1.8	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Qg	_	12.4				
Total Gate Charge (V <sub>GS</sub> = 10V)	Qg	_	29.1	_	1		
Gate-Source Charge	Q <sub>gs</sub>	_	5.9	_	nC	$V_{DS} = 30V, I_{D} = 20A$	
Gate-Drain Charge	Q <sub>qd</sub>	_	3.5	_			
Turn-On Delay Time	t <sub>D(ON)</sub>	_	5.4	_			
Turn-On Rise Time	t <sub>R</sub>	_	4.5	_		$V_{DD} = 30V, V_{GS} = 10V,$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	16.2	_	ns	$I_D = 20A$ , $R_G = 3\Omega$	
Turn-Off Fall Time	t <sub>F</sub>	_	3.5	_			
Body Diode Reverse Recovery Time	t <sub>RR</sub>		30.6	_	ns	IE 204 di/dt 1004/u-	
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>		28.1	_	nC		

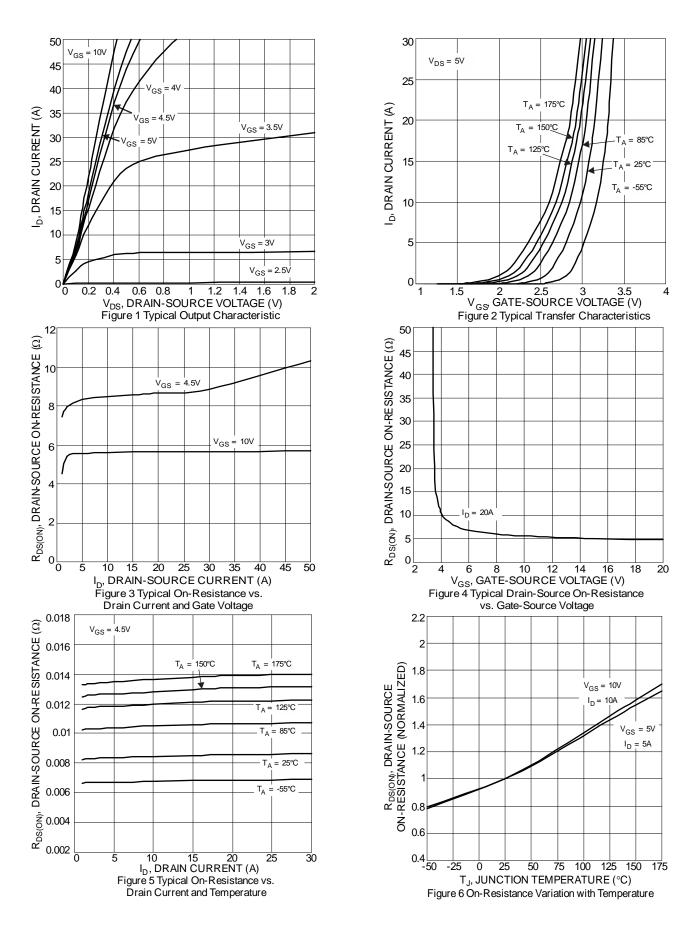
Notes: 6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.

7. Thermal resistance from junction to soldering point (on the exposed drain pad).

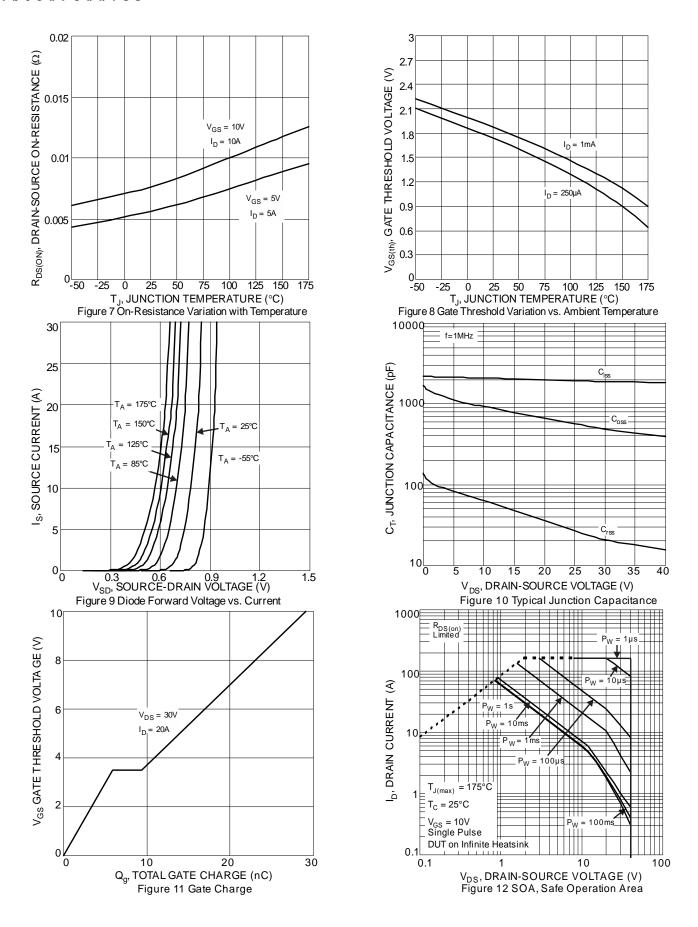
8. Short duration pulse test used to minimize self-heating effect.

9. Guaranteed by design. Not subject to product testing.

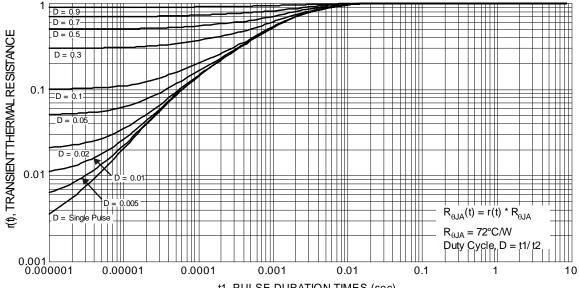












t1, PULSE DURATION TIMES (sec) Figure 13 Transient Thermal Resistance



# **Package Outline**

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

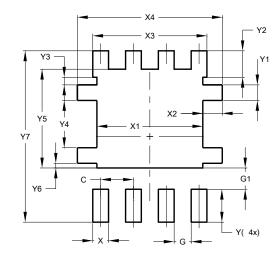
# PowerDI5060-8 Detail A O(4X) O(4X) O(4X) D(4X) D(4X)

PowerDI5060-8				
Dim	Min	Max	Тур	
Α	0.90	1.10	1.00	
A1	0.00	0.05	_	
b	0.33	0.51	0.41	
b2	0.200	0.350	0.273	
b3	0.40	0.80	0.60	
С	0.230	0.330	0.277	
D		5.15 BSC	;	
D1	4.70	5.10	4.90	
D2	3.70	4.10	3.90	
D3	3.90	4.30	4.10	
Е	•	3.15 BSC	,	
E1	5.60	6.00	5.80	
E2	3.28	3.68	3.48	
E3	3.99	4.39	4.19	
е	•	1.27 BSC	;	
G	0.51	0.71	0.61	
K	0.51	_	_	
L	0.51	0.71	0.61	
L1	0.100	0.200	0.175	
M	3.235	4.035	3.635	
M1	1.00	1.40	1.21	
θ	10°	12°	11°	
θ1	6°	8°	7°	
All Dimensions in mm				

# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

# PowerDI5060-8



value (in mm)			
1.270			
0.660			
0.820			
0.610			
4.100			
0.755			
4.420			
5.610			
1.270			
0.600			
1.020			
0.295			
1.825			
3.810			
0.180			
6.610			



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