



DT2042-04TS

Product Summary

V _{BR (min)}	IPP (max)	CI/O (typ)
6V	10A	1.2pF

Description

This new generation TVS is designed to protect sensitive electronics from the damage due to ESD. The combination of low capacitance and high ESD surge capability makes it ideal for use in high-speed ports such as USB 2.0, IEEE1394, HDMI, Laptop and Personal Computers, Flat Panel Displays, Video Graphics Displays, SIM Ports.

Applications

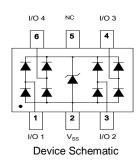
- **USB 2.0**
- **IEEE1394**
- SIM Ports
- **Portable Electronics**
- **Computers and Peripheral**

Features

- Low Clamping Voltage Typical 9V at 10A 100ns, TLP Typical 9V at 10A 8µs/20µs
- IEC 61000-4-2 (ESD): Air ±30kV, Contact ±30kV
- IEC 61000-4-4 (EFT): 60A(5/50ns, I/O to Vss)
- 4 Channels of ESD protection
- TLP Dynamic Resistance: 0.25Ω
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3) •

Mechanical Data

- Case: TSOT26
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed Over Copper Leadframe
- (Lead Free Plating).
 - Solderable per MIL-STD-202, Method 208(e3)
- Weight: 0.013 grams (Approximate)



Ordering Information (Note 4)

Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
DT2042-04TS-7	Standard	BE6	7	8	3,000/Tape & Reel

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

TSOT26

Top View

Marking Information

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В	E6	ΥM	
• 			

BE6= Product Type Marking Code YM = Date Code Marking Y = Year (ex: A = 2013)M = Month (ex: 9 = September)

Date Code Kev

Year	20	13	20	14	20	15	20	16	20	17	20	18
Code	A	A	E	3	()	[)	E		F	-
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	Ν	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Current, per IEC 61000-4-5	I _{PP}	±10	А	I/O to V _{SS} , 8/20µs
Peak Pulse Power, per IEC 61000-4-5	P _{PP}	105	W	I/O to V _{SS} , 8/20µs
Operating Voltage (DC)	V _{DC}	5.5	V	I/O to V _{SS}
ESD Protection – Contact Discharge, per IEC61000-4-2	V _{ESD_Contact}	±30	kV	I/O to V _{SS}
ESD Protection – Air Discharge, per IEC 61000-4-2	V _{ESD_Air}	±30	kV	I/O to V _{SS}

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation Typical (Note 5)	PD	300	mW
Thermal Resistance, Junction to Ambient Typical (Note 5)	R _{0JA}	417	°C/W
Operating Junction Temperature Range	TJ	-65 to +150	°C
Storage Temperature Range	T _{STG}	-65 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Working Voltage	V _{RWM}	_	—	5.5	V	I/O to V _{SS}
Reverse Current(Note6)	I _R	_	—	1	μA	V_R = 5V, any I/O to V_{SS}
Reverse Breakdown Voltage	V _{BR}	6	—	9	V	$I_R = 1mA$, I/O to V _{SS}
Forward Clamping Voltage	VF	-1.0	-0.8	_	V	I_F = -15mA, I/O to V _{SS}
Holding Voltage	V _H	5.5	—	_	V	—
Trigger Voltage	V _{TRIG}	_	9	9.5	V	—
Reverse Clamping Voltage (Note 7)	V _{C_5A}	_	7.5	—	V	I _{PP} = 5A, I/O to V _{SS} , 8/20μs
Reverse Clamping Voltage (Note 7)	V _{C_10A}	_	9	10.5	V	I _{PP} = 10A, I/O to V _{SS} , 8/20μs
ESD Clamping Voltage	V _{ESD}	_	9	_	V	TLP, 10A, tp = 100 ns, I/O to V_{SS} , per Figure 7
Dynamic Resistance	R _{DIF}	_	0.25	_	Ω	TLP, 10A, tp = 100 ns, I/O to V_{SS}
Channel Input Capacitance	C _{I/O}	_	1.2	1.5	pF	V _R = 2.5V, f = 1MHz
Variation of Channel Input Capacitance	ΔCI/O		0.02	_	pF	$\label{eq:Vss} \begin{array}{l} Vss = 0V, I/O = 2.5V, \ f = 1MHz, \ T = +25^\circC \ , \\ I/O_x \ to \ V_{SS} = I/O_y \ to \ V_{SS} \end{array}$

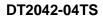
Notes: 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes, Inc. suggested pad layout AP02001, which can be found on our website at http://www.diodes.com.

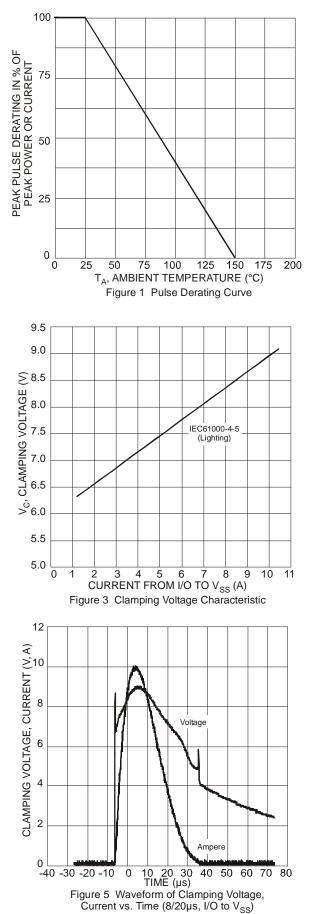
6. Short duration pulse test used to minimize self-heating effect.

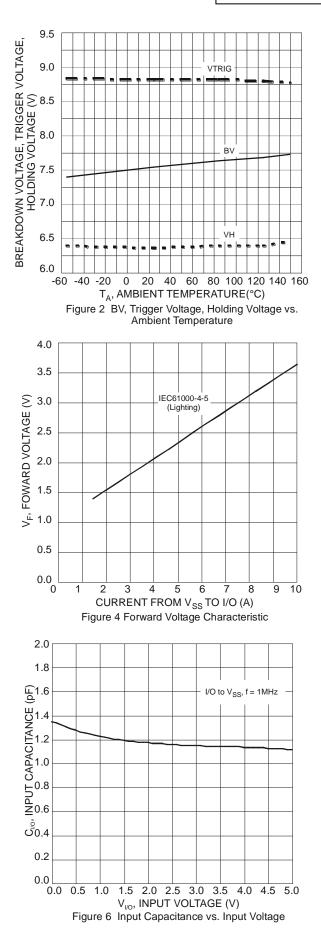
7. Clamping voltage value is based on an $8x20\mu s$ peak pulse current (Ipp) waveform.



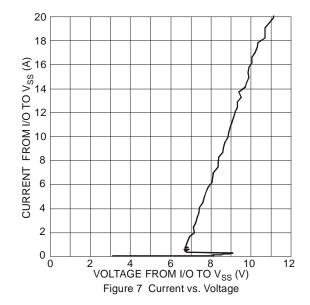
NEW PRODUCT





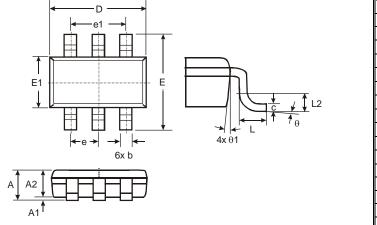






Package Outline Dimensions

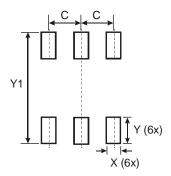
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



TSOT26							
Dim	Min Max Typ						
Α	-	1.00	-				
A1	0.01	0.10	-				
A2	0.84	0.90	-				
D	-	-	2.90				
Е	-	-	2.80				
E1	-	-	1.60				
b	0.30	0.45	-				
С	0.12	0.20	-				
е	-		0.95				
e1	-	-	1.90				
L	0.30	0.50					
L2	-	—	0.25				
θ	0°	8°	4°				
θ1	4°	12°	-				
All D	All Dimensions in mm						

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	0.950
Х	0.700
Y	1.000
Y1	3.199

NEW PRODUCT

DT2042-04TS Document number: DS36674 Rev. 3 – 2



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