





500V PNP SILICON PLANAR HIGH VOLTAGE TRANSISTOR POWERDI®5

Features and Benefits

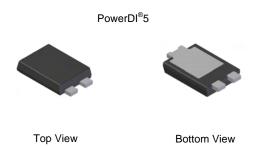
- BV_{CEO} > -500V
- I_C = -150mA Continuous Collector Current
- 47% smaller than SOT223; 60% smaller than TO252 (D-PAK)
- Profile height just 1.1mm for thin application
- R_θJA efficient giving high P_D rating up to 2.8W
- "Lead Free", RoHS Compliant (Note 1)
- Halogen and Antimony Free, "Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

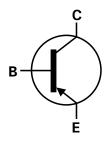
Mechanical Data

- Case: POWERDI[®]5
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Copper leadframe.
 Solderable per MIL-STD-202, Method 208
- Weight: 0.093 grams (approximate)

Applications

- Gate driver
- Startup switch in offline lighting
- Motor Control







Device Schematic

Top View Pin-Out

Ordering Information (Note 3)

I	Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
	DXTP560BP5-13	DXTP560B	13	16	5,000

Notes:

- 1. No purposefully added lead.
- 2. Diodes Inc's "Green" Policy can be found on our website at http://www.diodes.com.
- 3. For packaging details, go to our website at http://www.diodes.com

Marking Information



DXTP560B = Product Type Marking Code

Oli = Manufacturers' Code Marking

K = Factory Designator

YYWW = Date Code Marking

YY = Last Two Digits of Year (ex: 10 for 2010)

WW = Week code (01 - 53)





Maximum Ratings @TA = 25°C unless otherwise specified

Characteristic	Symbol	Limit	Unit	
Collector-Base Voltage	V _{CBO}	-500		
Collector-Emitter Voltage	V _{CEO}	-500 V		
Emitter-Base Voltage		V _{EBO}	-7	
Continuous Collector Current	(Note 4)	Ic	-150	A
Peak Pulse Current	I _{CM}	-500	mA mA	

Thermal Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit		
	(Note 4)		2.8 22.4		
Power Dissipation Linear Derating Factor	(Note 5)	P _D	1.3 10.4	W mW/°C	
	(Note 6)		0.7 5.6		
	(Note 4)		45		
Thermal Resistance, Junction to Ambient	(Note 5)	$R_{\theta JA}$	R _{eJA} 96		
	(Note 6)		179	1	
Thermal Resistance, Junction to Lead	(Note 7)	$R_{ heta JL}$	14	°C/W	
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C		

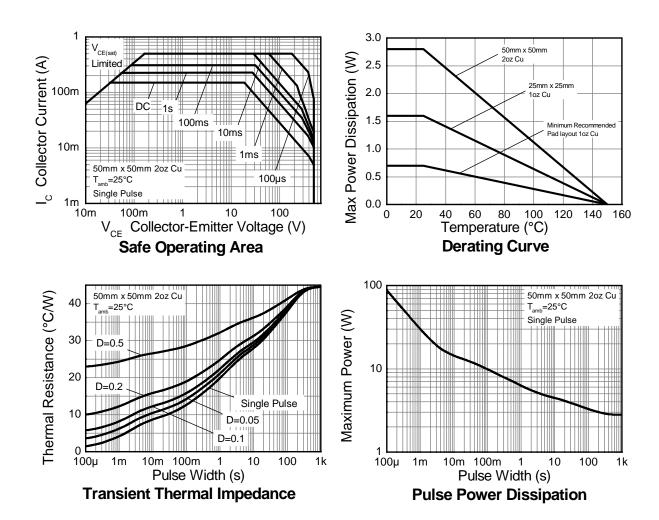
Notes:

- For a device surface mounted on 50mm x 50mm x 1.6mm FR4 PCB with high coverage of single sided 2oz copper, in still air conditions; the device is measured when operating in a steady-state condition. The entire exposed collector pad is attached to the heatsink.
 Same as note (4), except the device is mounted on 25mm x 25mm 1oz copper.
- 6. Same as note (4), except the device is mounted on a minimum recommended pad layout of 1oz copper.
- 7. Thermal resistance from junction to solder-point (at the end of the collector lead).

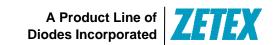




Thermal Characteristics





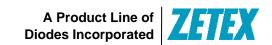


Electrical Characteristics @TA = 25°C unless otherwise specified

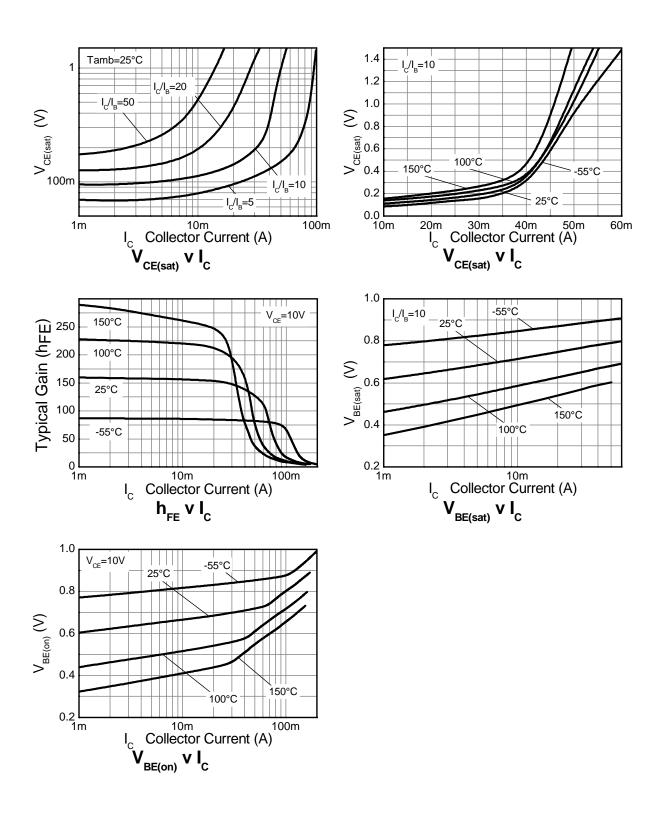
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage		-500	_	_	V	$I_{C} = -100 \mu A$
Collector-Emitter Breakdown Voltage (Note 8)		-500	_	_	V	$I_C = -10 \text{mA}$
Emitter-Base Breakdown Voltage	BV_{EBO}	-7	_	_	V	$I_E = -100 \mu A$
Collector Cutoff Current	I _{CBO}	_	_	-100	nA	V _{CB} = -500V
Collector Cutoff Current	I _{CES}		_	-100	nA	V _{CE} = -500V
Emitter Cutoff Current	I _{EBO}	_	_	-100	nA	V _{EB} = -5.6V
Collector-Emitter Saturation Voltage (Note 8)	V _{CE(sat)}		_	-200 -500	mV	$I_C = -20\text{mA}, I_B = -2\text{mA}$ $I_C = -50\text{mA}, I_B = -10\text{mA}$
Base-Emitter Saturation Voltage (Note 8)	V _{BE(sat)}	_	_	-900	mV	I _C = -50mA, I _B = -10mA
Base-Emitter Turn-On Voltage (Note 8)	V _{BE(on)}	_	_	-900	mV	V _{CE} = -10V, I _C = -50mA
DC Current Gain (Note 8)	h _{FE}	100 80 —	— — 15	300 300 —		$V_{CE} = -10V, I_C = -1mA$ $V_{CE} = -10V, I_C = -50mA$ $V_{CE} = -10V, I_C = -100mA$
Transition Frequency	f⊤	60	_	_	MHz	$V_{CE} = -20V, I_{C} = -10mA,$ f = 50MHz
Output Capacitance	C _{obo}	_		8	pF	V _{CB} = -20V, f = 1MHz
Switching Times	t _{on} t _{off}		110 1500	_	ns	$V_{CC} = -100V, I_C = -50mA,$ $I_{B1} = 5mA, I_{B2} = -10mA$

Notes: 8. Measured under pulsed conditions. Pulse width $\leq 300 \mu s$. Duty cycle $\leq 2\%$.





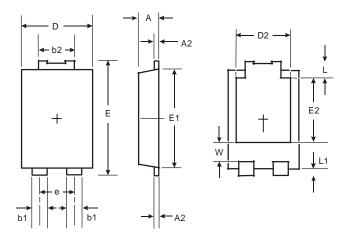
Typical Electrical Characteristics





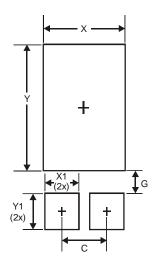


Package Outline Dimensions



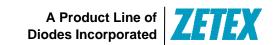
POWERDI [®] 5				
Dim	Min	Max		
Α	1.05	1.15		
A2	0.33	0.43		
b1	0.80	0.99		
b2	b2 1.70 1.88			
D	3.90	4.05		
D2	3.054 Typ			
Е	6.40	6.60		
е	1.84 Typ			
E1	5.30	5.45		
E2	E2 3.549 Typ			
L	0.75	0.95		
L1	0.50	0.65		
W	1.10	1.41		
All Dimensions in mm				

Suggested Pad Layout



Dimensions	Value (in mm)
С	1.840
G	0.852
Х	3.360
X1	1.390
Y	4.860
V1	1 400





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