



SBR02U100LP

0.2A SBR®

SURFACE MOUNT SUPER BARRIER RECTIFIER

Features

- Ultra Low Forward Voltage Drop
- Superior Reverse Avalanche Capability
- Patented Super Barrier Rectifier Technology
- Soft, Fast Switching Capability
- +150°C Operating Junction Temperature
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: X1-DFN1006-2
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: Cathode Dot
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @4
- Weight: 0.001 grams (Approximate)

X1-DFN1006-2







Bottom View

Ordering Information (Note 4)

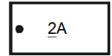
Part Number	Case	Packaging
SBR02U100LP-7	X1-DFN1006-2	3,000/Tape & Reel
SBR02U100LP-7B	X1-DFN1006-2	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

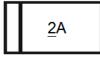
Marking Information

SBR02U100LP-7



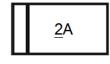
Top View Dot Denotes Cathode Side

OR



Top View Bar Denotes Cathode Side

SBR02U100LP-7B



Top View Bar Denotes Cathode Side 2A = Product Type Marking Code



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _{RM}	100	V
RMS Reverse Voltage	V _{R(RMS)}	70	V
Average Rectified Output Current (See Figure 1)	lo	250	mA
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	5	Α

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Maximum Thermal Resistance			
Thermal Resistance, Junction to Ambient (Note 5) T _A = +25°C	$R_{\theta JA}$	270	°C /W
Thermal Resistance, Junction to Ambient (Note 6) $T_A = +25$ °C	$R_{\theta JA}$	235	
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150	°C

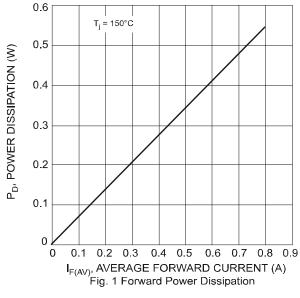
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

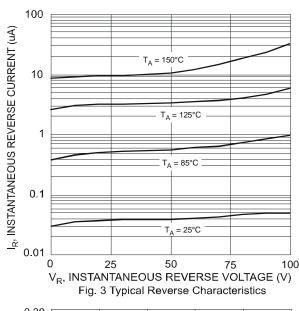
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 7)	$V_{(BR)R}$	100	_	_	V	I _R = 1mA
Forward Voltage Drop	VF		0.67 0.76 0.60	0.72 0.80 0.65		I _F = 100mA, T _J = +25°C I _F = 200mA, T _J = +25°C I _F = 200mA, T _J = +125°C
Leakage Current (Note 7)	I _R	_	0.04 6	1.0 50	μA	V _R = 75V, T _J = +25°C V _R = 75V, T _J = +85°C

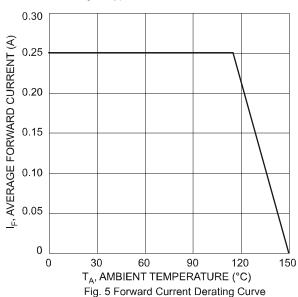
Notes:

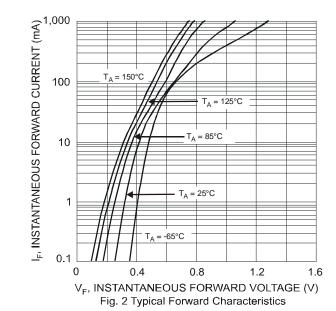
- 5. FR-4 PCB, 2oz. Copper, minimum recommended pad layout per http://www.diodes.com.6. Polyimide PCB, 2oz. Copper, minimum recommended pad layout per http://www.diodes.com.7. Short duration pulse test used to minimize self-heating effect.

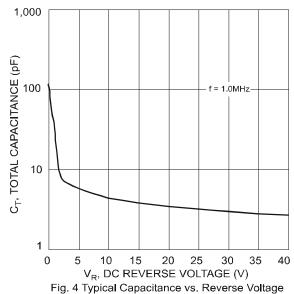












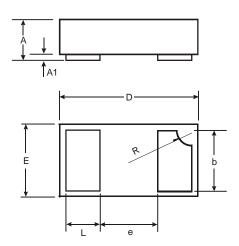
150 0 125 100 100 100 100 V_R, DC REVERSE VOLTAGE (V) Fig. 6 Operating Temperature Derating



Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

X1-DFN1006-2

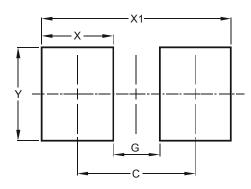


X1-DFN1006-2					
Dim	Min	Max	Тур		
Α	0.47	0.53	0.50		
A1	0	0.05	0.03		
b	0.45	0.55	0.50		
D	0.95	1.075	1.00		
E	0.55	0.675	0.60		
е	-	-	0.40		
L	0.20	0.30	0.25		
R	0.05	0.15	0.10		
All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

X1-DFN1006-2



Dimensions	Value (in mm)
С	0.70
G	0.30
Х	0.40
X1	1.10
Υ	0.70



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