



# 2A SBR®

#### SURFACE MOUNT SUPER BARRIER RECTIFIER

#### Product Summary(@T<sub>A</sub> = +25°C)

V <sub>RRM</sub> (V)	I <sub>O</sub> (A)	V <sub>F</sub> (MAX)(V)	I <sub>R(MAX)</sub> (mA)
10	2	0.46	2

## **Description and Applications**

Packaged in the compact X1-DFN1411-3 package, the SBR2U10LP provides ultra-low forward voltage drop (V<sub>F</sub>) and provides excellent low reverse leakage stability at high temperatures. It is ideal for use as a bypass, freewheeling or polarity protection diode in applications such as:

- Solar Panels
- **Portable Electronics**

# **Features and Benefits**

- Small Form factor Package with a PCB Footprint of just 1.54mm<sup>2</sup> - 40% Smaller Than SOT666
- Lower Reverse Leakage Ensuring Greater Stability at Higher Temperatures
- Low Forward Voltage (V<sub>F</sub>) Minimises Conduction Losses and Improving Efficiency
- Totally Lead-Free; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

## **Mechanical Data**

- Case: X1-DFN1411-3
- Case Material: Molded Plastic, "Green" Molding Compound.
- UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: Cathode Bar (See Note 5)
- Terminals: Finish NiPdAu over Copper Lead Frame.
- Solderable per MIL-STD-202, Method 208 4
- Weight: 2.35mg (approximate)



X1-DFN1411-3



Top View Internal Schematic

#### Ordering Information (Note 4)

Part Number	Case	Packaging
SBR2U10LP-7	X1-DFN1411-3	3000/Tape & Reel

Bottom View

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant Notes:

2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

D5 = Product Type Marking Code

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

5. It is recommended that Pins 2 and 3 be electrically connected at the printed circuit board.

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## **Marking Information**

				<u>D</u> 5	MY	i = Year (e) 1 = Month (	с: В = 201					
Date Code Key	0044			0040	0047		40	0040				
Year	2014	20	15	2016	2017	20	18	2019	2020	20	)21	2022
Code	В		C	D	E		F	G	Н		I	J
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	Ν	D



# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>RM</sub>	10	V
Average Rectified Output Current (See Figure 1)	lo	2	A
Non-Repetitive Peak Forward Surge Current, 8.3ms Single Half Sine-Wave Superimposed on Rated Load	IFSM	21	А

## **Thermal Characteristics**

Characteristic	Symbol	Value	Unit	
Thermal Resistance Junction to Case (Note 6)	R <sub>θJC</sub>	55	*CAN	
Thermal Resistance Junction to Ambient (Note 6)	R <sub>0JA</sub>	210	°C/W	
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-65 to +150	°C	

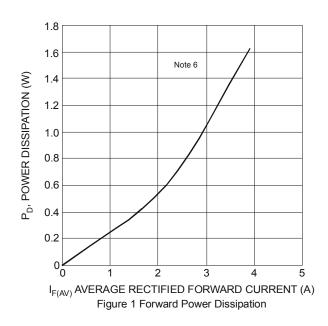
# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

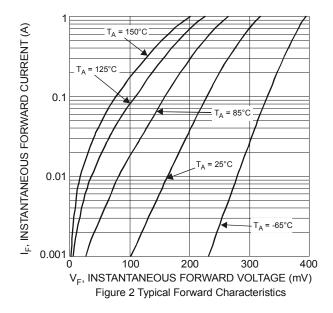
Characteristic		Min	Тур	Max	Unit	Test Condition
Forward Voltage Drop (Note 7)	VF	_	0.40	0.46	V	I <sub>F</sub> = 2.0A, T <sub>J</sub> = +25°C
Leakage Current (Note 8)		_	0.5	2	mA	V <sub>R</sub> = 10V, T <sub>J</sub> = +25°C
		_	25	100	mA	V <sub>R</sub> = 10V, T <sub>J</sub> = +125°C
Reverse Recovery Time	t <sub>rr</sub>		43	60	ns	$I_F = 10 \text{mA}, I_{rr} = 0.1^* I_{RM},$ $R_L = 100\Omega$
Junction Capacitance	Cj	_	102	_	pF	V <sub>R</sub> = 5V, f = 1.0MHz

 Notes:
 6. Device mounted on FR-4 substrate, 1"\*1", 2oz, single-sided, PC boards with 0.1"\*0.15" copper pad.

 7. It is recommended to electrically connect both Anode pins together during operation to achieve optimal performance.

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 Short duration pulse test used to minimize self-heating effect.

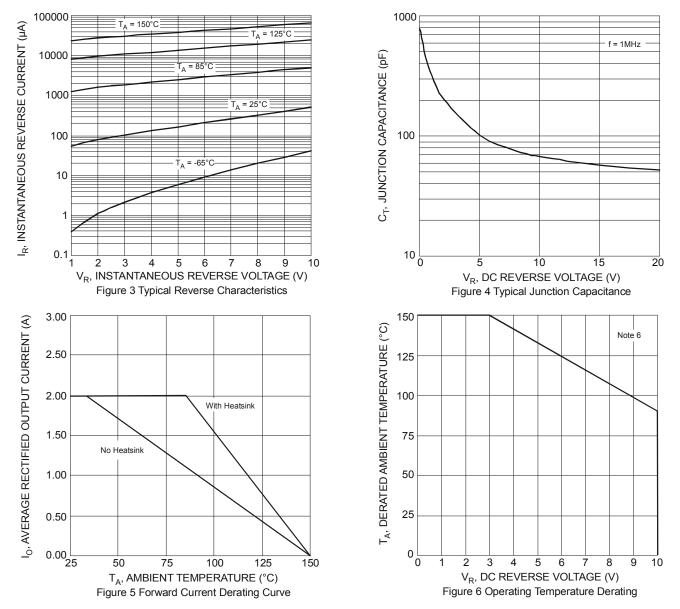




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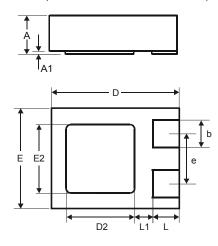


# SBR2U10LP



#### **Package Outline Dimensions**

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



)	X1-DFN1411-3					
Dim	Min	Max	Тур			
Α	0.47	0.53	0.50			
A1	0	0.05	0.02			
b	0.25	0.35	0.30			
D	1.35	1.475	1.40			
D2	0.65	0.85	0.75			
Е	1.05	1.175	1.10			
E2	0.65	0.85	0.75			
е			0.55			
L	0.225	0.325	0.275			
L1		_	0.20			
All Dimensions in mm						

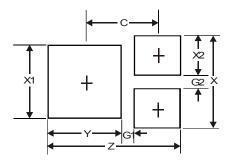
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## Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	1.38
G1	0.15
G2	0.15
X	0.95
X1	0.75
X2	0.40
Y	0.75
С	0.76

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