

### 8A SBR® **SUPER BARRIER RECTIFIER** POWERDI®5

#### **Features**

- Designed as Bypass Diodes for Solar Panels
- Selectively Rated for 200°C Maximum Junction Temperature for High Thermal Reliability
- Patented Super Barrier Rectifier Technology
- High Forward Surge Capability
- Ultra Low Forward Voltage Drop
- **Excellent High Temperature Stability**
- Lead Free Finish, RoHS Compliant (Note 1)
- "Green" Molding Compound (No Br, Sb)

### **Mechanical Data**

- Case: POWERDI®5
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 🚳
- Weight: 0.093 grams (approximate)





**BOTTOMSIDE** HEAT SINK RIGHT PIN o

Top View

**Bottom View** 

Note: Pins Left & Right must be electrically connected at the printed circuit board.

### Ordering Information (Note 2)

| Part Number   | Case                   | Packaging        |
|---------------|------------------------|------------------|
| SBR8U20SP5-13 | POWERDI <sup>®</sup> 5 | 5000/Tape & Reel |

Notes:

- 1. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied, see EU Directive 2002/95/EC Annex Notes
- 2. For packaging details, go to our website at http://www.diodes.com.

# **Marking Information**



S8U20S = Product Type Marking Code Oll = Manufacturers' Code Marking K = Factory Designator YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 08 for 2008) WW = Week code (01 - 53)



## Maximum Ratings @TA = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

| Characteristic  | Symbol  | Value | Unit |
|---|---|-------|------|
| Peak Repetitive Reverse Voltage<br>Working Peak Reverse Voltage<br>DC Blocking Voltage              | V <sub>RRM</sub><br>V <sub>RWM</sub><br>V <sub>RM</sub> | 20    | V    |
| Average Rectified Output Current  | lo  | 8     | Α    |
| Non-Repetitive Peak Forward Surge Current 8.3ms<br>Single Half Sine-Wave Superimposed on Rated Load | I <sub>FSM</sub>  | 180   | Α    |

## **Thermal Characteristics**

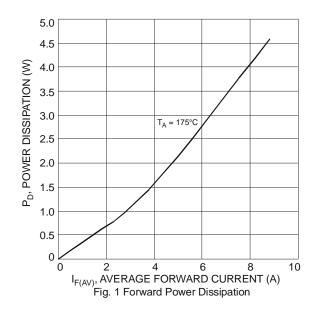
| Characteristic   |                                       | Symbol                      | Value       | Unit |
|--|---------------------------------------|-----------------------------|-------------|------|
| Maximum Thermal Resistance<br>Thermal Resistance Junction to Ambient (Note 3<br>Thermal Resistance Junction to Ambient (Note 4 |                                       | $R_{	hetaJA}$ $R_{	hetaJA}$ | 102<br>60   | °C/W |
|  | V <sub>R</sub> ≤ 80% V <sub>RRM</sub> |                             | -65 to +150 |      |
| Operating Temperature Range  | V <sub>R</sub> ≤ 50% V <sub>RRM</sub> | $T_J$                       | ≤180        | °C   |
|  | DC Forward Mode                       |                             | ≤200        |      |
| Storage Temperature Range  |                                       | T <sub>STG</sub>            | -65 to +175 | °C   |

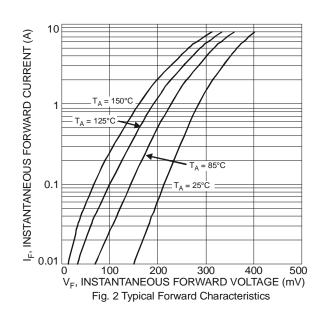
# Electrical Characteristics @TA = 25°C unless otherwise specified

| Characteristic           | Symbol         | Min | Тур  | Max  | Unit | Test Condition                 |
|--------------------------|----------------|-----|------|------|------|--------------------------------|
| Forward Voltage Drop     | VE             | -   | 0.41 | 0.51 | I V  | $I_F = 8A, T_J = 25^{\circ}C$  |
|                          | ٧F             | -   | 0.33 | 0.43 |      | $I_F = 8A, T_J = 125^{\circ}C$ |
| Leakage Current (Note 5) | I <sub>R</sub> | -   | 0.08 | 0.2  | I MA | $V_R = 4V, T_J = 25^{\circ}C$  |
|                          |                | -   | 0.2  | 0.5  |      | $V_R = 20V, T_J = 25^{\circ}C$ |

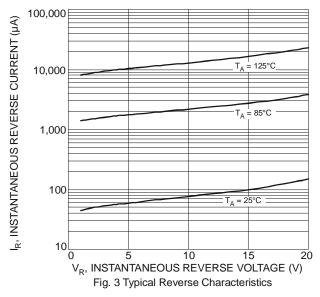
Notes:

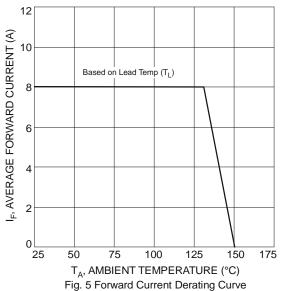
- 3. FR-4 PCB, 2oz. Copper, minimum recommended pad layout per http://www.diodes.com.
- 4. Polymide PCB, 2oz. Copper. Cathode pad dimensions 18.8mm x 14.4mm. Anode pad dimensions 5.6mm x 14.4mm.
- 5. Short duration pulse test used to minimize self-heating effect.











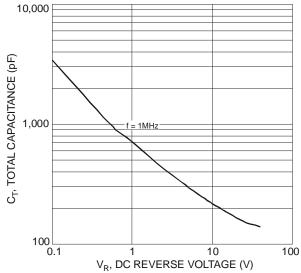
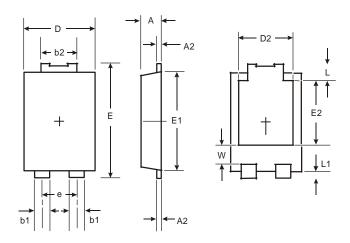


Fig. 4 Total Capacitance vs. Reverse Voltage

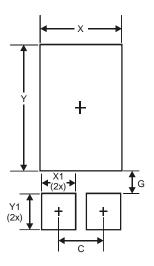
# **Package Outline Dimensions**



| POWERDI <sup>®</sup> 5 |           |      |  |  |
|------------------------|-----------|------|--|--|
| Dim                    | Min       | Max  |  |  |
| Α                      | 1.05      | 1.15 |  |  |
| A2                     | 0.33      | 0.43 |  |  |
| b1                     | 0.80      | 0.99 |  |  |
| b2                     | 1.70      | 1.88 |  |  |
| D                      | 3.90      | 4.05 |  |  |
| D2                     | 3.054 Typ |      |  |  |
| Е                      | 6.40      | 6.60 |  |  |
| е                      | 1.84 Typ  |      |  |  |
| E1                     | 5.30      | 5.45 |  |  |
| E2                     | 3.549 Typ |      |  |  |
| L                      | 0.75      | 0.95 |  |  |
| L1                     | 0.50      | 0.65 |  |  |
| W                      | 1.10      | 1.41 |  |  |
| All Dimensions in mm   |           |      |  |  |



### **Suggested Pad Layout**



| Dimensions | Value (in mm) |
|------------|---------------|
| С          | 1.840         |
| G          | 0.852         |
| Х          | 3.360         |
| X1         | 1.390         |
| Y          | 4.860         |
| Y1         | 1.400         |

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