## SBRT20U50SLPQ <br> 20A TrenchSBR <br> TRENCH SUPER BARRIER RECTIFIER <br> POWERDI ${ }^{\circledR} 5060$

## Product Summary

| VRRM (V) | Io (A) | $\begin{gathered} \hline \mathrm{V}_{\mathrm{F}(\mathrm{MAX})}(\mathrm{V}) \\ @+25^{\circ} \mathrm{C} \end{gathered}$ | $\begin{gathered} \hline \mathrm{I}_{\mathrm{R} \text { (MAX) })}(\mathrm{mA}) \\ @+25^{\circ} \mathrm{C} \end{gathered}$ |
| :---: | :---: | :---: | :---: |
| 50 | 20 | 0.5 | 0.5 |

## Description and Applications

Packaged in the compact thermally efficient POWERDI5060-8 package, the SBRT20U50SLPQ provides very low $\mathrm{V}_{\mathrm{F}}$ and excellent reverse leakage stability at high temperatures. It is ideal for use as a rectifier, freewheel diode or blocking diode in:

- Automotive Applications


## POWERDI5060-8



## Features and Benefits

- Patented Trench SBR technology provides superior avalanche capability versus Schottky diodes, ensuring more rugged and reliable end applications.
- Reduced ultra-low forward voltage drop ( $\mathrm{V}_{\mathrm{F}}$ ); Better efficiency and cooler operation.
- Reduced high temperature reverse leakage; Increased reliability against thermal runaway failure in high temperature operation.
- Less than 1.1 mm package profile - ideal for thin applications.
- Lead-Free Finish; RoHS Compliant (Notes 1 \& 2)
- Halogen and Antimony Free "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)


## Mechanical Data

- Case: POWERDI5060-8
- Case Material: Molded Plastic, "Green" Molding compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Matte Tin annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 ③
- Polarity: See Below
- Weight: 0.097 grams (approximate)


## Ordering Information (Note 5)

| Part Number | Compliance | Case | Packaging |
| :---: | :---: | :---: | :---: |
| SBRT20U50SLPQ-13 | Automotive | POWERDI5060-8 | $2500 /$ Tape \& Reel |

Notes: 1. EU Directive 2002/95/EC (RoHS) \& 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain $<900 \mathrm{ppm}$ bromine, $<900 \mathrm{ppm}$ chlorine ( $<1500 \mathrm{ppm}$ total $\mathrm{Br}+\mathrm{Cl}$ ) and <1000ppm antimony compounds
4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product_compliance_definitions/.
5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

## Marking Information



SBRT20U50 = Product Type Marking Code YYWW = Date Code Marking
YY = Last two digits of year (ex: $14=2014$ ) WW = Week (01-53)

SBRT20U50SLPQ

Maximum Ratings $@_{\mathrm{A}}=+25^{\circ} \mathrm{C}$, unless otherwise specified.)
Single phase, half wave, 60 Hz , resistive or inductive load.
For capacitance load, derate current by 20\%.

| Characteristic | Symbol | Value | Unit |
| :---: | :---: | :---: | :---: |
| Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage | $V_{\text {RRM }}$ <br> $V_{\text {RWM }}$ <br> $V_{\text {RM }}$ | 50 | V |
| RMS Reverse Voltage | $\mathrm{V}_{\mathrm{R} \text { (RMS) }}$ | 35 | V |
| Average Rectified Output Current | $\mathrm{I}_{0}$ | 20 | A |
| Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load | Ifsm | 200 | A |
| Non-Repetitive Avalanche Energy $\\|\left(\mathrm{T}_{\mathrm{J}}=+25^{\circ} \mathrm{C}, \mathrm{I}_{\mathrm{AS}}=14.5 \mathrm{~A}, \mathrm{~L}=8.5 \mathrm{Mh}\right)$ | $\mathrm{E}_{\text {AS }}$ | 640 | mJ |
| Repetitive Peak Avalanche Energy ( $1 \mu \mathrm{~s},+25^{\circ} \mathrm{C}$ ) | PARM | 40000 | W |

## Thermal Characteristics

| Characteristic | Symbol | Value | Unit |
| :--- | :---: | :---: | :---: |
| Typical Thermal Resistance Junction to Ambient (Note 6) | $\mathrm{R}_{\text {QJA }}$ | 12 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Operating Temperature Range | $\mathrm{T}_{\mathrm{J}}$ | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature Range | $\mathrm{T}_{\text {STG }}$ | -55 to +175 | ${ }^{\circ} \mathrm{C}$ |

Electrical Characteristics (@T $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$, unless otherwise specified.)

| Characteristic | Symbol | Min | Typ | Max | Unit | Test Condition |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Forward Voltage Drop (Note 7) | $V_{F}$ | - | $\begin{aligned} & 0.375 \\ & 0.445 \end{aligned}$ | $\begin{aligned} & \hline 0.420 \\ & 0.500 \end{aligned}$ | V | $\begin{aligned} & \mathrm{IF}_{F}=10 \mathrm{~A}, \mathrm{~T}_{\mathrm{J}}=+25^{\circ} \mathrm{C} \\ & \mathrm{I}_{\mathrm{F}}=20 \mathrm{~A}, \mathrm{~T}_{\mathrm{J}}=+25^{\circ} \mathrm{C} \end{aligned}$ |
| Leakage Current (Note 7) | $\mathrm{I}_{\mathrm{R}}$ | - | 0.144 - | $\begin{gathered} 0.500 \\ 100 \end{gathered}$ | mA | $\begin{aligned} & \mathrm{V}_{\mathrm{R}}=50 \mathrm{~V}, \mathrm{~T}_{\mathrm{J}}=+25^{\circ} \mathrm{C} \\ & \mathrm{~V}_{\mathrm{R}}=50 \mathrm{~V}, \mathrm{~T}_{\mathrm{J}}=+125^{\circ} \mathrm{C} \end{aligned}$ |
| Total Capacitance | $\mathrm{C}_{\text {T }}$ | - | 350 | - | pF | $\mathrm{V}_{\mathrm{R}}=50 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ |
| Reverse Recovery Time | $t_{\text {rr }}$ | - | 48 | - | ns | $\begin{aligned} & \mathrm{I}_{\mathrm{F}}=0.5 \mathrm{~A}, \mathrm{I}_{\mathrm{R}}=1.0 \mathrm{~A}, \\ & \mathrm{I}_{\mathrm{rr}}=0.25 \mathrm{~A}, \mathrm{RG} 1 \end{aligned}$ |

Notes: 6. Device mounted on AI substrate PCB ( $30 \mathrm{~mm} * 30 \mathrm{~mm}$ ) with additional heat sink (AI $48 \mathrm{~mm} * 35 \mathrm{~mm}$ * 80 mm )
7. Short duration pulse test used to minimize self-heating effect.



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SBRT20U50SLPQ


Figure 3 Typical Reverse Characteristics


Figure 4 Total Capacitance vs. Reverse Voltage

Figure 5 Maximum Avalanche Power Curve


## Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.


| POWERDI5060-8 |  |  |  |
| :---: | :---: | :---: | :---: |
| Dim | Min | Max | Typ |
| A | 0.90 | 1.10 | 1.00 |
| A1 | 0.00 | 0.05 | - |
| b | 0.33 | 0.51 | 0.41 |
| b2 | 0.200 | 0.350 | 0.273 |
| b3 | 0.40 | 0.80 | 0.60 |
| c | 0.230 | 0.330 | 0.277 |
| D | 5.15 BSC |  |  |
| D1 | 4.70 | 5.10 | 4.90 |
| D2 | 3.70 | 4.10 | 3.90 |
| D3 | 3.90 | 4.30 | 4.10 |
| E | 6.15 BSC |  |  |
| E1 | 5.60 | 6.00 | 5.80 |
| E2 | 3.28 | 3.68 | 3.48 |
| E3 | 3.99 | 4.39 |  |
| e | 1.27 BSC |  |  |
| G | 0.51 | 0.71 | 0.61 |
| K | 0.51 | - | - |
| L | 0.51 | 0.71 | 0.61 |
| L1 | 0.050 | 0.20 | 0.175 |
| M | 3.235 | 4.035 | 3.635 |
| M1 | 1.00 | 1.40 | 1.21 |
| O | $10^{\circ}$ | $12^{\circ}$ |  |
| 01 | $66^{\circ}$ | $8^{\circ}$ | $7^{\circ}$ |
| All Dimensions in | mm |  |  |

## Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.


| Dimensions | Value (in mm) |
| :---: | :---: |
| $\mathbf{C}$ | 1.270 |
| $\mathbf{G}$ | 0.660 |
| $\mathbf{G 1}$ | 0.820 |
| $\mathbf{X}$ | 0.610 |
| $\mathbf{X 1}$ | 4.100 |
| $\mathbf{X 2}$ | 0.755 |
| $\mathbf{X 3}$ | 4.420 |
| $\mathbf{X 4}$ | 5.610 |
| $\mathbf{Y}$ | 1.270 |
| $\mathbf{Y 1}$ | 0.600 |
| $\mathbf{Y 2}$ | 1.020 |
| $\mathbf{Y 3}$ | 0.295 |
| $\mathbf{Y 4}$ | 1.825 |
| $\mathbf{Y 5}$ | 3.810 |
| $\mathbf{Y 6}$ | 0.180 |
| $\mathbf{Y 7}$ | 6.610 |

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