



ZXGD3006E6

#### **40V 10A GATE DRIVER IN SOT26**

### **Description**

ZXGD3006E6 is a 40V Gate Driver for switching IGBTs and SiC MOSFETs. It can transfer up to 10A peak source/sink current into the gate for effective charging and discharging of a large capacitive load.

The ZXGD3006E6 can drive typically 4A into the low gate impedance of an IGBT, with just 1mA input from a controller. Also, the turn-on and turn-off switching behavior of the IGBT can be individually tailored to suit an application. In particular, by defining the switching characteristics appropriately, EMI and cross conduction problems can be reduced.

#### **Applications**

Gate driving IGBTs and SiC MOSFETs in:

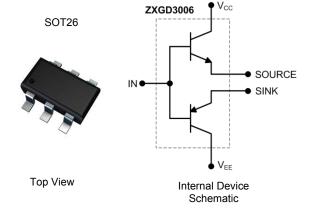
- DC-DC Converters in Electric Cars
- Automotive Active Suspension Systems
- Solar Inverters
- Power Supplies
- Plasma Display Panel Power Modules

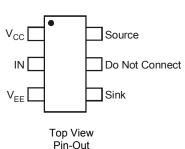
# Features

- High-gain buffer with typically 4A output from 1mA input
- 40V supply for +20V to -18V gate driving to prevent dV/dt induced false triggering
- Emitter-follower that is rugged to latch-up / shoot-through issues, and delivers <10ns propagation delay time</li>
- Separate source and sink outputs for independent control of IGBT turn-on and turn-off times
- Optimized pin-out to simplify PCB layout and reduce parasitic trace inductances
- Near-zero quiescent supply current
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)
- Halogen and Antimony free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP capable (Note 4)

### **Mechanical Data**

- Case: SOT26
- Case material: molded plastic. "Green" molding compound.
- UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208@3
- Weight: 0.018 grams (approximate)





Pin Name	Pin Function	
V <sub>CC</sub>	Supply Voltage High	
IN	Driver Input Pin	
V <sub>EE</sub>	Supply Voltage Low	
SOURCE	Source Current Output	
SINK	Sink Current Output	

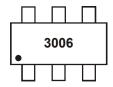
### Ordering Information (Notes 4 & 5)

Product	Compliance	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXGD3006E6TA	AEC-Q101	3006	7	8	3,000
ZXGD3006E6QTA	Automotive	3006	7	8	3,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen and Antimony free, "Green" and Lead-Free.
- 3. Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified.
- 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

## **Marking Information**

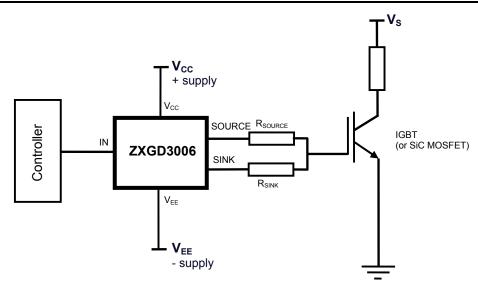


3006 = Product Type Marking Code





## **Typical Application Circuit**



## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Supply voltage, with respect to V <sub>EE</sub>	V <sub>CC</sub>	40	٧
Input voltage, with respect to V <sub>EE</sub>	V <sub>IN</sub>	40	V
Output difference voltage (Source – Sink)	$\Delta V_{(source-sink)}$	±7.5	V
Peak output current	I <sub>PK</sub>	±10	Α
Input current	I <sub>IN</sub>	±100	mA

# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Power Dissipation (Notes 6 & 7)	PD	1.1	W	
Linear derating factor	гр	8.8	mW/°C	
Thermal Resistance, Junction to Ambient (Notes 6 & 7)	$R_{ heta JA}$	113	°C/W	
Thermal Resistance, Junction to Lead (Note 8)	$R_{ heta JL}$	105		
Operating and Storage Temperature Range	$T_{J_i} T_{STG}$	-55 to +150	°C	

### ESD Ratings (Note 9)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	≥ 1,500	V	1C
Electrostatic Discharge – Charged Device Model	ESD CDM	≥ 1,000	V	IV

Notes:

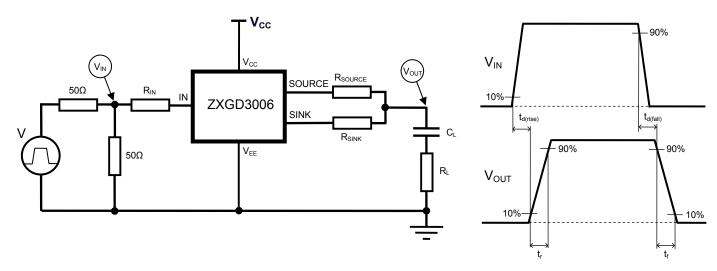
- 6. For a device mounted on 25mm x 25mm 1oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in a steady-state. The heatsink is split in half with the pin 1 (V<sub>CC</sub>) and pin 3 (V<sub>EE</sub>) connected separately to each half.
- 7. For device with two active die running at equal power.
- The main resistance from junction to solder-point at the end of each lead on pin 1 (V<sub>CC</sub>) and pin 3 (V<sub>EE</sub>).
   Refer to JEDEC specification JESD22-A114 and JESD22-C101.



# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit		Test Condition
Output voltage, high	V <sub>OUT(hi)</sub>	V <sub>CC</sub> - 1.0	V <sub>CC</sub> - 0.8	_	V	V <sub>IN</sub> = V <sub>CC</sub>	C <sub>L</sub> = 1nF
Output voltage, low	V <sub>OUT(low)</sub>	_	V <sub>EE</sub> + 0.12	V <sub>EE</sub> + 0.3	V	V <sub>IN</sub> = V <sub>EE</sub>	$R_{SOURCE} = 0\Omega$ , $R_{SINK} = 0\Omega$
Supply breakdown voltage	BV <sub>CC</sub>	40	_	_	V :	$I_Q = 100 \mu A, V_{IN} = V_{CC}$	
Supply breakdown voltage		40	_	_		$I_Q = 100 \mu A, V$	<sub>IN</sub> = V <sub>EE</sub> = 0V
Quiescent supply current	1-	_	1	50	nA	V <sub>CC</sub> = 30V, V <sub>I</sub>	<sub>V</sub> = V <sub>CC</sub>
Quiescent supply current	IQ	_	1	50	ПА	V <sub>CC</sub> = 30V, V <sub>IN</sub>	= V <sub>EE</sub> = 0V
Source current	I <sub>(source)</sub>	_	4.0	l	Α	V <sub>CC</sub> = 5V, I <sub>IN</sub> =	1mA, V <sub>OUT</sub> = 0V
Sink current	I <sub>(sink)</sub>	_	3.8		^	V <sub>CC</sub> = 5V, I <sub>IN</sub> =-	1mA, V <sub>OUT</sub> = 5V
Source current with varying input resistances	I <sub>(source)</sub>	_	6.4 5.5 3.9 2.2 0.44	l	Α	$R_{\text{IN}} = 200\Omega$ $R_{\text{IN}} = 1k\Omega$ $R_{\text{IN}} = 10k\Omega$ $R_{\text{IN}} = 100k\Omega$ $R_{\text{IN}} = 1000k\Omega$	$V_{CC}$ = 15V, $V_{EE}$ = 0V $V_{IN}$ = 15V $C_{L}$ = 100nF, $R_{L}$ = 0.18 $\Omega$ $R_{SOURCE}$ = 0 $\Omega$ , $R_{SINK}$ = 0 $\Omega$
Sink current with varying input resistances	I <sub>(sink)</sub>	_	7.7 6.5 4.4 2.3 0.46	1	А	$R_{IN} = 200\Omega$ $R_{IN} = 1k\Omega$ $R_{IN} = 10k\Omega$ $R_{IN} = 100k\Omega$ $R_{IN} = 1000k\Omega$	$V_{CC}$ = 15V, $V_{EE}$ = 0V $V_{IN}$ = 15V $C_{L}$ = 100nF, $R_{L}$ = 0.18 $\Omega$ $R_{SOURCE}$ = 0 $\Omega$ , $R_{SINK}$ = 0 $\Omega$
Switching times with low load capacitance $C_L = 10$ nF	$t_{\text{d(rise)}} \\ t_{\text{r}} \\ t_{\text{d(fall)}} \\ t_{\text{f}}$	_	8 48 16 35	1	ns	$V_{CC}$ = 15V, $V_{E}$ $V_{IN}$ = 0 to 15V $R_{IN}$ = 1k $\Omega$ $C_{L}$ = 10nF, $R_{L}$ RSOURCE = 0 $\Omega$	= 0.18Ω
Switching times with high load capacitance $C_L$ = 100nF	$t_{ m d(rise)}$ $t_{ m r}$ $t_{ m d(fall)}$ $t_{ m f}$	_	46 419 47 467		ns	$V_{CC} = 15V, V_{E}$ $V_{IN} = 0 \text{ to } 15V$ $R_{IN} = 1k\Omega$ $C_{L} = 100nF, R$ $R_{SOURCE} = 0\Omega$	<sub>L</sub> = 0.18Ω
Switching times with asymmetric source and sink resistors	$t_{ m d(rise)}$ $t_{ m r}$ $t_{ m d(fall)}$ $t_{ m f}$	_	27 208 11 53	1	ns	$V_{CC} = 20V, V_{E}$ $V_{IN} = -18 \text{ to } 20$ $R_{IN} = 1k\Omega$ $C_{L} = 10nF, R_{L}$ $R_{SOURCE} = 4.7$	V = 0.18Ω

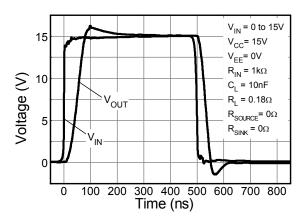
# **Switching Test Circuit and Timing Diagram**





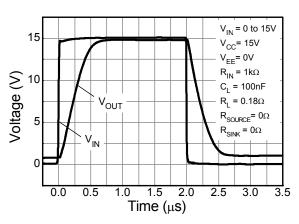


## Typical Switching Characteristics (@TA = +25°C, unless otherwise specified.)



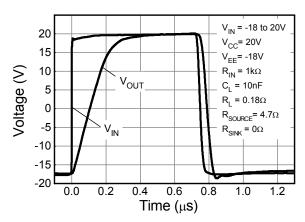
**Switching Speed** 

Low Load Capacitance  $C_I = 10nF$ 



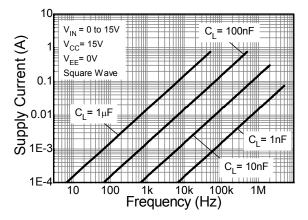
**Switching Speed** 

High Load Capacitance  $C_I = 100nF$ 



Switching Speed

Asymmetric Source and Sink Resistors

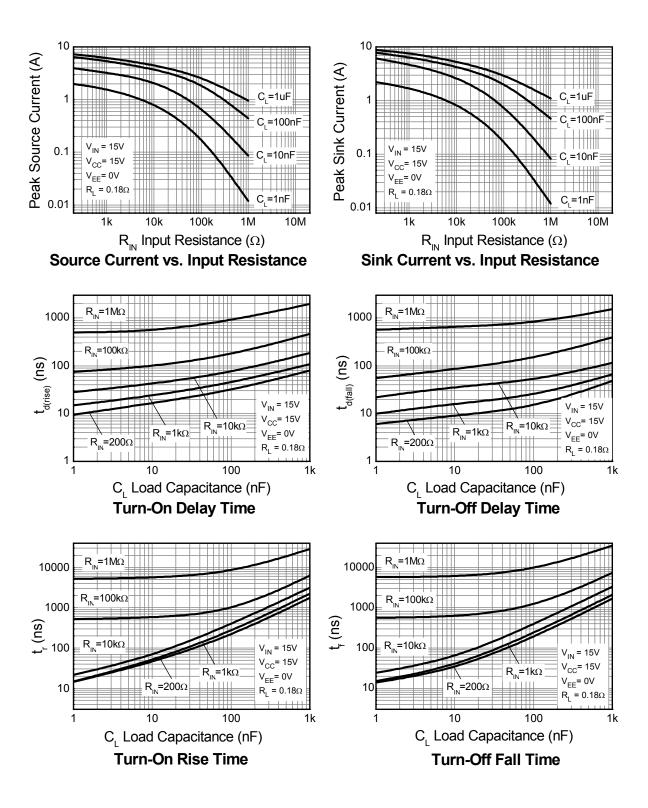


**Supply Current** 





### Typical Switching Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)



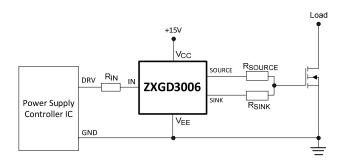


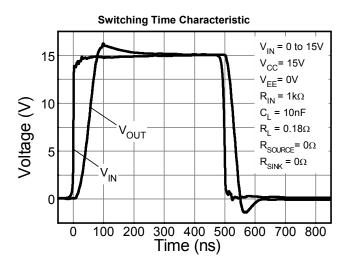


### **Circuit Examples**

### ZXGD3006 driving a MOSFET

Application example of the ZXGD3006 driving the gate of a MOSFET from 0 to +15V with R\_{SOURCE} =  $R_{SINK}$  =  $0\Omega$ 

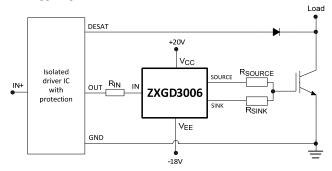


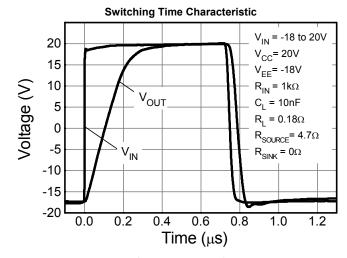


Symmetric Source and Sink Resistors

### **ZXGD3006 driving an IGBT**

Application example of ZXGD3006 driving the gate of an IGBT with independent  $t_{on}$  and  $t_{off}$  using asymmetric  $R_{SOURCE}$  and  $R_{SINK}$  In addition, the gate is driven negative to -18V to prevent dV/dt induced false triggering.





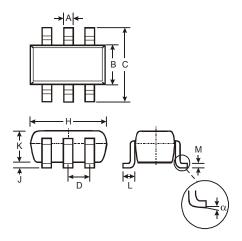
Asymmetric Source and Sink Resistors





# **Package Outline Dimensions**

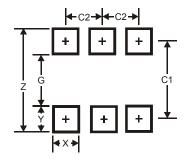
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SOT26					
Dim	Min	Max	Тур		
Α	0.35	0.50	0.38		
В	1.50	1.70	1.60		
С	2.70	3.00	2.80		
D	_		0.95		
Н	2.90	3.10	3.00		
J	0.013	0.10	0.05		
K	1.00	1.30	1.10		
L	0.35	0.55	0.40		
M	0.10	0.20	0.15		
α	0°	8°			
All Dimensions in mm					

# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	3.20
G	1.60
X	0.55
Υ	0.80
C1	2.40
C2	0.95





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