



#### **100V N-CHANNEL ENHANCEMENT MODE MOSFET**

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(on)</sub>	I <sub>D</sub> T <sub>A</sub> = +25°C
1001/	$350 \mathrm{m}\Omega @ \mathrm{V}_{\mathrm{GS}} = 10 \mathrm{V}$	2.4A
100V	$450m\Omega @ V_{GS} = 6.0V$	2.1A

## **Description and Applications**

This MOSFET is designed to minimize the on-state resistance and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Motor Control
- DC-DC Converters
- Power Management Functions
- Uninterrupted Power Supply

#### **Features and Benefits**

- Fast Switching Speed
- Low Gate Drive
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

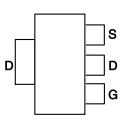
#### **Mechanical Data**

- Case: SOT223
- Case Material: Molded Plastic; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe; Solderable per MIL-STD-202, Method 208
- Weight: 0.112 grams (Approximate)

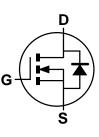


SOT223

Top View



Pin Out - Top View



Equivalent Circuit

#### Ordering Information (Note 4)

Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
FIOUUCI	Marking	Reel size (inches)	Tape width (min)	Quantity per reel
ZXMN10A11GTA	See Below	7	12	1,000

1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.

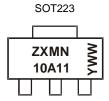
2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green"

Notes:

and Lead-free. 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

# **Marking Information**



ZXMN10A11 = Product Type Marking Code YWW = Date Code Marking Y or  $\overline{Y}$  = Last Digit of Year (ex: 5= 2015) WW or  $\overline{W}W$  = Week Code (01~53)



# Maximum Ratings (@T<sub>A</sub> = +25°C unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V <sub>DSS</sub>	100	V
Gate-Source Voltage			V <sub>GS</sub>	±20	V
		(Note 6)		2.4	
Continuous Drain Current	$V_{GS} = 10V$	$T_{A} = +70^{\circ}C$ (Note 6)	ID	1.9	А
		(Note 5)		1.7	
Pulsed Drain Current	V <sub>GS</sub> = 10V	(Note 7)	I <sub>DM</sub>	7.9	А
Continuous Source Current (Body Diode) (Note 6)		(Note 6)	Is	4.6	А
Pulsed Source Current (Body Diode) (Note 7)		I <sub>SM</sub>	7.9	А	

# Thermal Characteristics (@T<sub>A</sub> = +25°C unless otherwise specified.)

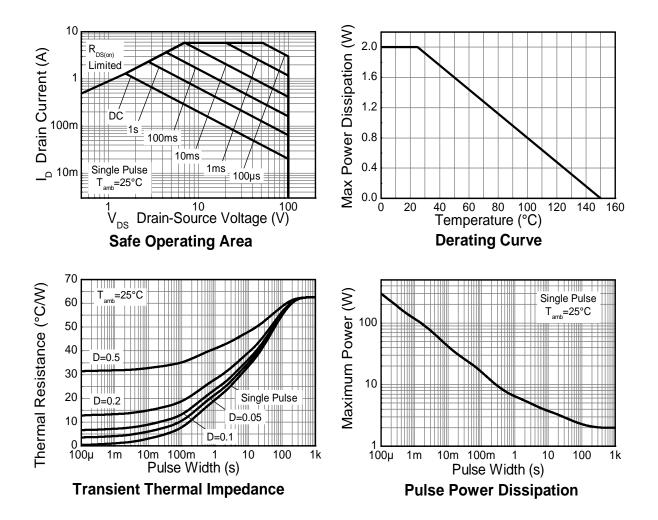
Characteristic	Symbol	Value	Unit		
Power Dissipation	(Note 5)		2.0 16	W	
Linear Derating Factor	(Note 6)	P <sub>D</sub>	3.9 31	mW/°C	
Thermal Resistance, Junction to Ambient	(Note 5)		62.5	°C/W	
Thermal Resistance, Junction to Ambient	(Note 6)	R <sub>0JA</sub>	32.0	C/VV	
Thermal Resistance, Junction to Lead	(Note 8)	$R_{ ext{ heta}JL}$	9.8	°C/W	
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	°C	

5. For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.
6. Same as Note 5, except the device is measured at t ≤ 10 seconds.
7. Same as Note 5, except the device is pulsed with D = 0.02 and pulse width 300µs. The pulse current is limited by the maximum junction temperature.
9. The pulse reacting the device is pulsed with D = 0.02 and pulse width 300µs. The pulse current is limited by the maximum junction temperature. Notes:

8. Thermal resistance from junction to solder-point (at the end of the drain lead).



# **Thermal Characteristics**





## Electrical Characteristics (@T<sub>A</sub> = +25°C unless otherwise specified.)

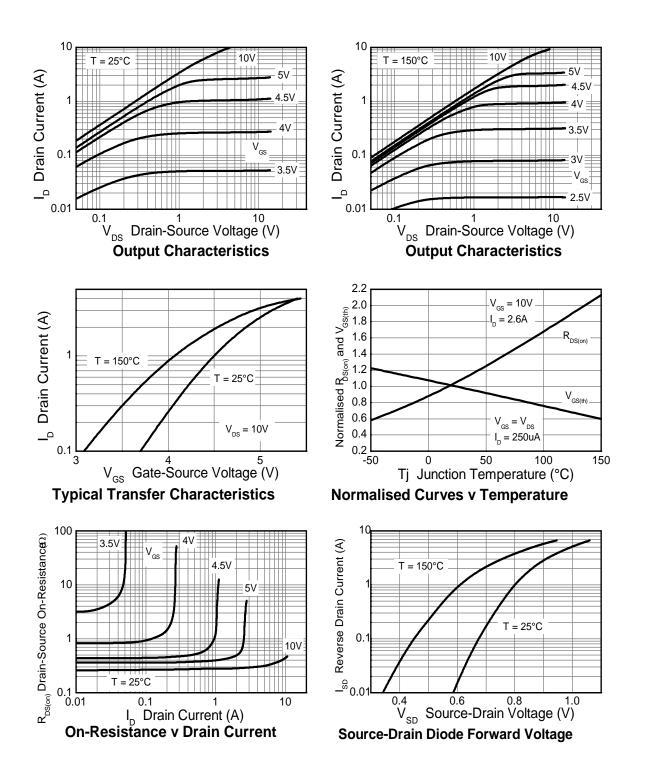
Characteristic	Symbol	Min	Тур	Max	Unit	Test	Condition
OFF CHARACTERISTICS				•	•		
Drain-Source Breakdown Voltage	<b>BV</b> <sub>DSS</sub>	100	_	_	V	$I_D = 250 \mu A, V_{GS} = 0 V$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_		1	μA	V <sub>DS</sub> = 100V, V	<sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS							
Gate Threshold Voltage	V <sub>GS(th)</sub>	2.0		4.0	V	I <sub>D</sub> = 250μA, V <sub>I</sub>	os = V <sub>GS</sub>
Static Drain-Source On-Resistance (Note 9)	D			0.35	Ω	$V_{GS}$ = 10V, $I_D$	= 2.6A
Static Drain-Source On-Resistance (Note 9)	R <sub>DS (ON)</sub>	_		0.45		$V_{GS} = 6V, I_D =$	1.3A
Forward Transconductance (Notes 9 & 10)	<b>g</b> fs	_	4		S	V <sub>DS</sub> = 15V, I <sub>D</sub> = 2.6A	
Diode Forward Voltage (Note 9)	V <sub>SD</sub>	_	0.85	0.95	V	I <sub>S</sub> = 1.85A, V <sub>GS</sub> = 0V	
Reverse Recovery Time (Note 10)	t <sub>rr</sub>		26		ns	I <sub>F</sub> = 1.0A, di/dt = 100A/µs	
Reverse Recovery Charge (Note 10)	Qrr	_	30	_	nC		
DYNAMIC CHARACTERISTICS (Note 6)	·				•	•	
Input Capacitance	Ciss		274	—	pF	V <sub>DS</sub> = 50V, V <sub>GS</sub> = 0V f = 1MHz	
Output Capacitance	C <sub>oss</sub>	_	21	_	pF		
Reverse Transfer Capacitance	C <sub>rss</sub>	_	11		pF		
Total Gate Charge (Note 11)	Qg	_	3.5		nC	$V_{GS} = 6.0V$	
Total Gate Charge (Note 11)	Qg	_	5.4	_	nC		$V_{DS} = 50V$
Gate-Source Charge (Note 11)	Q <sub>gs</sub>	_	1.4	_	nC	V <sub>GS</sub> = 10V	$I_D = 2.5A$
Gate-Drain Charge (Note 11)	Q <sub>gd</sub>	_	1.5		nC	7	
Turn-On Delay Time (Note 11)	t <sub>D(on)</sub>	_	2.7		ns		
Turn-On Rise Time (Note 11)	tr		1.7		ns	$V_{DD} = 50V, V_{GS} = 10V$ $I_D = 1A, R_G \cong 6.0\Omega$	
Turn-Off Delay Time (Note 11)	t <sub>D(off)</sub>	_	7.4	_	ns		
Turn-Off Fall Time (Note 11)	t <sub>f</sub>	_	3.5	_	ns		

Notes:

9. Measured under pulsed conditions. Pulse width ≤ 300µs; duty cycle ≤ 2%.
 10. For design aid only, not subject to production testing.
 11. Switching characteristics are independent of operating junction temperatures.

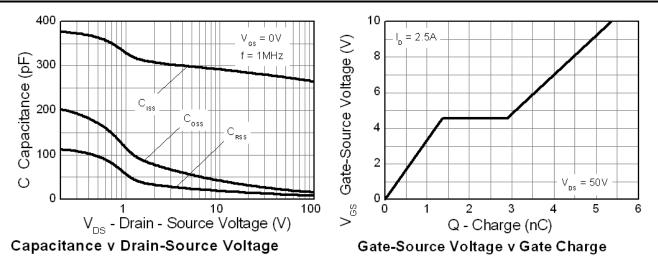


# **Typical Characteristics**

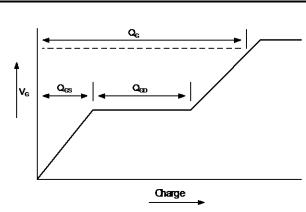




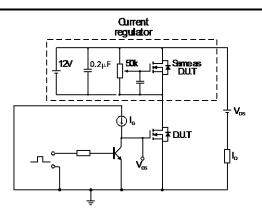
# Typical Characteristics (cont.)



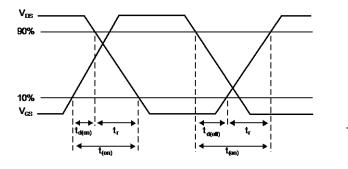
**Test Circuits** 



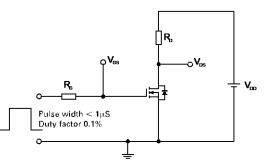
Basic gate charge waveform



Gate charge test circuit



Switching time waveforms

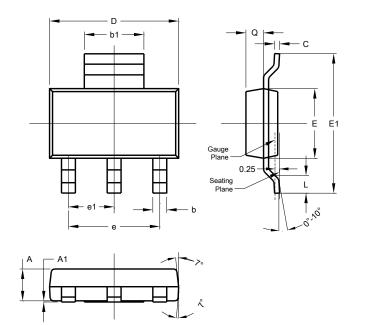


Switching time test circuit



# Package Outline Dimensions

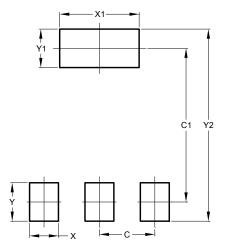
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



SOT223					
Dim	Min	Max	Тур		
Α	1.55	1.65	1.60		
A1	0.010	0.15	0.05		
b	0.60	0.80	0.70		
b1	2.90	3.10	3.00		
С	0.20	0.30	0.25		
D	6.45	6.55	6.50		
ш	3.45	3.55	3.50		
E1	6.90	7.10	7.00		
e	-	-	4.60		
e1	-	-	2.30		
L	0.85	1.05	0.95		
q	0.84	0.94	0.89		
All Dimensions in mm					

# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	2.30
C1	6.40
Х	1.20
X1	3.30
Y	1.60
Y1	1.60
Y2	8.00



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