



ZXMN3A01E6

Product Summary

V _{(BR)DSS}	RDS(ON) Max	I _D T _A = +25°C
30V	0.12Ω @ V _{GS} = 10V	3.0A
	$0.18\Omega @ V_{GS} = 4.5V$	2.5A

Description

This MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$ and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

- DC-DC Converters
- Power Management Functions
- Backlighting

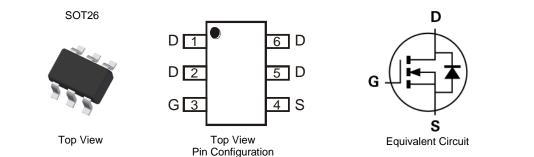
30V N-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

- Low Input Capacitance
- Low On-Resistance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: SOT26
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Tin Finish Annealed over Copper Leadframe; Solderable per MIL-STD-202, Method 208 (£3)
- Weight: 0.018 grams (Approximate)



Ordering Information (Note 4)

Part Number	Case	Packaging
ZXMN3A01E6TA	SOT26	3,000/Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

	SC	212	26		
3	8A1			ΥM	
Т					

00700

3A1 = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: C = 2015) M or \overline{M} = Month (ex: 9 = September)

Date Code Key

Notes:

Eale eeae hoj												
Year	2015		2016	2017		2018	2019		2020	2021		2022
Code	С		D	E		F	G		Н			J
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage		V _{DSS}	30	V	
Gate-Source Voltage	V _{GSS}	±20	V		
Continuous Drain Current, V _{GS} = 10V	Steady State	$T_A = +25^{\circ}C \text{ (Note 6)}$ $T_A = +70^{\circ}C \text{ (Note 6)}$ $T_A = +25^{\circ}C \text{ (Note 5)}$	ID	3.0 2.4 2.4	А
Maximum Body Diode Forward Current (Note 6)		I _S	2.4	A	
Pulsed Drain Current (Note 7)	I _{DM}	10	A		
Pulsed Source Current (Note 7)	I _{SM}	10	A		

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Total Power Dissipation Linear Derating Factor	T _A = +25°C (Note 5)	PD	1.1 8.8	W mW/°C
Total Power Dissipation Linear Derating Factor	T _A = +25°C (Note 6)	PD	1.7 13.6	W mW/°C
Thermal Resistance, Junction to Ambient	Steady State (Note 5)	Devi	113	°C/W
Thermal Resistance, Junction to Ambient	Steady State (Note 6)	R _{0JA}	70	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 9)	1 - 1				•	
Drain-Source Breakdown Voltage	BV _{DSS}	30	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current	IDSS			0.5	μA	$V_{DS} = 30V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}			±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 9)	-					
Gate Threshold Voltage	V _{GS(TH)}	1.0	_	_	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$
Static Drain-Source On-Resistance (Note 8)	D		0.106	0.12	Ω	$V_{GS} = 10V, I_D = 2.5A$
Static Drain-Source On-Resistance (Note 6)	R _{DS(ON)}		_	0.18		V _{GS} = 4.5V, I _D = 2.0A
Diode Forward Voltage (Note 8)	V _{SD}		0.84	0.95	V	V _{GS} = 0V, I _S = 1.7A
Forward Transconductance (Notes 8 & 10)	g fs	_	3.5	_	S	V _{DS} = 4.5V, I _D = 2.5A
DYNAMIC CHARACTERISTICS (Note 10)	•			•	•	•
Input Capacitance	C _{iss}		190	_		$V_{DS} = 25V, V_{GS} = 0V$ f = 1.0MHz
Output Capacitance	Coss	_	38	_	pF	
Reverse Transfer Capacitance	Crss	_	20	_		
Total Gate Charge (V _{GS} = 5.0V)	Qg		2.3	—		
Total Gate Charge (V _{GS} = 10V)	Qg		3.9	—	nC	
Gate-Source Charge	Q _{gs}	_	0.6	_		V _{DS} = 15V, I _D = 2.5A
Gate-Drain Charge	Q _{gd}	_	0.9	_		
Turn-On Delay Time	t _{D(ON)}		1.7	—		
Turn-On Rise Time	t _R		2.3	—	ns	$V_{GS} = 10V, V_{DD} = 15V, R_G = 6.0\Omega,$
Turn-Off Delay Time	t _{D(OFF)}	_	6.6	_	115	I _D = 2.5A
Turn-Off Fall Time	t _F		2.9	_]	
Body Diode Reverse Recovery Time	t _{RR}		17.7		ns	I _F = 2.5A, dl/dt = 100A/µs
Body Diode Reverse Recovery Charge	Q _{RR}	_	13.0	—	nC	$\gamma_{1} = 2.5A, u/u_1 = 100A/\mu_5$

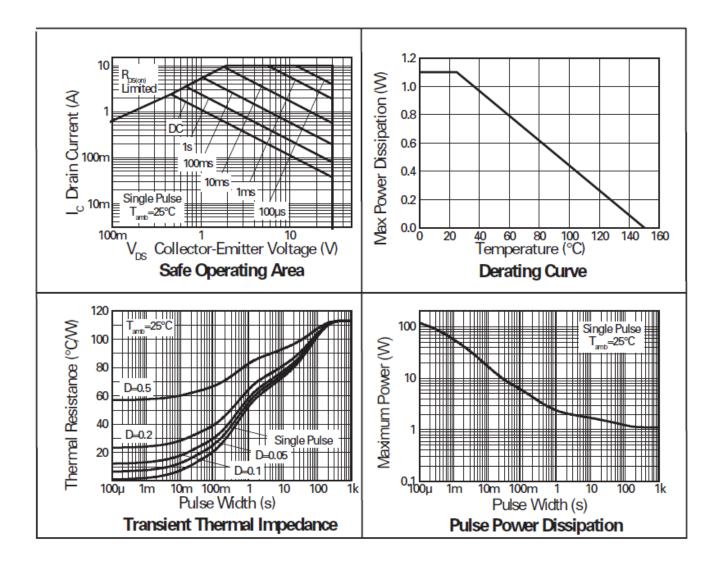
Notes: 5. For a device surface mounted on 25mm x 25mm FR-4 PCB with high coverage of single sided 1oz copper, in still air conditions.

For a device surface mounted on FR-4 PCB measured at t ≤5 secs.
For a device surface mounted on FR-4 PCB measured at t ≤5 secs.
Repetitive rating 25mm x 25mm FR-4 PCB, D = 0.05, pulse width 10µs - pulse width limited by maximum junction temperature. Refer to Transient Thermal Impedance graph.
Measured under pulsed conditions. Width=300µs. Duty cycle ≤ 2%.
Short duration pulse test used to minimize self-heating effect.
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10. Guaranteed by design. Not subject to product testing.

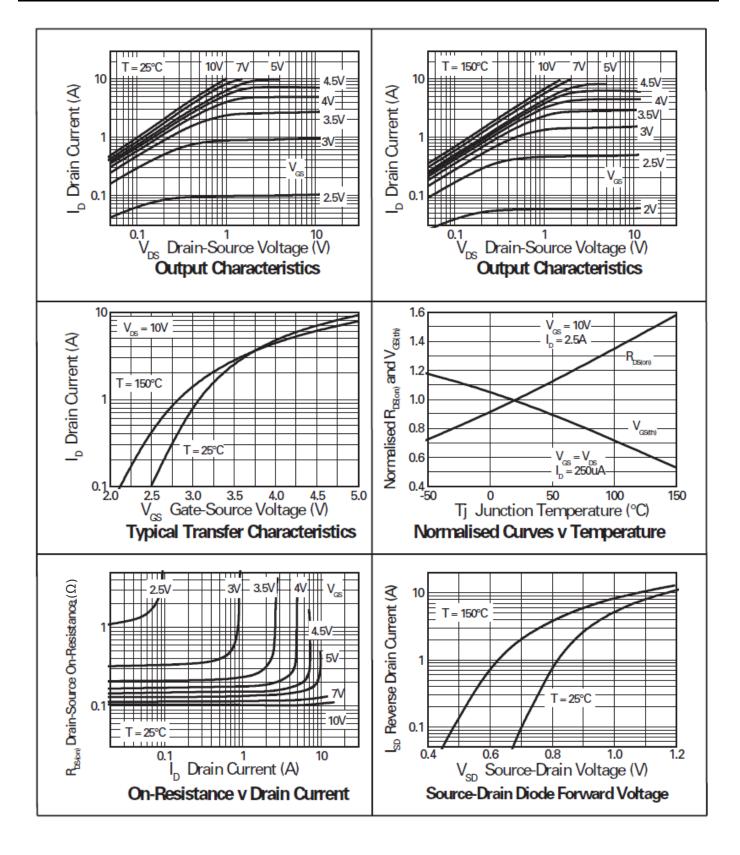


Typical Characteristics



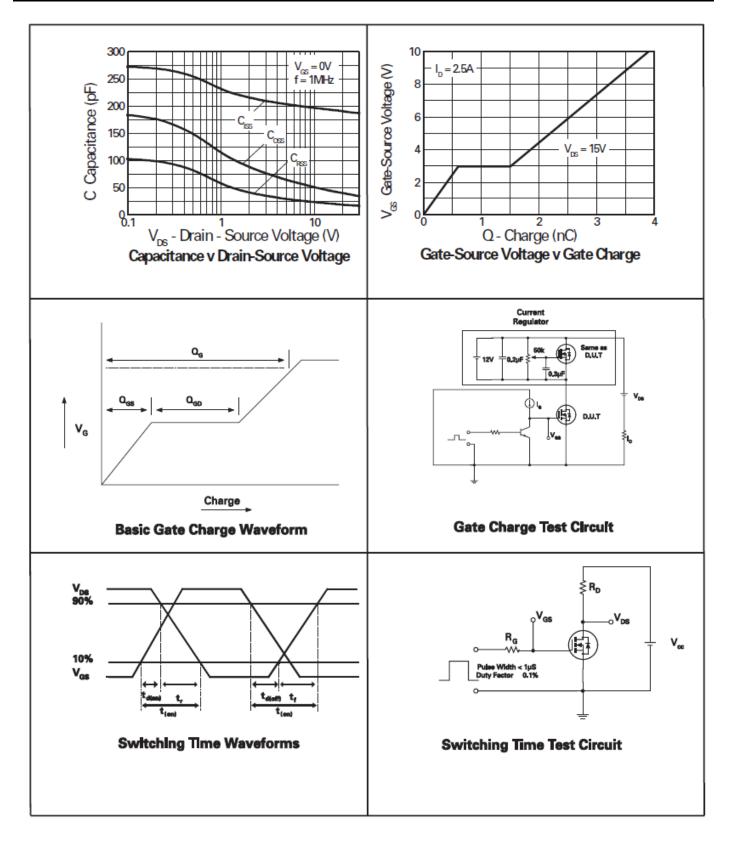


Typical Characteristics (cont.)





Typical Characteristics (cont.)





Тур

0.05

1.10

0.75

0.38

0.15

3.00

0.95

1.90

2.80

1.60

0.40

8°

7°

1.30

0.80

3.10

3.00

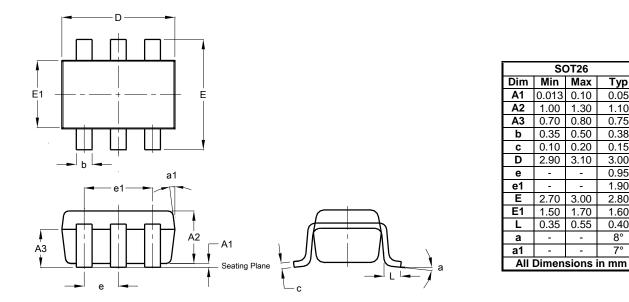
1.70

0.55

-

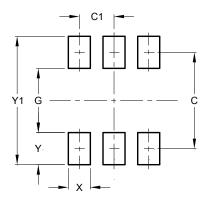
Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	2.40
C1	0.95
G	1.60
Х	0.55
Y	0.80
Y1	3.20



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