



60V SOT223 N-channel enhancement mode MOSFET

Product Summary

BV _{DSS}	R _{DS(on)} (Ω)	I _D (A)	
60V	0.08 @ V _{GS} = 10V	5.3	
	0.15 @ V _{GS} = 4.5V	2.8	

Description and Applications

This MOSFET features a unique structure combining the benefits of low on-resistance and fast switching, making it ideal for highefficiency power management applications.

- DC-DC Converters
- Power Management Functions
- Disconnect Switches
- Motor Control

Features and Benefits

- Low On-Resistance
- Fast Switching Speed
- Low Threshold
- Low Gate Drive
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

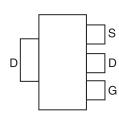
Mechanical Data

- Case: SOT223
- Case Material: Molded Plastic.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Solderable per MIL-STD-202, Method 208
- Weight: 0.112 grams (Approximate)

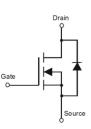


SOT223

Top View



Pin Out - Top View



Equivalent Circuit

Ordering Information (Note 4)

Part Number	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXMN6A08GTA	ZXMN6A08	7	12	1,000
ZXMN6A08GTC	ZXMN6A08	13	12	4,000

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

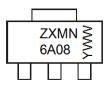
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

SOT223



 $\begin{array}{l} \mbox{ZXMN6A08} = \mbox{Product Type Marking Code} \\ \mbox{YWW} = \mbox{Date Code Marking} \\ \mbox{Y or } \overline{Y} = \mbox{Last Digit of Year (ex: 5 = 2015)} \\ \mbox{WW or } \overline{WW} = \mbox{Week Code (01 - 53)} \\ \end{array}$



Absolute Maximum Ratings

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage		V _{DSS}	60	V
Gate-Source Voltage		V _{GSS}	±20	V
	T _A = +25 °C (Note 6)		5.3	А
Continuous Drain Current V _{GS} = 10V	T _A = +70 °C (Note 6)	Ι _D	4.2	А
	T _A = +25 °C (Note 5)		3.8	А
Pulsed Drain Current (Note 7)	I _{DM}	20	А	
Continuous Source Current (body diode)(Note 6)		Is	2.1	А
Pulsed Source Current (body diode)(Note 7)		I _{SM}	20	А
Power Dissipation at $T_A = +25 ^{\circ}$ C (Note 5) Linear Derating Factor		PD	2 16	W mW/℃
Power Dissipation at T _A = +25 °C (Note 6) Linear Derating Factor		PD	3.9 31	W mW/℃
Linear Derating Factor		TJ, T _{STG}	-55 to +150	°C

Thermal Characteristics (@T_A = +25 °C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Junction to Ambient (Note 5)	R _{0JA}	62.5	°C/W
Junction to Ambient (Note 6)	R _{0JA}	32	°C/W

Electrical Characteristics (@T_A = +25 °C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	BV _{DSS}	60	-	-	V	$V_{GS} = 0V, I_D = 250 \mu A$	
Zero Gate Voltage Drain Current	IDSS	-	-	0.5	μA	$V_{DS} = 60V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	-	_	100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS							
Gate Threshold Voltage	V _{GS(th)}	1	-	-	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-State Resistance (Note 8)		-	_	0.08	Ω	$V_{GS} = 10V, I_D = 4.8A$	
	R _{DS} (ON)	-	-	0.15	Ω	V _{GS} = 4.5V, I _D = 4.2A	
Forward Transconductance (Notes 8 & 10)	g fs	-	6.6	_	S	V _{DS} =15V,I _D =4.8A	
DYNAMIC CHARACTERISTICS (Note 10)							
Input Capacitance	C _{iss}	-	459	-	pF		
Output Capacitance	Coss	-	44.2	-	pF	$V_{DS} = 40V, V_{GS} = 0V,$ f = 1MHz	
Reverse Transfer Capacitance	C _{rss}	-	24.1	-	pF		
Turn-On Delay Time (Note 9)	t _{D(on)}	_	2.6	-	ns		
Turn-On Rise Time (Note 9)	tr	-	2.1	-	ns	$V_{DD} = 30V, I_{D} = 1.5A$	
Turn-Off Delay Time (Note 9)	t _{D(off)}	-	12.3	-	ns	$RG \cong 6.0\Omega, V_{GS}= 10V$	
Turn-Off Fall Time (Note 9)	tf	_	4.6	_	ns		
Gate Charge (Note 9)	Qg	-	4.0	-	nC	V _{DS} = 30V, V _{GS} = 5V I _D = 1.4A	
Total Gate Charge (Note 9)	Qg	-	5.8	-	nC	V 00V/V 10V/	
Gate-Source Charge (Note 9)	Qgs	-	1.4	-	nC	V_{DS} = 30V, V_{GS} = 10V	
Gate Drain Charge (Note 9)	Qgd	-	1.9	-	nC	I _D = 1.4A	
SOURCE-DRAIN DIODE							
Diode Forward Voltage (Note 8)	V _{SD}	_	0.88	1.2	V	Tj=+25℃, I _S = 4A, V _{GS} =0V	
Reverse Recovery Time (Note 10)	trr	-	19.2	_	ns	Tj=+25℃, I _S = 1.4A,	
Reverse Recovery Charge (Note 10)	Qrr	-	30.3	-	nC	di/dt=100A/µs	

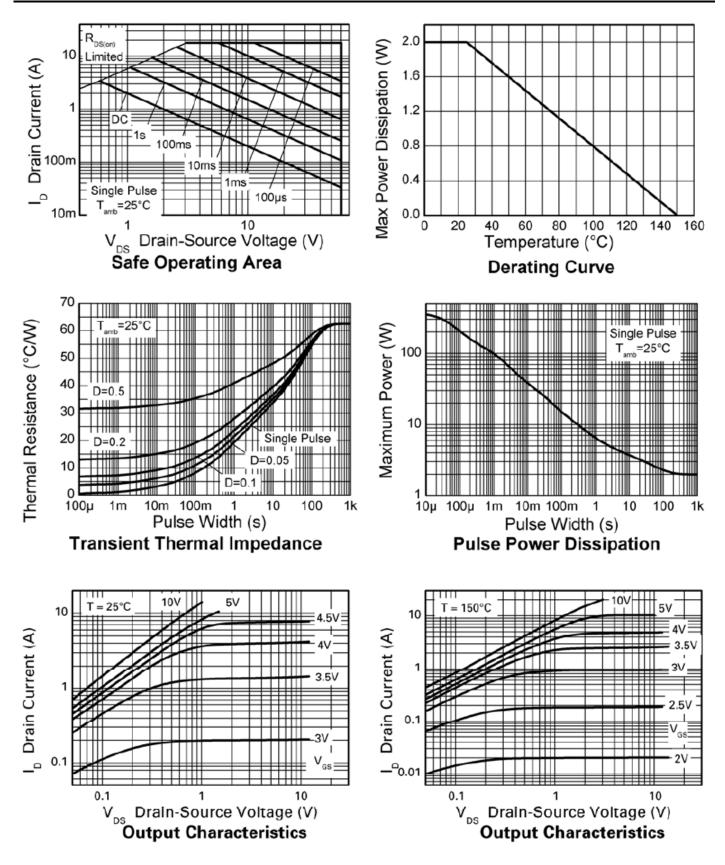
5. For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.
6. For a device surface mounted on FR4 PCB measured at t <= 10 sec.
7. Repetitive rating - 25mm x 25mm FR4 PCB, D=0.02, pulse width 300_s - pulse width limited by maximum junction temperature.
8. Measured under pulsed conditions. Pulse width <= 300_s; duty cycle <=2%.

9. Switching characteristics are independent of operating junction temperature.

10. For design aid only, not subject to production testing.

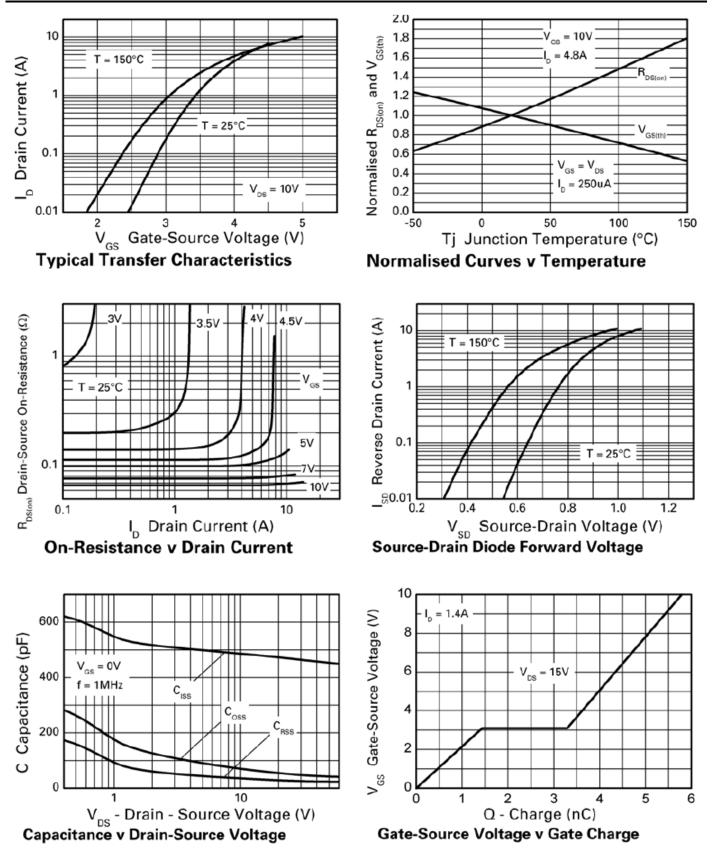






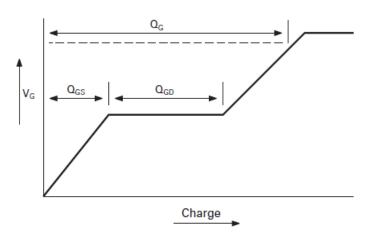


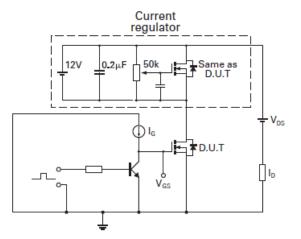
Typical Characteristics (continued)





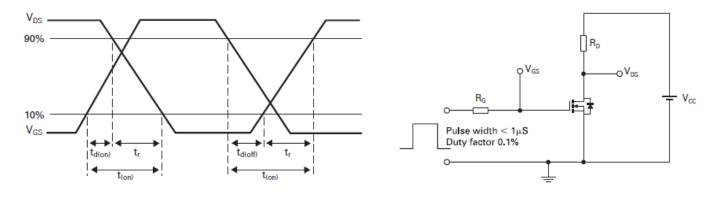
Test Circuits





Basic gate charge waveform

Gate charge test circuit



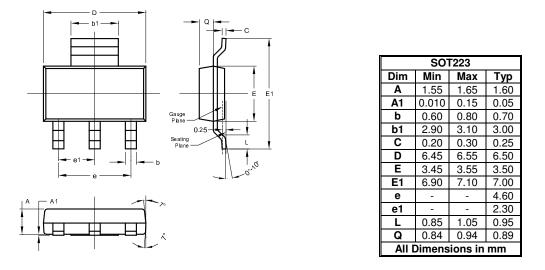
Switching time waveforms

Switching time test circuit



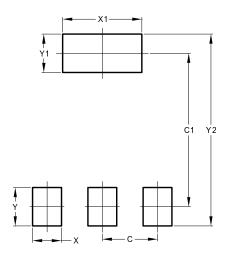
Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	2.30
C1	6.40
Х	1.20
X1	3.30
Y	1.60
Y1	1.60
Y2	8.00



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