



60V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(on)}	I _D T _A = +25°C	
-60V	125mΩ @ V _{GS} = -10V	-4.3A	
-60 V	190mΩ @ V _{GS} = -4.5V	-3.5A	

Description

This MOSFET is designed to minimize the on-state resistance and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

- Motor Control
- DC-DC Converters
- Power Management Functions
- Uninterrupted Power Supply

Features and Benefits

- Fast Switching Speed
- Low Gate Drive
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

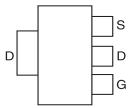
Mechanical Data

- Case: SOT223
- Case Material: Molded Plastic, UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208[®]
- Weight: 0.112 grams (Approximate)

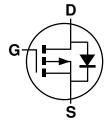




Top View



Pin Out - Top View



Equivalent Circuit

Ordering Information (Notes 4 & 5)

Part Number	Compliance	Case	Packaging
ZXMP6A17GQTA	Automotive	SOT223	1,000 / Tape & Reel

Note:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product_grade_definitions/.
- $5. \ For packaging \ details, go \ to \ our \ website \ at \ http://www.diodes.com/products/packages.html.$

Marking Information

SOT223

ZXMP6A17 = Product Type Marking Code YWW = Date Code Marking Y or Y = Year (ex: 5 = 2015) WW or WW = Week (01 - 53)



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage		V_{DSS}	-60	V	
Gate-Source Voltage		V_{GS}	±20	V	
		(Note 7)		-4.3	
Continuous Drain Current	$V_{GS} = 10V$	T _A = +70 °C (Note 7)	I_{D}	-3.5	Α
		(Note 6)		-3	
Pulsed Drain Current	$V_{GS} = 10V$	(Note 8)	I _{DM}	-13.7	Α
Continuous Source Current (Body Diode) (Note 7)		I _S	-4.8	Α	
Pulsed Source Current (Body Diode) (Note 8)		I _{SM}	-13.7	Α	

Thermal Characteristics (@T_A = +25 °C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Power Dissipation Linear Derating Factor	(Note 6)	D	2 16		
	(Note 7)	P _D	3.9 31	mW/°C	
Thermal Desistance Junction to Ambient	(Note 6)	В	62.5		
Thermal Resistance, Junction to Ambient	(Note 7)	R _{θJA}	32	°C/W	
Thermal Resistance, Junction to Lead	(Note 9)	R _{eJL}	9.8]	
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	∞	

Notes:

^{6.} For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.

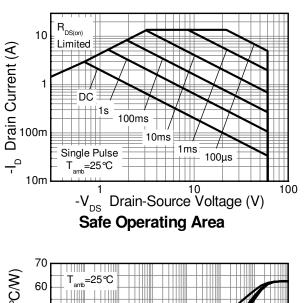
^{7.} Same as Note 6, except the device is measured at $t \le 10$ sec.

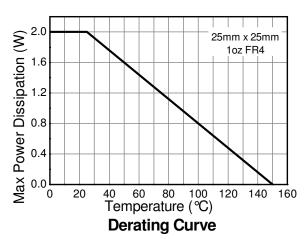
^{8.} Same as Note 6, except the device is pulsed with D = 0.02 and pulse width 300 µs. The pulse current is limited by the maximum junction temperature.

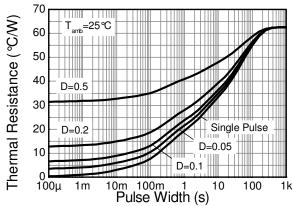
^{9.} Thermal resistance from junction to solder-point (at the end of the drain lead).

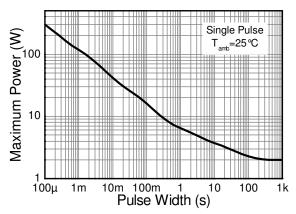


Thermal Characteristics









Transient Thermal Impedance

Pulse Power Dissipation



Electrical Characteristics (@T_A = +25 °C, unless otherwise specified.)

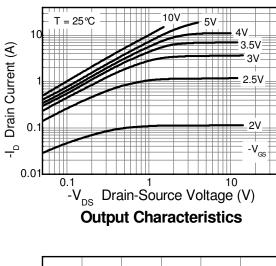
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	BV _{DSS}	-60	_	_	V	$I_D = -250 \mu A, V_{GS} = 0 V$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	-0.5	μΑ	$V_{DS} = -60V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS							
Gate Threshold Voltage	$V_{GS(th)}$	-1	_	_	V	$I_D = -250 \mu A, V_{DS} = V_{GS}$	
Statia Drain Saurea On Desigtance (Note 10)			0.096	0.125	Ω	V _{GS} = -10V, I _D = -2.2A	
Static Drain-Source On-Resistance (Note 10)	R _{DS(ON)}	_	0.12	0.19	12	V _{GS} = -4.5V, I _D = -1.8A	
Forward Transconductance (Notes 10 & 11)	g _{fs}	_	4.7	_	S	V _{DS} = -15V, I _D = -2.2A	
Diode Forward Voltage (Note 10)	V _{SD}	_	-0.85	-0.95	V	I _S = -2A, V _{GS} = 0V, T _J = +25℃	
Reverse Recovery Time (Note 11)	t _{rr}		25.1	_	ns	$I_S = -1.7A$, $di/dt = 100A/\mu s$,	
Reverse Recovery Charge (Note 11)	Q _{rr}	_	27.2	_	nC	T _J = +25℃	
DYNAMIC CHARACTERISTICS (Note 11)	•						
Input Capacitance	C _{iss}	_	637	_	pF	V _{DS} = -30V, V _{GS} = 0V -f = 1MHz	
Output Capacitance	Coss	_	70	_	pF		
Reverse Transfer Capacitance	C_{rss}	_	53	_	рF		
Total Gate Charge (Note 12)	Qg	_	9	_	nC	V _{GS} = -4.5V	
Total Gate Charge (Note 12)	Qg	_	17.7	_	nC	V _{DS} = -30V	
Gate-Source Charge (Note 12)	Q _{gs}	_	1.6	_	nC	$V_{GS} = -10V$ $I_{D} = -2.2A$	
Gate-Drain Charge (Note 12)	Q _{gd}	_	4.4	_	nC	1	
Turn-On Delay Time (Note 12)	t _{D(on)}	_	2.6	_	ns		
Turn-On Rise Time (Note 12)	t _r	_	3.4	_	ns	$V_{DD} = -30V, V_{GS} = -10V$ $I_D = -1A, R_G \cong 6\Omega$	
Turn-Off Delay Time (Note 12)	t _{D(off)}	_	26.2	_	ns		
Turn-Off Fall Time (Note 12)	t _f	_	11.3		ns	<u> </u>	

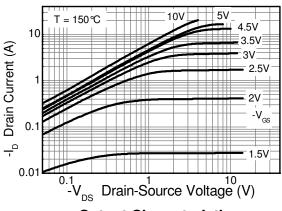
Notes:

- 10. Measured under pulsed conditions. Pulse width ≤ 300µs; duty cycle ≤ 2%.
 11. For design aid only, not subject to production testing.
 12. Switching characteristics are independent of operating junction temperatures.

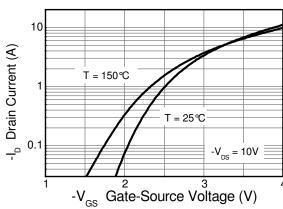


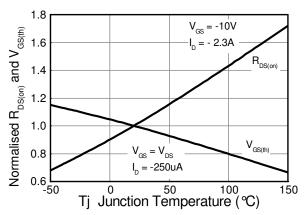
Typical Characteristics





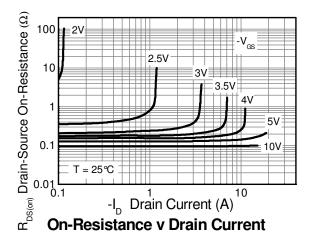
Output Characteristics

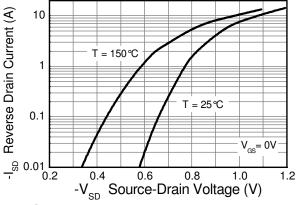




Typical Transfer Characteristics



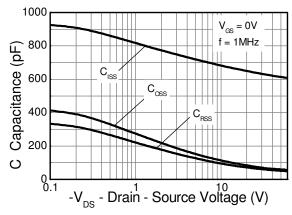




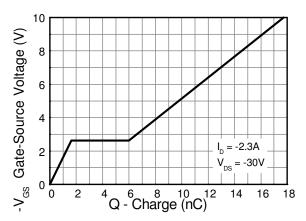
Source-Drain Diode Forward Voltage



Typical Characteristics (continued)

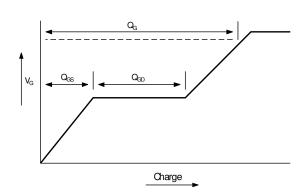


Capacitance v Drain-Source Voltage

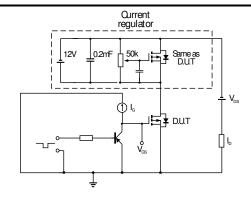


Gate-Source Voltage v Gate Charge

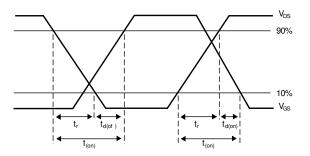
Test Circuits



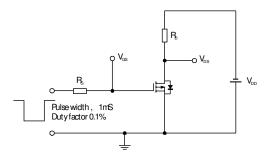
Basic gate charge waveform



Gate charge test circuit



Switching time waveforms

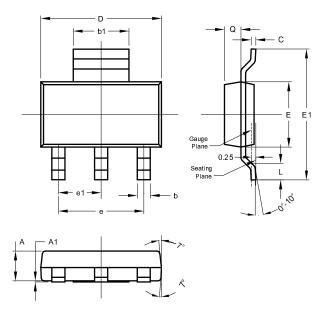


Switching time test circuit



Package Outline Dimensions

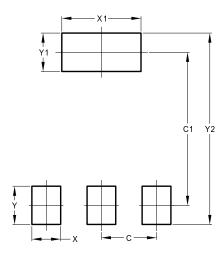
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



SOT223				
Dim	Min	Max	Тур	
Α	1.55	1.65	1.60	
A1	0.010	0.15	0.05	
b	0.60	0.80	0.70	
b1	2.90	3.10	3.00	
С	0.20	0.30	0.25	
D	6.45	6.55	6.50	
Е	3.45	3.55	3.50	
E1	6.90	7.10	7.00	
е	-	-	4.60	
e1	-	-	2.30	
L	0.85	1.05	0.95	
Q	0.84	0.94	0.89	
All Dimensions in mm				

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	2.30
C1	6.40
X	1.20
X1	3.30
Υ	1.60
Y1	1.60
Y2	8.00



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