



120V NPN MEDIUM POWER DARLINGTON TRANSISTOR IN SOT23F

Features

- BV_{CEO} > 120V
- I_C = 1A Continuous Collector Current
- V_{CE(SAT)} < 1.5V @ 1A
- R_{CE(SAT)} = 38mΩ
- 1.5W Power Dissipation
- Complementary PNP Type: ZXTP05120FF
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Description

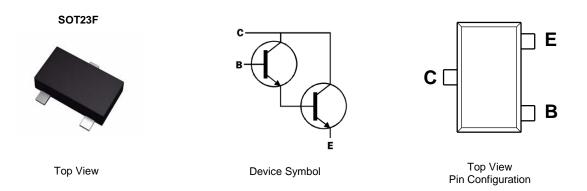
This high performance NPN Darlington transistor is housed in the small outline SOT23 flat package for applications where space is at a premium.

Mechanical Data

- Case: SOT23F
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208⁽³⁾
- Weight: 0.012 grams (Approximate)

Applications

- Lamp, Relay and Solenoid Drive
- Lighting



Ordering Information (Note 4)

Part Number	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
ZXTN04120HFFTA	AEC-Q101	1F6	7	8	3,000

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

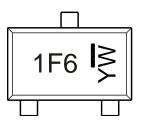
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information





 $\begin{array}{l} 1F6 = Product Type Marking Code \\ YW = Date Code Marking \\ Y = Year: 0~9 \\ \overline{W} = Week: A~Z: 1~26 \\ a~z: 27~52 \\ z \ represents \ 52 \ \& \ 53 \ week \end{array}$



Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	140	V
Collector-Emitter Voltage	V _{CEO}	120	V
Emitter-Base Voltage	V _{EBO}	10	V
Continuous Collector Current	Ic	1	A
Peak Pulse Current	I _{CM}	4	A
Base Current	IB	0.5	A

Thermal Characteristics ($@T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
	(Note 5)		0.84 6.72		
Power Dissipation	(Note 6)		1.34 10.72	W	
Linear Derating Factor	(Note 7)	PD	1.50 12.0	mW/°C	
	(Note 8)		2.0 16.0		
	(Note 5)		149		
Thermal Desistance, lunction to Archient	(Note 6)		93	90.444	
Thermal Resistance, Junction to Ambient	(Note 7)	R _{θJA}	83	°C/W	
	(Note 8)		60		
Thermal Resistance, Junction to Lead (Note 9)		R _{θJL}	43.8	°C/W	
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C		

ESD Ratings (Note 10)

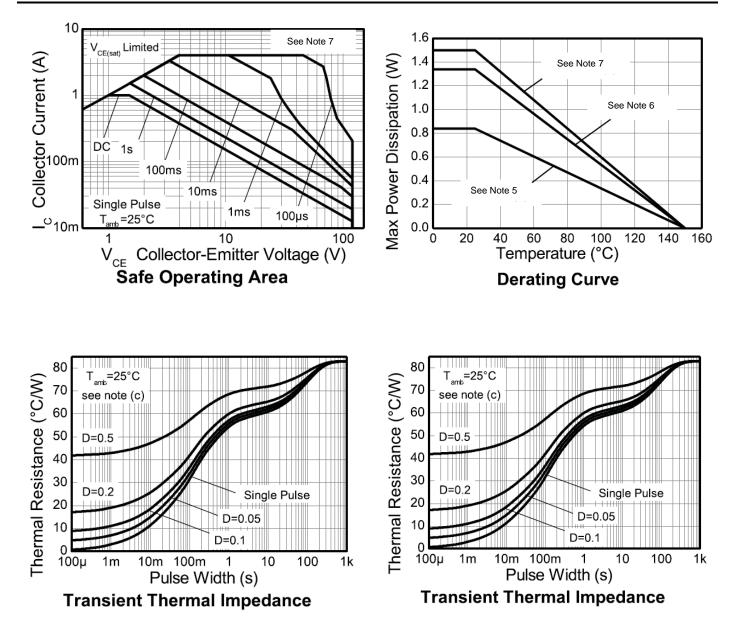
Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge – Human Body Model	ESD HBM	2,000	V	2
Electrostatic Discharge – Machine Model	ESD MM	200	V	В

5. For a device mounted with the exposed collector pad on 15mm x 15mm 1oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured Notes: under still air conditions whilst operating in a steady-state. 6. Same as Note 5, except the device is mounted on 25mm x 25mm 2oz copper. 7. Same as Note 5, except the device is mounted on 50mm x 50mm 2oz copper.

Same as Note 7, whilst measured at t < 5 seconds.
Thermal resistance from junction to solder-point (at the end of the collector lead).
Refer to JEDEC specification JESD22-A114 and JESD22-A115.



Thermal Characteristics and Derating Information





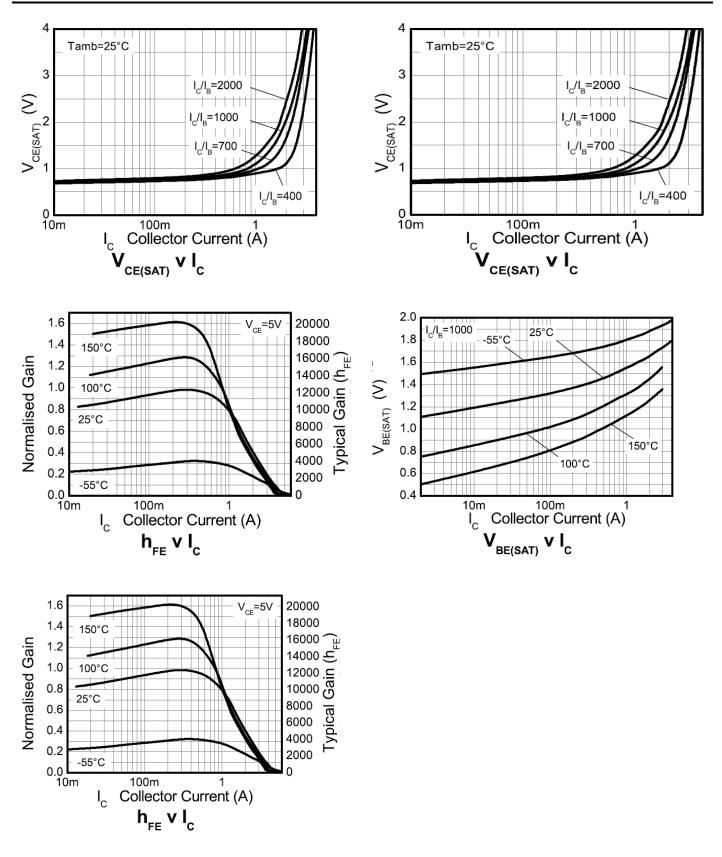
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS						
Collector-Base Breakdown Voltage	BV _{CBO}	140	300	_	V	$I_{\rm C} = 100 \mu {\rm A}$
Collector-Emitter Breakdown Voltage (Base Open) (Note 11)	BV _{CEO}	120	140	—	V	$I_{\rm C} = 10 {\rm mA}$
Emitter-Base Breakdown Voltage	BV _{EBO}	10	16	—	V	I _E = 100μA
Collector-Base Cutoff Current	I _{CBO}	—	<1 —	100 10	nΑ μΑ	V _{CB} = 120V V _{CB} = 120V, T _A = +100°C
Collector-Emitter Cutoff Current	ICES	_	<0.1	10	μA	V _{CE} = 120V
Emitter-Base Cutoff Current	I _{EBO}	_	<1	100	nA	$V_{EB} = 8V$
ON CHARACTERISTICS (Note 11)						
Static Forward Current Transfer Ratio	h _{FE}	3k 3k 3k 1k	11k 12k 10k 5k	— — 30k —	_	$\label{eq:lc} \begin{array}{l} I_{C} = 50 \text{mA}, \ V_{CE} = 5 \text{V} \\ I_{C} = 500 \text{mA}, \ V_{CE} = 5 \text{V} \\ I_{C} = 1 \text{A}, \ V_{CE} = 5 \text{V} \\ I_{C} = 2 \text{A}, \ V_{CE} = 5 \text{V} \end{array}$
Collector-Emitter Saturation Voltage	Vce(sat)	_	0.8 1.1 1.1	0.9 1.5 1.5	V	$I_{C} = 250$ mA, $I_{B} = 25$ mA $I_{C} = 1$ A, $I_{B} = 1$ mA $I_{C} = 2$ A, $I_{B} = 5$ mA
Base-Emitter Saturation Voltage	V _{BE(SAT)}	-	1.55	1.7	V	$I_{C} = 1A, I_{B} = 1mA$
Base-Emitter On Voltage	V _{BE(ON)}	—	1.45	1.7	V	$I_C = 1A$, $V_{CE} = 5V$
SMALL SIGNAL CHARACTERISTICS						_
Transition Frequency	fT	—	120	—	MHz	$I_C = 100 \text{mA}, V_{CE} = 10 \text{V},$ f = 20MHz
Input Capacitance	CIBO	-	68	90	pF	$V_{EB} = 500 \text{mV}, \text{ f} = 1 \text{MHz}$
Output Capacitance	C _{OBO}	_	12.8	25	pF	$V_{CB} = 10V$, f = 1MHz
Delay Time	tD	—	507	—	ns	101/
Rise Time	t _R	—	136	—	ns	$V_{CC} = 10V,$ $I_{C} = 500mA,$
Storage Time	t _S	—	910	_	ns	$-I_{B1} = -I_{B2} = 0.5 \text{mA}$
Fall Time	tF	-	369 -	-	ns	

Note: 11. Measured under pulsed conditions. Pulse width \leq 300µs. Duty cycle \leq 2%.



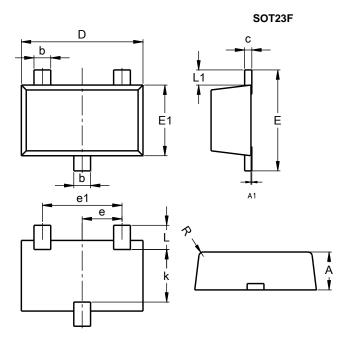
Typical Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)





Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

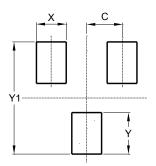


SOT23F						
Dim	Min	Max	Тур			
Α	0.80	1.00	0.90			
b	0.35	0.50	0.44			
С	0.10	0.20	0.16			
D	2.80	3.00	2.90			
e	0.95 REF					
e1	(0.190 RE	F			
Ш	2.30	2.50	2.40			
E1	1.50	1.70	1.65			
k	1.20	I	-			
L	0.30	0.65	0.50			
L1	0.30	0.50	0.40			
R	0.05	0.15	_			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23F



Dimensions	Value (in mm)		
С	0.95		
Х	0.80		
Y	1.110		
Y1	3.000		

Note: For high voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance distances between device Terminals and PCB tracking.



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