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Input Voltage = 10 to 100V

Output Voltage = 5V ± 10%

UL Flammability Rating 94V-0

MIL-STD-202, Method 208 @3 Weight: 0.34 grams (approximate)

Moisture Sensitivity: Level 1 per J-STD-020

Mechanical Data Case: TO252 (DPAK)

Fully Integrated Into a TO252 Package

A Product Line of **Diodes Incorporated** 

### **ZXTR2005K**

#### 100V INPUT, 5V 50mA REGULATOR TRANSISTOR

Series Linear Regulator Using Emitter-Follower Stage

Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)

Case Material: Molded Plastic. "Green" Molding Compound

Terminals: Finish - Matte Tin Plated Leads, Solderable per

Halogen and Antimony Free. "Green" Device (Note 3)

Qualified to AEC-Q101 Standards for High Reliability

#### Description

The ZXTR2005K monolithically integrates a transistor, Zener diode and resistor to function as a high voltage linear regulator. The device regulates with a 5V nominal output at 15mA. It is designed for use in high voltage applications where standard linear regulators cannot be used. This function is fully integrated into a TO252 package, minimizing PCB area and reducing number of components when compared with a multi-chip discrete solution.

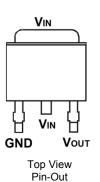
### Applications

Supply voltage regulation in:

- Networking
- Telecom
- Power Over Ethernet (PoE)

TO252 (DPAK)

# **ZXTR2005** V<sub>OUT</sub> GŇD Top View Pin-Out



Pin Name	Pin Function
Vin	Input Supply
GND	Power Ground
Vout	Voltage Output

#### Ordering Information (Note 4)

Top View

Product	Package	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXTR2005K-13	TO252 (DPAK)	ZXTR 2005	13	16	2,500

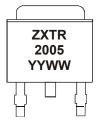
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead free.html for more information about Diodes Incorporated's definitions of Halogen and Antimony free, "Green" and Lead-Free.

3. Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

#### **Marking Information**



ZXTR 2005 = Product Type Marking Code YYWW = Date Code Marking YY = Last Digit of Year, (ex: 13 = 2013) WW = Week Code 01 - 52

Notes:



## Absolute Maximum Ratings (Voltage relative to GND, @T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Input Voltage	V <sub>IN</sub>	-0.3 to 100	V
Continuous Input & Output Current	I <sub>IN,</sub> I <sub>OUT</sub>	450	mA
Peak Pulsed Input & Output Current	I <sub>IM</sub> , I <sub>OM</sub>	2	A
Maximum Voltage applied to V <sub>OUT</sub>	V <sub>OUT(max)</sub>	11	V

#### Maximum Current at VIN = 48V (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Continuous Output Current	(Note 7)	I <sub>OUT</sub>	50	mA
Pulsed Output Current	(Note 8)		830	<b>~</b> ^
Pulsed Output Cultent	(Note 9)	IOM	170	mA

#### Thermal Characteristics

Characteristic		Symbol	Value	Unit
Rower Dissinction	(Note 5)	P	2.3	w
Power Dissipation	(Note 6)	PD PD	1.1	vv
Thermal Decistores Junction to Ambient	(Note 5)		44	
Thermal Resistance, Junction to Ambient	(Note 6)	R <sub>0JA</sub>	90	20111
Thermal Resistance, Junction to Lead	(Note 10)	R <sub>θJL</sub>	8.4	°C/W
Thermal Resistance, Junction to Case (Note 10)		Rejc	14.6	
Recommended Operating Junction Temperature Range		TJ	-40 to +125	°C
Maximum Operating Junction and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-65 to +150	°C

#### ESD Ratings (Note 11)

Characteristics	Symbols	Value	Unit	JEDEC Class
Electrostatic Discharge – Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge – Machine Model	ESD MM	400	V	С

Notes:

5. For a device mounted with the exposed V<sub>IN</sub> pad on 50mm x 50mm 1oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in steady-state.

6. Same as note 5, except mounted on 15mm x 15mm 1oz copper.

7. Same as note 5, whilst operating at  $V_{IN}$  = 48V. Refer to Safe Operating Area for other Input Voltages.

8. Same as note 5, except measured with a single pulse width = 100 $\mu$ s and V<sub>IN</sub> = 48V.

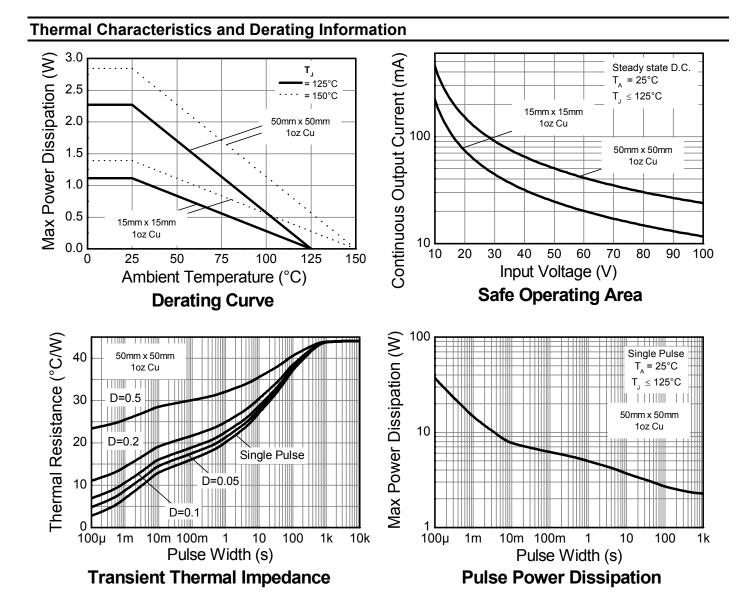
9. Same as note 5, except measured with a single pulse width = 10ms and  $V_{IN}$  = 48V.

10.  $R_{\theta JL}$  = Thermal resistance from junction to solder-point (on the exposed V<sub>IN</sub> pad).

 $R_{\theta JC}$  = Thermal resistance from junction to the top of case.

11. Refer to JEDEC specification JESD22-A114 and JESD22-A115.







Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Output Voltage (Note 12)	V <sub>OUT</sub>	4.5	5.0	5.5	V	V <sub>IN</sub> = 48V, I <sub>OUT</sub> = 15mA
Line Regulation (Notes 12 & 13)	ΔVout	_	195	300	mV	V <sub>IN</sub> = 10 to 72V, I <sub>OUT</sub> = 15mA
Temperature Coefficient	$\Delta V_{OUT} / \Delta T$	_	7.0	_	mV/°C	T <sub>J</sub> = -40°C to +125°C V <sub>IN</sub> = 48V, I <sub>OUT</sub> = 15mA
Load Regulation (Notes 12 & 14)	$\Delta V_{OUT}$	_	-185 -205	-350 -400	mV	I <sub>OUT</sub> = 0.1 to 30mA, V <sub>IN</sub> = 48V I <sub>OUT</sub> = 0.1 to 100mA, V <sub>IN</sub> = 48V
Minimum Value of Input Voltage Required to Maintain Line Regulation	V <sub>IN(MIN)</sub>	10	_	_	V	—
Quiescent Current	lQ	_	260 550	500 900	μA	V <sub>IN</sub> = 48V, I <sub>OUT</sub> = 10μA V <sub>IN</sub> = 100V, I <sub>OUT</sub> = 10μA
Power Supply Rejection Ratio	$\Delta V_{in} / \Delta V_{out}$	_	45	_	dB	$C_{OUT}$ = 100nF, I <sub>OUT</sub> = 15mA, V <sub>OUT</sub> = 5V, V <sub>IN</sub> = 10 to 100V, f = 100H

# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

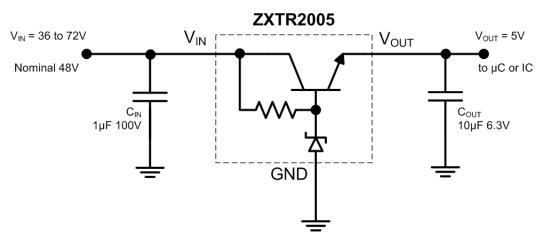
Notes: 12. Measured under pulsed conditions. Pulse width  $\leq$  300µs. Duty cycle  $\leq$  2%.

13. Line regulation  $\Delta V_{OUT} = V_{OUT} (@V_{IN} = 72V) - V_{OUT} (@V_{IN} = 10V)$ 

ulation  $\Delta V_{OUT} = V_{OUT}(@ I_{OUT} = 30mA) - V_{OUT}(@ I_{OUT} = 0.1mA)$ 

ΔVOUT = VOUT(@ IOUT = 100mA) - VOUT(@ IOUT = 0.1mA)

# **Typical Application Circuit**



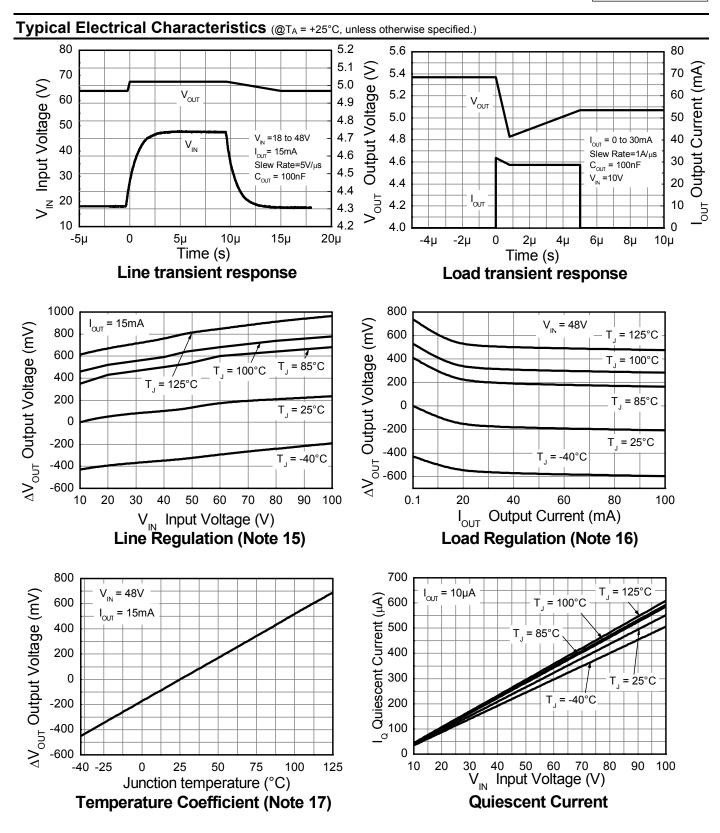
Example of a 5V regulated supply from a nominal 48V for powering a Controller IC.

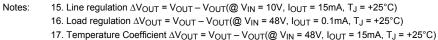
## **Pin Functions**

Pin Name	Pin Function	Notes
V <sub>IN</sub>	Input Supply	To maintain output regulation the input voltage can vary from 10V to 100V with respect to the GND pin. It is recommended to connect a $1\mu$ F capacitor to GND.
GND	Power Ground	This pin should be tied to the system ground.
V <sub>OUT</sub>	Voltage Output	Outputs a regulated 5V. It is recommended to connect a $10\mu$ F capacitor to GND. Minimum of $10\mu$ A must be drawn from V <sub>OUT</sub> to maintain regulation. The pin can be pulled high to a maximum of 11V with respect to ground.

<sup>14.</sup> Load regulation



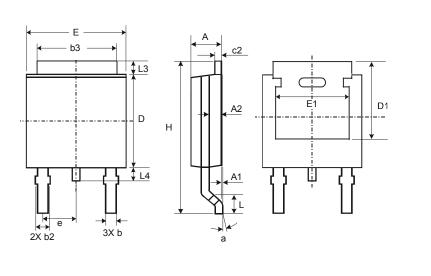






# Package Outline Dimensions

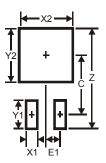
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



TO252								
Dim	Min	Max	Тур					
Α	2.19	2.39	2.29					
A1	0.00	0.13	0.08					
A2	0.97	1.17	1.07					
b	0.64	0.88	0.783					
b2	0.76	1.14	0.95					
b3	5.21	5.46	5.33					
c2	0.45	0.58	0.531					
D	6.00	6.20	6.10					
D1	5.21	-	-					
е	-	_	2.286					
Е	6.45	6.70	6.58					
E1	4.32	-	-					
Н	9.40	10.41	9.91					
L	1.40	1.78	1.59					
L3	0.88	1.27	1.08					
L4	0.64	1.02	0.83					
а	0°	10°	_					
All	Dimen	sions i	All Dimensions in mm					

#### Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	11.6
X1	1.5
X2	7.0
Y1	2.5
Y2	7.0
С	6.9
E1	2.3



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