



DDC (LO-R1) H

NPN PRE-BIASED SMALL SIGNAL DUAL SURFACE MOUNT TRANSISTOR

Features

Product Summary

- Epitaxial Planar Die Construction
- Complementary PNP Types Available (DDA)
- Built-In Biasing Resistors
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

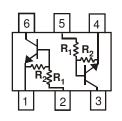
P/N	R1 (NOM)	R2 (NOM)	MARKING
DDC122LH	0.22ΚΩ	10ΚΩ	N81
DDC142JH	0.47ΚΩ	10ΚΩ	N82
DDC122TH	0.22ΚΩ	OPEN	N83
DDC142TH	0.47ΚΩ	OPEN	N84

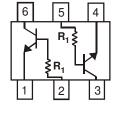
Mechanical Data

Case: SOT-563, Molded Plastic

- Case Material: Molded Plastic.
 - UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram
- Weight: 0.005 grams (Approximate)

Pin Assignments





 $R_1,\,R_2$

R₁ Only

SCHEMATIC DIAGRAM, TOP VIEW

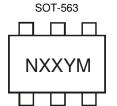
Ordering Information (Note 4)

Device	Packaging	Shipping
DDC122LH-7	SOT-563	3,000/Tape & Reel
DDC142JH-7	SOT-563	3,000/Tape & Reel
DDC122TH-7	SOT-563	3,000/Tape & Reel
DDC142TH-7	SOT-563	3,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



NXX = Product Type Marking Code (See Page 1)

YM = Date Code Marking Y = Year ex: T = 2006

M = Month ex: 9 = September

Date Code Key

Year	2002	2003	2004	200	5 20	06 2	007	2008	8 2	2009	2010	2011	2012
Code	N	Р	R	S	Т	-	U	V		W	Χ	Υ	Z
Month	Jan	Feb	Mar	Apr	May	Jun	Ju	ı	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7		8	9	0	N	D



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Supply Voltage (Note 4) to (Note 5) and (Note 1) to (Note 3)	V _{CC}	50	V
Input Voltage (Note 6) to (Note 5) and (Note 7) to (Note 3) DDC122LH DDC142JH	V _{IN}	-5 to +6 -5 to +6	V
Input Voltage (Note 5) to (Note 6) and (Note 3) to (Note 7) DDC122TH DDC142TH	V _{EBO} (MAX)	5	V
Output Current All	I _C	100	mA
Power Dissipation	P _d	150	mW
Thermal Resistance, Junction to Ambient Air (Note 6)	$R_{ heta JA}$	833	°C/W

Notes:

- 5. Package is non-polarized. Parts may be on reel in orientation illustrated, 180° rotated, or mixed (both ways).
- Mounted on FR4 Board with recommended pad layout at http://www.diodes.com/datasheets/ap02001.pdf.
 Product manufactured with Date Code UO (week 40, 2007) and newer are built with Green Molding Compound. Product manufactured prior to Date Code UO are built with Non-Green Molding Compound and may contain Halogens or Sb2O3 Fire Retardants.

Electrical Characteristics (@T_A = +25 °C, unless otherwise specified.) R1, R2 Types

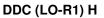
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
Input Voltage	DDC122LH DDC142JH	V _{I(off)}	0.3 0.3	_	_	٧	V _{CC} = 5V, I _O = 100μA
,	DDC122LH DDC142JH	$V_{I(on)}$	_	_	2.0 2.0	٧	$V_O = 0.3V$, $I_O = 20mA$ $V_O = 0.3V$, $I_O = 20mA$
Output Voltage		$V_{O(on)}$	_	_	0.3V	٧	$I_{O}/I_{I} = 5mA/0.25mA$
Input Current	DDC122LH DDC142JH	lı	_	_	28 13	mA	V _I = 5V
Output Current		I _{O(off)}	_	_	0.5	μΑ	$V_{CC} = 50V, V_I = 0V$
DC Current Gain	DDC122LH DDC142JH	G _l	56 56	_	_	_	V _O = 5V, I _O = 10mA
Gain-Bandwidth Product*		f⊤	_	200	_	MHz	V _{CE} = 10V, I _E = 5mA, f = 100MHz

^{*} Transistor - For Reference Only

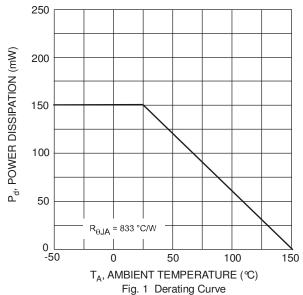
Electrical Characteristics (@TA = +25 °C, unless otherwise specified.) R1-Only

Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV _{CBO}	50			V	$I_C = 50\mu A$	
Collector-Emitter Breakdown Voltage		BV _{CEO}	40	_	_	V	I _C = 1mA
Emitter-Base Breakdown Voltage DDC122TH DDC142TH		BV _{EBO}	5	_	_	V	$I_E = 50\mu A$ $I_E = 50\mu A$
Collector Cutoff Current		I _{CBO}	_	_	0.5	μΑ	V _{CB} = 50V
Emitter Cutoff Current DDC122TH DDC142TH		I _{EBO}	_	_	0.5 0.5	μΑ	V _{EB} = 4V
Collector-Emitter Saturation Voltage		V _{CE(sat)}	_		0.3	V	$I_C = 5mA$, $I_B = 0.25mA$
DC Current Transfer Ratio DDC122TH DDC142TH		h _{FE}	100 100	250 250	600 600	_	I _C = 1mA, V _{CE} = 5V
Gain-Bandwidth Product*		f _T	_	200		MHz	V _{CE} = 10V, I _E = -5mA, f = 100MHz

^{*} Transistor - For Reference Only

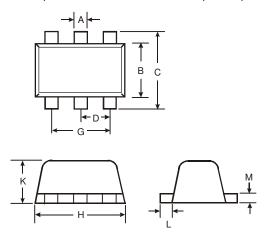






Package Outline Dimensions

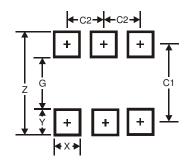
 $Please see AP02002 \ at \ http://www.diodes.com/datasheets/ap02002.pdf \ for \ the \ latest \ version.$



	SOT563							
Dim	Min	Max	Тур					
Α	0.15	0.30	0.20					
В	1.10	1.25	1.20					
С	1.55	1.70	1.60					
D	-	-	0.50					
G	0.90	1.10	1.00					
Н	1.50	1.70	1.60					
K	0.55	0.60	0.60					
L	0.10	0.30	0.20					
M	0.10	0.18	0.11					
All	All Dimensions in mm							

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.2
G	1.2
X	0.375
Y	0.5
C1	1.7
C2	0.5



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