PDS560

## Features

- Guard Ring Die Construction for Transient Protection
- High Surge Current Capability
- Low Leakage Current
- Low Power Loss, High Efficiency
- For Use in High Frequency Inverters, Free Wheeling, and Polarity Protection Applications
- High Forward Surge Current Capability
- Lead-Free Finish; RoHS Compliant (Notes $1 \& 2$ )
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability


## Mechanical Data

- Case: POWERDI ${ }^{\oplus} 5$
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 e3)
- Polarity: See Diagram
- Weight: 0.094 grams (Approximate)


## POWERDI ${ }^{\oplus} 5$



Top View


Bottom View


Note: Pins Left \& Right must be electrically connected at the printed circuit board.

## Ordering Information (Note 4)

| Part Number | Case | Packaging |
| :---: | :---: | :---: |
| PDS560-13 | POWERDI ${ }^{( } 5$ | $5,000 /$ Tape \& Reel |
| PDS5600-13 | POWERDI ${ }^{\ominus} 5$ | $5,000 /$ Tape \& Reel |

Notes: 1. EU Directive 2002/95/EC (RoHS) \& 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine ( $<1500 \mathrm{ppm}$ total $\mathrm{Br}+\mathrm{Cl}$ ) and <1000ppm antimony compounds.
4. For packaging details, go to our website at http://www.diodes.com.

## Marking Information



S560 = Product Type Marking Code
J! I = Manufacturers' Code Marking
YYWW = Date Code Marking
YY = Last Two Digits of Year (ex: 15 for 2015)
WW = Week Code (01-53)
K = Factory Designator

PDS560

Maximum Ratings (@ $T_{A}=+25^{\circ} \mathrm{C}$, unless otherwise specified.)
Single phase, half wave, 60 Hz , resistive or inductive load.
For capacitance load, derate current by $20 \%$.

| Characteristic | Symbol | Value | Unit |
| :--- | :---: | :---: | :---: |
| Peak Repetitive Reverse Voltage <br> Working Peak Reverse Voltage <br> DC Blocking Voltage | $V_{\text {RRM }}$ <br> $V_{\text {RWM }}$ <br> $V_{R}$ | 60 | V |
| RMS Reverse Voltage | $\mathrm{V}_{\mathrm{R}(\mathrm{RMS})}$ | 42 | V |
| Average Rectified Output Current | lo | 5 | A |
| Non-Repetitive Peak Forward Surge Current |  |  |  |
| 8.3ms Single half sine-wave Superimposed on Rated Load | $I_{\text {FSM }}$ | 150 | A |

## Thermal Characteristics

| Characteristic | Symbol | Typ | Max | Unit |
| :--- | :---: | :---: | :---: | :---: |
| Thermal Resistance Junction to Soldering Point | $\mathrm{R}_{\theta J \mathrm{~S}}$ | - | 2.0 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Thermal Resistance Junction to Ambient Air (Note 5) $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ | $\mathrm{R}_{\theta \mathrm{JA}}$ | 95 | - | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Thermal Resistance Junction to Ambient Air (Note 6) $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ | $\mathrm{R}_{\mathrm{JJA}}$ | 70 | - | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Thermal Resistance Junction to Ambient Air (Note 7) $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ | $\mathrm{R}_{\theta J \mathrm{~A}}$ | 50 | - | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Operating and Storage Temperature Range | $\mathrm{T}_{\mathrm{J},}, \mathrm{T}_{\mathrm{STG}}$ | -65 to +150 | ${ }^{\circ} \mathrm{C}$ |  |

Electrical Characteristics ( $@ \mathrm{~T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$, unless otherwise specified.)

| Characteristic | Symbol | Min | Typ | Max | Unit | Test Condition |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Reverse Breakdown Voltage (Note 8) | $\mathrm{V}_{(\mathrm{BR}) \mathrm{R}}$ | 60 | - | - | V | $\mathrm{I}_{\mathrm{R}}=0.2 \mathrm{~mA}$ |
| Forward Voltage | $V_{\text {F }}$ | $\begin{aligned} & - \\ & - \end{aligned}$ | $\begin{aligned} & 0.61 \\ & 0.54 \\ & 0.71 \end{aligned}$ | $\begin{aligned} & \hline 0.67 \\ & 0.60 \\ & 0.77 \\ & 0.68 \\ & \hline \end{aligned}$ | V | $\begin{aligned} & \mathrm{I}_{\mathrm{F}}=5 \mathrm{~A}, \mathrm{~T}_{\mathrm{S}}=+25^{\circ} \mathrm{C} \\ & \mathrm{I}_{\mathrm{F}}=5 \mathrm{~A}, \mathrm{~T}_{\mathrm{S}}=+125^{\circ} \mathrm{C} \\ & \mathrm{IF}_{\mathrm{F}}=8 \mathrm{~A}, \mathrm{~T}_{\mathrm{S}}=+25^{\circ} \mathrm{C} \\ & \mathrm{I}_{\mathrm{F}}=8 \mathrm{~A}, \mathrm{~T}_{\mathrm{S}}=+125^{\circ} \mathrm{C} \end{aligned}$ |
| Reverse Leakage Current (Note 8) | $\mathrm{I}_{\mathrm{R}}$ | - | $\frac{4}{2}$ | $\begin{aligned} & \hline 150 \\ & 15 \\ & 30 \end{aligned}$ | $\begin{aligned} & \mu \mathrm{A} \\ & \mathrm{~mA} \\ & \mathrm{~mA} \end{aligned}$ | $\begin{aligned} & \mathrm{T}_{S}=+25^{\circ} \mathrm{C}, \mathrm{~V}_{\mathrm{R}}=60 \mathrm{~V} \\ & \mathrm{~T}_{\mathrm{S}}=+100^{\circ} \mathrm{C}, \mathrm{~V}_{\mathrm{R}}=60 \mathrm{~V} \\ & \mathrm{~T}_{\mathrm{S}}=+125^{\circ} \mathrm{C}, \mathrm{~V}_{\mathrm{R}}=60 \mathrm{~V} \end{aligned}$ |

Notes: $\quad$ 5. FR-4 PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com.
6. Polymide PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com.
7. Polymide PCB, 2 oz. Copper. Cathode pad dimensions $9.4 \mathrm{~mm} \times 7.2 \mathrm{~mm}$. Anode pad dimensions $2.7 \mathrm{~mm} \times 1.6 \mathrm{~mm}$.
8. Short duration pulse test used to minimize self-heating effect.

PDS560


Fig. 1 Forward Power Dissipation


Fig. 3 Typical Reverse Characteristics



Fig. 2 Typical Forward Characteristics


Fig. 4 Total Capacitance vs. Reverse Voltage


Fig. 6 Operating Temperature Derating

## Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.


| POWERDI ${ }^{(\mathbb{7}} \mathbf{5}$ |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Dim | Min | Max | Typ |  |
| A | 1.05 | 1.15 | 1.10 |  |
| A1 | 0.00 | 0.05 | -- |  |
| A2 | 0.33 | 0.43 | 0.381 |  |
| b1 | 0.80 | 0.99 | 0.89 |  |
| b2 | 1.70 | 1.88 | 1.78 |  |
| D | 3.90 | 4.05 | 3.966 |  |
| D2 | -- | -- | 3.054 |  |
| E | 6.40 | 6.60 | 6.504 |  |
| e | -- | -- | 1.84 |  |
| E1 | 5.30 | 5.45 | 5.37 |  |
| E2 | -- | -- | 3.549 |  |
| L | 0.75 | 0.95 | 0.85 |  |
| L1 | 0.50 | 0.65 | 0.57 |  |
| W | 1.10 | 1.41 | 1.255 |  |
| All Dimensions in $\mathbf{~ m m}$ |  |  |  |  |
|  |  |  |  |  |

## Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.


| Dimensions | Value (in mm) |
| :---: | :---: |
| $\mathbf{C}$ | 1.840 |
| $\mathbf{G}$ | 0.852 |
| $\mathbf{X}$ | 1.390 |
| $\mathbf{X 1}$ | 3.360 |
| $\mathbf{Y}$ | 1.400 |
| $\mathbf{Y 1}$ | 4.860 |

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