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#### 1.0A HIGH VOLTAGE SCHOTTKY BARRIER RECTIFIER POWERDI®123

### **Features**

- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- Patented Interlocking Clip Design for High Surge Current Capacity
- Lead Free Finish, RoHS Compliant (Note 1)
- "Green" Molding Compound (No Br, Sb)
- Qualified to AEC-Q101 Standards for High Reliability

### **Mechanical Data**

- Case: POWERDI<sup>®</sup>123
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: Cathode Band
- Terminals: Finish Matte Tin annealed over Copper Leadframe.
   Solderable per MIL-STD-202, Method 208 63
- Weight: 0.01 grams (approximate)



Top View

# Ordering Information (Note 2)

| Part Number | Case                     | Packaging        |
|-------------|--------------------------|------------------|
| DFLS1150-7  | POWERDI <sup>®</sup> 123 | 3000/Tape & Reel |

Notes:

- 1. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied, see EU Directive 2002/95/EC Annex Notes.
- 2. For packaging details, go to our website at http://www.diodes.com.

## **Marking Information**



F07 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: R = 2004) M = Month (ex: 9 = September)

Date Code Key

| Year  | 2004 | 2005 | 2006 | 2007 | 2008 | 2009 | 2010 | 2011 | 2012 | 2013 | 2014 | 2015 |
|-------|------|------|------|------|------|------|------|------|------|------|------|------|
| Code  | R    | S    | Т    | U    | V    | W    | X    | Υ    | Z    | Α    | В    | С    |
|       |      |      |      |      |      |      |      |      |      |      |      |      |
| Month | Jan  | Feb  | Mar  | Apr  | May  | Jun  | Jul  | Aug  | Sep  | Oct  | Nov  | Dec  |



# Maximum Ratings @TA = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

| Characteristic  | Symbol   | Value | Unit |
|---|--|-------|------|
| Peak Repetitive Reverse Voltage<br>Working Peak Reverse Voltage<br>DC Blocking Voltage              | V <sub>RRM</sub><br>V <sub>RWM</sub><br>V <sub>R</sub> | 150   | ٧    |
| RMS Reverse Voltage   | V <sub>R(RMS)</sub>                                    | 106   | V    |
| Average Forward Current   | I <sub>F(AV)</sub>                                     | 1.0   | A    |
| Non-Repetitive Peak Forward Surge Current 8.3ms<br>Single Half Sine-Wave Superimposed on Rated Load | I <sub>FSM</sub>                                       | 50    | A    |

### **Thermal Characteristics**

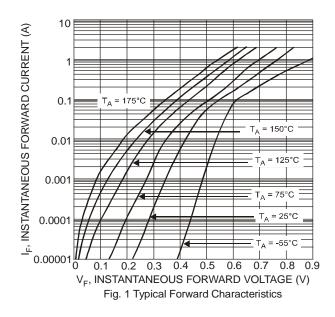
| Characteristic  | Symbol                            | Тур    | Max  | Unit |
|---|-----------------------------------|--------|------|------|
| Thermal Resistance Junction to Soldering Point (Note 3)               | $R_{\theta JS}$                   | _      | 7    | °C/W |
| Thermal Resistance Junction to Ambient (Note 4) T <sub>A</sub> = 25°C | $R_{	heta JA}$                    | 125    | _    | °C/W |
| Operating and Storage Temperature Range                               | T <sub>J</sub> , T <sub>STG</sub> | -55 to | +175 | °C   |

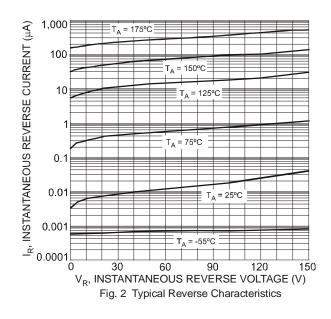
## Electrical Characteristics @TA = 25°C unless otherwise specified

| Characteristic                     | Symbol         | Min | Тур | Max  | Unit | Test Condition                               |
|------------------------------------|----------------|-----|-----|------|------|--|
| Reverse Breakdown Voltage (Note 5) | $V_{(BR)R}$    | 150 |     |      | V    | $I_R = 2\mu A$                               |
| Forward Voltage                    | $V_{F}$        | _   | _   | 0.82 | V    | I <sub>F</sub> = 1.0A                        |
| Leakage Current (Note 5)           | I <sub>R</sub> | _   | _   | 2    | μА   | V <sub>R</sub> = 150V, T <sub>A</sub> = 25°C |
| Total Capacitance                  | C <sub>T</sub> | _   | 28  | _    | pF   | $V_R = 5V_{DC}$ , $f = 1MHz$                 |

Notes:

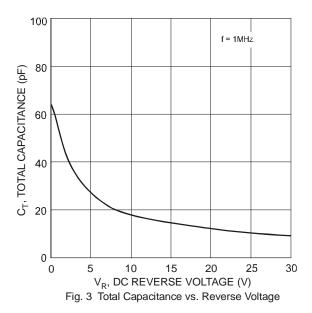
- 3. Theoretical  $R_{\text{OJS}}$  calculated from the top center of the die straight down to the PCB/cathode tab solder junction.
- 4. Part mounted on FR-4 board with 2 oz., minimum recommended copper pad layout, which can be found on our website at http://www.diodes.com.
- 5. Short duration pulse test used to minimize self-heating effect.



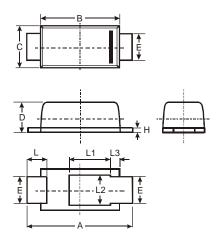


POWERDI is a registered trademark of Diodes Incorporated.



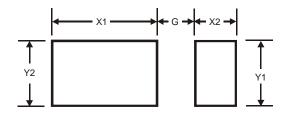


# **Package Outline Dimensions**



| POWERDI <sup>®</sup> 123 |      |      |      |  |  |  |
|--------------------------|------|------|------|--|--|--|
| Dim                      | Min  | Max  | Тур  |  |  |  |
| Α                        | 3.50 | 3.90 | 3.70 |  |  |  |
| В                        | 2.60 | 3.00 | 2.80 |  |  |  |
| ပ                        | 1.63 | 1.93 | 1.78 |  |  |  |
| D                        | 0.93 | 1.00 | 0.98 |  |  |  |
| Е                        | 0.85 | 1.25 | 1.00 |  |  |  |
| Н                        | 0.15 | 0.25 | 0.20 |  |  |  |
| L                        | 0.40 | 0.50 | 0.45 |  |  |  |
| L1                       | -    | 1    | 1.35 |  |  |  |
| L2                       | -    | -    | 1.10 |  |  |  |
| L3                       | -    |      | 0.20 |  |  |  |
| All Dimensions in mm     |      |      |      |  |  |  |

# **Suggested Pad Layout**



| Dimensions | Value (in mm) |
|------------|---------------|
| G          | 1.0           |
| X1         | 2.2           |
| X2         | 0.9           |
| Y1         | 1.4           |
| Y2         | 1.4           |



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