



PDS4200H

4A HIGH VOLTAGE SCHOTTKY BARRIER RECTIFIER POWERDI®

Features

- Lower Forward Voltage Drop than Ultrafast Rectifiers
- Very Low Leakage Current
- Soft Recovery Characteristics: Softness Factor $(t_b/t_a) \ge 1$ (see Figure 8)
- Highly Stable Oxide Passivated Junction
- High Forward Surge Current Capability
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: POWERDI[®]5
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208@3
- Polarity: See Diagram
- Weight: 0.095 grams (Approximate)

POWERDI®5





RIGHT PIN O HEAT SINK

Top View

Bottom View

Note: Pins Left & Right must be electrically connected at the printed circuit board.

Ordering Information (Note 4)

Part Number	Case	Packaging
PDS4200H-13	POWERDI®5	5.000/Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



S4200H = Product Type Marking Code

| | = Manufacturers' Code Marking

YYWW = Date Code Marking

YY = Last Two Digits of Year (ex: 15 for 2015)

WW = Week Code (01 - 53)

K = Factory Designator



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _R WM V _R	200	V
RMS Reverse Voltage	V _{R(RMS)}	141	V
Average Rectified Output Current	Io	4	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave Superimposed on Rated Load	I _{FSM}	100	Α

Thermal Characteristics

Characteristic	Symbol	Тур	Max	Unit
Thermal Resistance Junction to Soldering Point	$R_{ heta}$ JS	_	3.0	°C/W
Thermal Resistance Junction to Ambient Air (Note 5)	$R_{ heta JA}$	80	_	°C/W
Thermal Resistance Junction to Ambient Air (Note 6)	$R_{ heta JA}$	65	_	°C/W
Thermal Resistance Junction to Ambient Air (Note 7)	$R_{ heta JA}$	45	_	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +175		°C

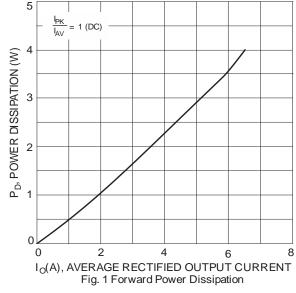
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

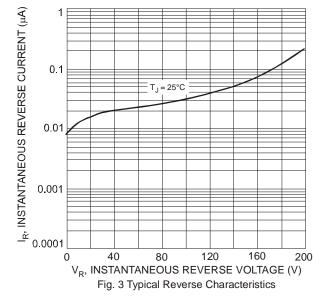
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 8)	$V_{(BR)R}$	200	_		V	$I_R = 5\mu A$
Forward Voltage	V _F		0.76 — 0.785 0.61 0.84 0.68	0.82 0.59 0.84 0.64 0.89 0.75	٧	I _F = 3A, T _S = +25°C I _F = 3A, T _S = +150°C I _F = 4A, T _S = +25°C I _F = 4A, T _S = +150°C I _F = 8A, T _S = +25°C I _F = 8A, T _S = +150°C
Reverse Leakage Current (Note 8)	I _R		0.2 0.8	1 4	μA mA	$T_S = +25$ °C, $V_R = 200V$ $T_S = +150$ °C, $V_R = 200V$
Reverse Recovery Time	t _{rr}		_	25	ns	$I_F = 0.5A$, $I_R = 1.0A$ $I_{RR} = 0.25A$ (see Figure 8)

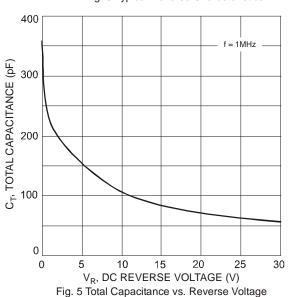
Notes:

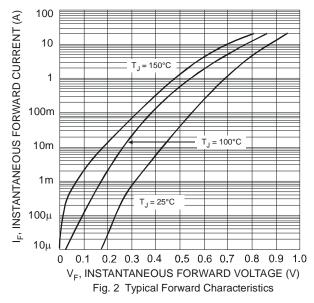
- FR-4 PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com.
 Polymide PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com.
 Polymide PCB, 2 oz. Copper. Cathode pad dimensions 9.4mm x 7.2mm. Anode pad dimensions 2.7mm x 1.6mm.
 Short duration test pulse used to minimize self-heating effect.

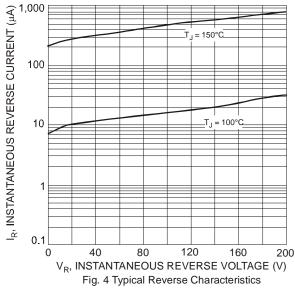


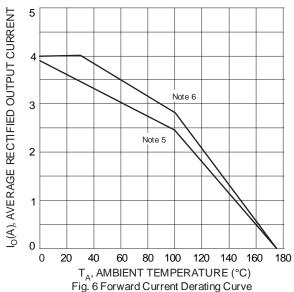




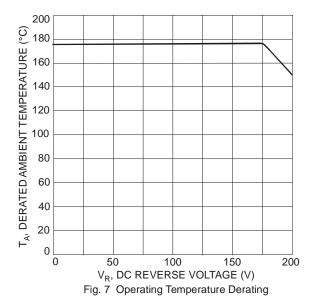


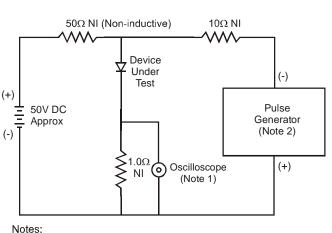


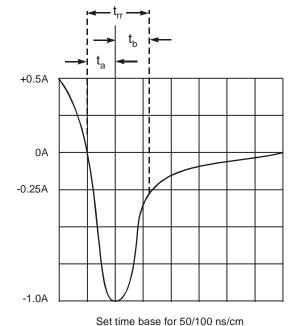












Notes:

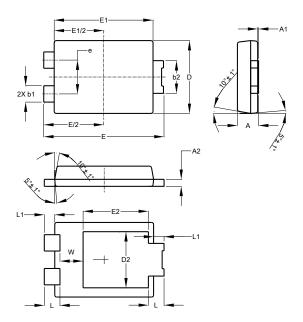
- 1. Rise Time = 7.0ns max. Input Impedance = $1.0M\Omega$, 22pF.
- 2. Rise Time = 10ns max. Input Impedance = 50Ω .

Fig. 8 Reverse Recovery Time Characteristic and Test Circuit



Package Outline Dimensions

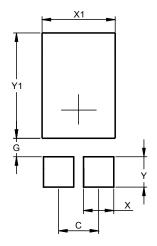
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



POWERDI [®] 5					
Dim	Min	Max	Тур		
Α	1.05	1.15	1.10		
A1	0.00	0.05			
A2	0.33	0.43	0.381		
b1	0.80	0.99	0.89		
b2	1.70	1.88	1.78		
D	3.90	4.05	3.966		
D2			3.054		
E	6.40	6.60	6.504		
е			1.84		
E1	5.30	5.45	5.37		
E2			3.549		
L	0.75	0.95	0.85		
L1	0.50	0.65	0.57		
W	1.10	1.41	1.255		
All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	1.840
G	0.852
Х	1.390
X1	3.360
Y	1.400
Y1	4 860



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