



DMP3160L

#### P-CHANNEL ENHANCEMENT MODE MOSFET

# **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(on)</sub>	I <sub>D</sub> T <sub>A</sub> = +25°C
-30V	122mΩ @ V <sub>GS</sub> = -10V	-2.7A
-30 V	190mΩ @ V <sub>GS</sub> = -4.5V	-2.0A

## **Description**

This new generation MOSFET has been designed to minimize the onstate resistance ( $R_{DS(ON)}$ ) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

# **Applications**

- DC-DC Converters
- Power Management Functions

### **Features**

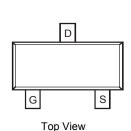
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

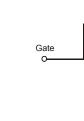
#### **Mechanical Data**

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Copper leadframe.
   Solderable per MIL-STD-202, Method 208 63
- Terminal Connections: See Diagram
- Weight: 0.008 grams (approximate)









**Equivalent Circuit** 

Source

Body Diode

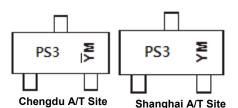
### Ordering Information (Note 4 & 5)

Part Number	Compliance	Case	Packaging
DMP3160L-7	Standard	SOT23	3000/Tape & Reel
DMP3160LQ-7	Automotive	SOT23	3000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.
- 5. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product\_grade\_definitions/.

### **Marking Information**



PS3 = Product Type Marking Code

YM = Date Code Marking for SAT (Shanghai Assembly/ Test site)

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YM = Date Code Marking for CAT (Chengdu Assembly/ Test site)
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Y or  $\overline{Y}$  = Year (ex: A = 2013)

M = Month (ex: 9 = September)

Date Code Key 2015 Year 2007 2008 2009 2010 2011 2012 2013 2014 Code B C Month Jan Feb Mar Apr May Jun Jul Aug Sep Oct Nov Dec Code 8 9 D



# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			$V_{DSS}$	-30	V
Gate-Source Voltage		$V_{GSS}$	±20	V	
Drain Current (Note 6) V <sub>GS</sub> = -10V	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	I <sub>D</sub>	-2.7 -2	Α
Pulsed Drain Current (Note 7)		I <sub>DM</sub>	-8	А	

# **Thermal Characteristics**

Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 6)	$P_{D}$	1.08	W	
Thermal Resistance, Junction to Ambient @T <sub>A</sub> = +25°C (Note 6)	$R_{\theta JA}$	115	°C/W	
Operating and Storage Temperature Range	$T_{J_i} T_{STG}$	-55 to +150	°C	

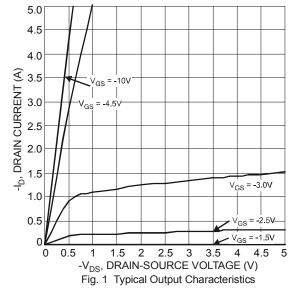
# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-30	_	_	V	$V_{GS} = 0V$ , $I_D = -250\mu A$
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	-800	nA	V <sub>DS</sub> = -30V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±80 ±800	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$ $V_{GS} = \pm 15V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	-1.3	-1.8	-2.1	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
Static Drain-Source On-Resistance	D		97	122	mΩ	$V_{GS} = -10V, I_D = -2.7A$
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>		165	190	11122	$V_{GS} = -4.5V$ , $I_D = -2.0A$
Forward Transfer Admittance	Y <sub>fs</sub>	_	5.9		S	$V_{DS} = -5V, I_D = -2.7A$
Diode Forward Voltage (Note 8)	$V_{SD}$	_	_	-1.26	V	$V_{GS} = 0V$ , $I_S = -2.7A$
DYNAMIC CHARACTERISTICS(Note 9)						
Input Capacitance	C <sub>iss</sub>	_	384.4		pF	<u> </u>
Output Capacitance	Coss	_	59.4		pF	$V_{DS} = -10V, V_{GS} = 0V$ f = 1.0MHz
Reverse Transfer Capacitance	C <sub>rss</sub>	_	52.8		pF	1.0141112
Gate Resistance	$R_{G}$	_	17.1		Ω	$V_{GS} = 0V, V_{DS} = 0V,$ f = 1.0MHz
Total Gate Charge(V <sub>GS</sub> = -4.5V)	Qg	_	4.0	_	nC	
Total Gate Charge(V <sub>GS</sub> = -10V)	Qg	_	8.2		nC	$V_{GS} = -10V/-4.5V$ ,
Gate-Source Charge	Qgs	_	0.9		nC	V <sub>DS</sub> = -15V, I <sub>D</sub> = -3A
Gate-Drain Charge	Q <sub>gd</sub>	_	1.2	_	nC	
Turn-On Delay Time	t <sub>D(on)</sub>	_	4.8	_	ns	V <sub>DS</sub> = -15V, V <sub>GS</sub> = -10V, R <sub>G</sub> = 6Ω, I <sub>D</sub> = -1A
Turn-On Rise Time	t <sub>r</sub>	_	7.3	_	ns	
Turn-Off Delay Time	t <sub>D(off)</sub>	_	22.5		ns	
Turn-Off Fall Time	t <sub>f</sub>	_	13.4		ns	

Notes:

- 6. Device mounted on FR-4 PCB. t ≤10 sec.
- 7. Pulse width  $\leq 10 \mu S$ , Duty Cycle  $\leq 1\%$ .
- 8. Short duration pulse test used to minimize self-heating effect.
  9. Guaranteed by design. Not subject to product testing.





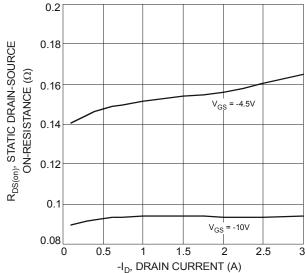


Fig. 3 On-Resistance vs. Drain Current and Gate Voltage

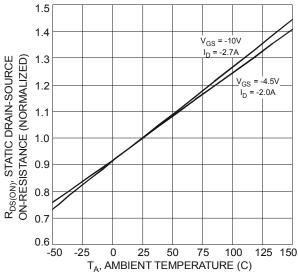
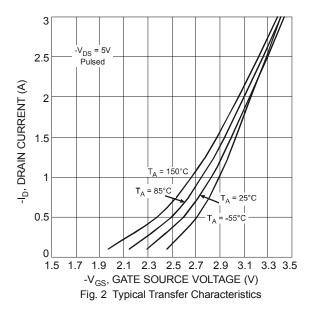


Fig. 5 Normalized Static Drain-Source On-Resistance vs. Ambient Temperature



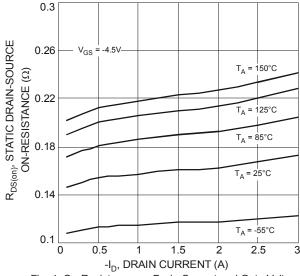
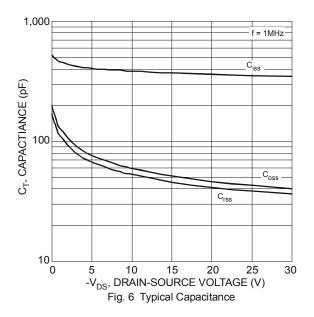


Fig. 4 On-Resistance vs. Drain Current and Gate Voltage





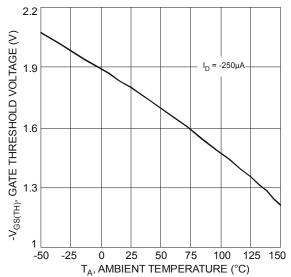
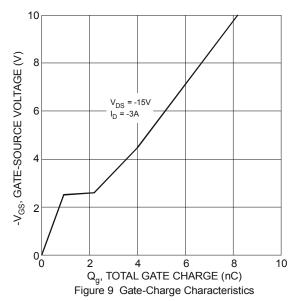


Fig. 7 Gate Threshold Voltage vs. Ambient Temperature



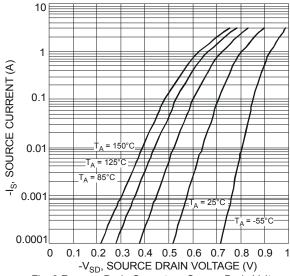
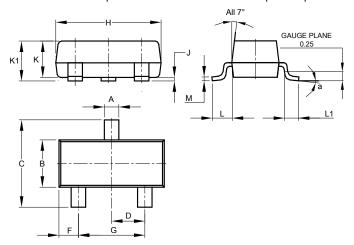


Fig. 8 Reverse Drain Current vs. Source-Drain Voltage

# **Package Outline Dimensions**

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

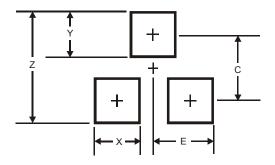


SOT23					
Dim	Min	Max	Тур		
Α	0.37	0.51	0.40		
В	1.20	1.40	1.30		
С	2.30	2.50	2.40		
D	0.89	1.03	0.915		
F	0.45	0.60	0.535		
G	1.78	2.05	1.83		
Н	2.80	3.00	2.90		
J	0.013	0.10	0.05		
K	0.890	1.00	0.975		
K1	0.903	1.10	1.025		
L	0.45	0.61	0.55		
L1	0.25	0.55	0.40		
М	0.085	0.150	0.110		
α	8°				
All	All Dimensions in mm				



# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
E	1.35

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