



DMN3051L

## **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub>	Ι <sub>D</sub> T <sub>A</sub> = +25°C
30V	38mΩ @ V <sub>GS</sub> = -10V	5.8A
300	64mΩ @ V <sub>GS</sub> = -4.5V	4.5A

#### Description

This new generation MOSFET has been designed to minimize the on-state resistance ( $R_{DS(ON)}$ ) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

# Applications

- Load Switch
- DC-DC Converters
- Power Management Functions

## **Features and Benefits**

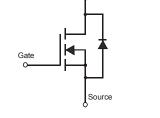
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

## **Mechanical Data**

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound.
  UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Weight: 0.008 grams (approximate)

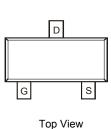


Top View



SOT23 Drain

Equivalent Circuit



## Ordering Information (Note 4)

Part Number	Case	Packaging
DMN3051L-7	SOT23	3000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

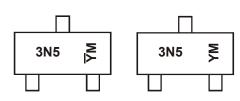
2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html

Shanghai A/T Site

# **Marking Information**



Chengdu A/T Site

 $\begin{array}{l} 3N5 = \mbox{Product Type Marking Code} \\ YM = \mbox{Date Code Marking for SAT (Shanghai Assembly/ Test site)} \\ \overline{Y}M = \mbox{Date Code Marking for CAT (Chengdu Assembly/ Test site)} \end{array}$ 

Y or  $\overline{Y}$  = Year (ex: A = 2013)

M = Month (ex: 9 = September)

Date Code Key

Year	2007	2008	2009	2010	201	1 20	)12	2013	2014	2015	2016	2017
Code	U	V	W	Х	Y		Z	А	В	С	D	E
Month	Jan	Feb	Mar	Apr	May	Jun	Ju	I Au	g Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	Ν	D



# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	V <sub>DSS</sub>	30	V		
Gate-Source Voltage	V <sub>GSS</sub>	±20	V		
	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	4.5 3.5	А
Continuous Drain Current (Note 6) V <sub>GS</sub> = 10V	t<5s	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	5.8 4.9	А
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	I <sub>DM</sub>	20	A		
Maximum Body Diode Forward Current (Note 6)			Is	2	A

# **Thermal Characteristics**

Characteristic	Symbol	Value	Units		
Tatal Dawar Dissinction (Nata 5)	T <sub>A</sub> = +25°C	P	0.7	w	
Total Power Dissipation (Note 5)	$T_A = +70^{\circ}C$ $P_D$		0.44	vv	
Thermal Desistance, Junction to Ambient (Note 5)	Steady state	D	182	°C/W	
Thermal Resistance, Junction to Ambient (Note 5)	t < 5s	$R_{ extsf{ heta}JA}$	109		
Tatal Dawar Dissinction (Nata 6)	T <sub>A</sub> = +25°C	D	1.4	W	
Total Power Dissipation (Note 6)	T <sub>A</sub> = +70°C	PD	0.85		
Thermal Registeres, Junction to Ambient (Note 6)	Steady state	D	94		
Thermal Resistance, Junction to Ambient (Note 6)	t < 5s	t < 5s R <sub>0JA</sub>		°C/W	
Thermal Resistance, Junction to Case (Note 6)	$R_{\theta JC}$	25			
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C	

# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

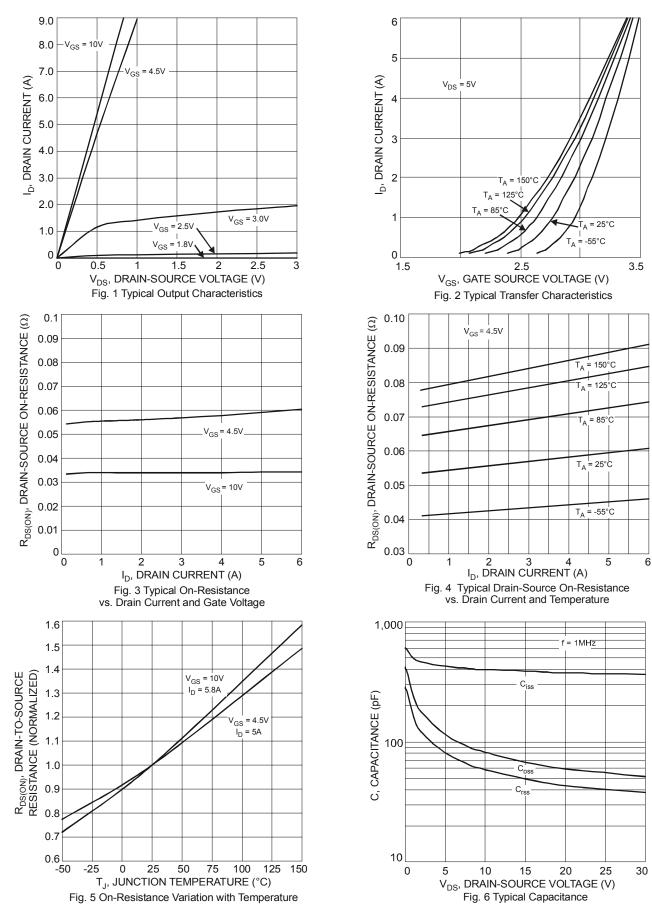
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)	Symbol	IVIIII	тур	Wax	Unit	Test condition
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30			V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250µA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>			800	nA	$V_{DS} = 28V, V_{GS} = 0V$
Gate-Body Leakage	I <sub>GSS</sub>			±80 ±800	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$ $V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	1.3	1.9	2.2	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>		33 54	38 64	mΩ	V <sub>GS</sub> = 10V, I <sub>D</sub> = 5.8A V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 5.0A
Forward Transconductance	Y <sub>fs</sub>	_	5		S	V <sub>DS</sub> = 5V, I <sub>D</sub> = 3.1A
Source-Drain Diode Forward Voltage	V <sub>SD</sub>	_	0.78	1.16	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 2.0A
DYNAMIC CHARACTERISTICS (Note 8)					_	
Input Capacitance	C <sub>iss</sub>	_	424	_	pF	
Output Capacitance	C <sub>oss</sub>	_	115	_	pF	−V <sub>DS</sub> = 5V, V <sub>GS</sub> = 0V −f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	81	_	pF	
Gate Resistance	Rg	_	1.51		Ω	$V_{DS}$ = 0V, $V_{GS}$ = 0V, f = 1MHz
Total Gate Charge	Qg	_	9.0		nC	
Gate-Source Charge	Q <sub>gs</sub>	_	1.3	_	nC	$V_{GS}$ = 10V, $V_{DS}$ = 15V, $I_D$ = 5.8A
Gate-Drain Charge	Q <sub>gd</sub>	_	1.3	_	nC	
Turn-On Delay Time	t <sub>D(on)</sub>	_	3.4		ns	
Turn-On Rise Time	tr	_	6.2		ns	V <sub>DD</sub> = 15V, V <sub>GS</sub> = 10V,
Turn-Off Delay Time	t <sub>D(off)</sub>		13.9		ns	$R_L$ = 2.6Ω, $R_G$ = 3Ω
Turn-Off Fall Time	t <sub>f</sub>		2.8	_	ns	

5. Device mounted on FR-4 PCB with minimum recommended pad layout, single sided. 6. Device mounted on 1" x 1" FR-4 PCB with high coverage 2 oz. Copper, single sided. Notes:

Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to production testing.

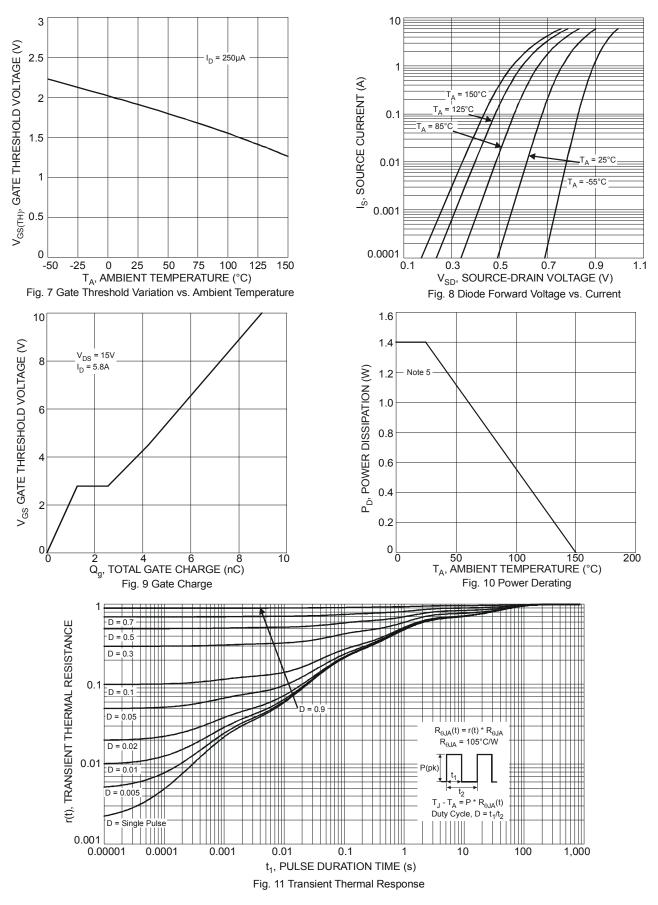








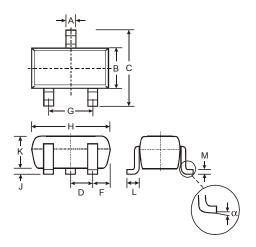
# DMN3051L





# **Package Outline Dimensions**

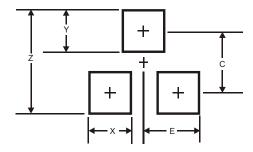
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SOT23						
Dim	Min	Max				
Α	0.37	0.51				
В	1.20	1.40				
С	2.30	2.50				
D	0.89	1.03				
F	0.45	0.60				
G	1.78	2.05				
Н	2.80	3.00				
J	0.013	0.10				
Κ	0.903 1.10					
L	0.45	0.61				
М	0.085	0.180				
α	0°	8°				
All Dir	nensions	in mm				

# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for latest version.



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
E	1.35



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