

# NOT RECOMMENDED FOR NEW DESIGN **USE DSS4160T**



# NLS160

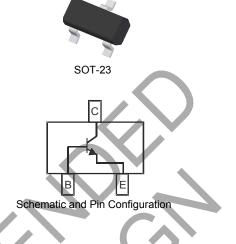
LOW V<sub>CE(SAT)</sub> NPN SURFACE MOUNT TRANSISTOR

## Features

- **Epitaxial Planar Die Construction** .
- Complementary PNP Type Available (DPLS160) •
- Surface Mount Package Suited for Automated Assembly
- Lead Free/RoHS Compliant (Note 1)
- "Green Device" (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

# **Mechanical Data**

- Case: SOT-23 •
- Case Material: Molded Plastic, "Green" Molding • Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.008 grams (approximate)



# **Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

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Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	80	V
Collector-Emitter Voltage	V <sub>CEO</sub>	60	V
Emitter-Base Voltage	VEBO	5	V
Collector Current - Continuous	l <sub>c</sub>	1	А
Peak Pulse Collector Current	ICM	2	A
Base Current (DC)	Iв	300	mA

# **Thermal Characteristics**

Notes:

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3) @ T <sub>A</sub> = 25°C	PD	300	mW
Thermal Resistance, Junction to Ambient (Note 3) @ T <sub>A</sub> = 25°C	$R_{ heta JA}$	417	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	0°

No purposefully added lead. 1.

2.

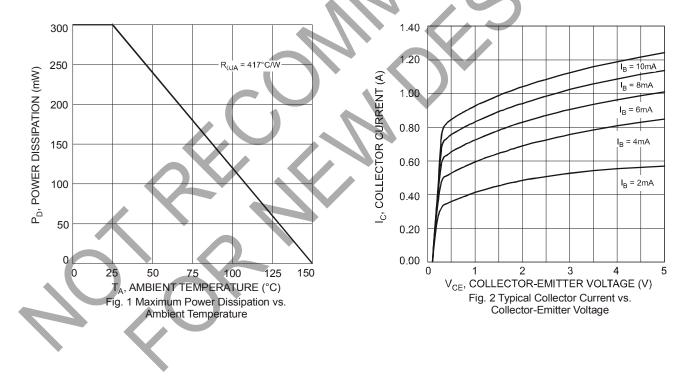
Diode's Inc.s "Green" policy can be found on our website at http://www.diodes.com/products/lead\_free/index.php. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on page 4 or in Diodes Inc. suggested pad layout document 3. AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.



# **Electrical Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

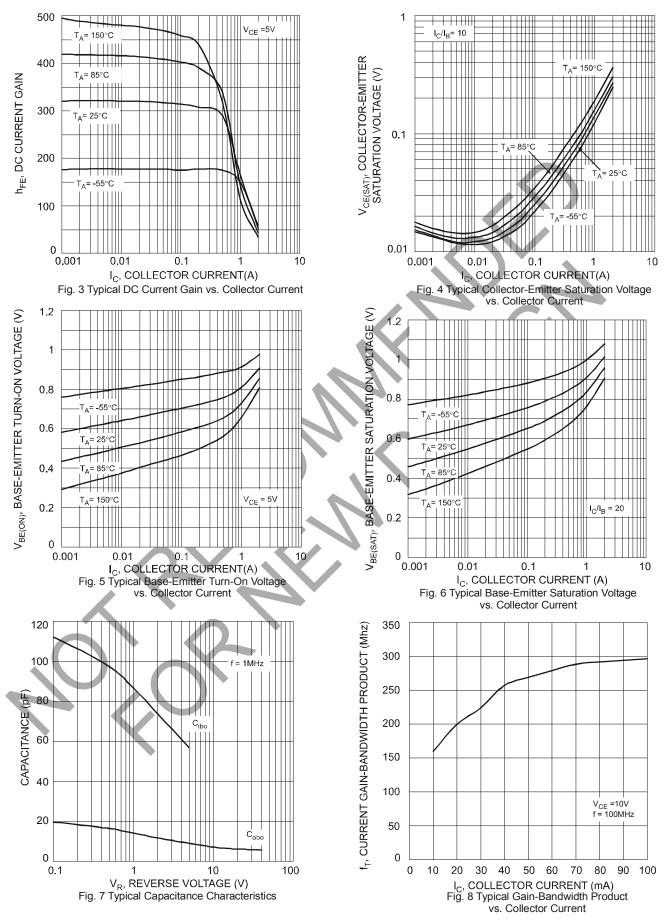
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 4)	Cymbol	NVIII I	тур	Max	Onit	Test condition
Collector-Base Breakdown Voltage	V <sub>(BR)CBO</sub>	80			V	$I_{\rm C} = 100 \mu A, I_{\rm E} = 0$
Collector-Emitter Breakdown Voltage	V(BR)CEO	60			V	$I_{\rm C} = 10 {\rm mA}, I_{\rm B} = 0$
Emitter-Base Breakdown Voltage	V <sub>(BR)EBO</sub>	5	_	_	V	$I_{\rm E} = 100 \mu A, I_{\rm C} = 0$
Collector Cutoff Current	I <sub>CBO</sub>	_	_	100 50	nA μA	$V_{CB} = 60V, I_E = 0$ $V_{CB} = 60V, I_E = 0, T_A = 150^{\circ}C$
Collector Cutoff Current	I <sub>CES</sub>			100	nA	$V_{CE} = 60V, V_{BE} = 0$
Emitter Cutoff Current	I <sub>EBO</sub>	_		100	nA	$V_{EB} = 5V, I_{C} = 0$
ON CHARACTERISTICS (Note 4)					_	
DC Current Gain	h <sub>FE</sub>	250 200 100	320 280 165		V	$V_{CE} = 5V, I_C = 1mA$ $V_{CE} = 5V, I_C = 500mA$ $V_{CE} = 5V, I_C = 1A$
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>		80 80 140	110 140 250	mV	$I_{C} = 100$ mA, $I_{B} = 1$ mA $I_{C} = 500$ mA, $I_{B} = 50$ mA $I_{C} = 1$ A, $I_{B} = 100$ mA
Collector-Emitter Saturation Resistance	R <sub>CE(SAT)</sub>		140	250	mΩ	$I_{\rm C} = 1$ A, $I_{\rm B} = 100$ mA
Base-Emitter Saturation Voltage	V <sub>BE(SAT)</sub>	_	0.91	1,1	V	$I_{\rm C} = 1$ A, $I_{\rm B} = 50$ mA
Base-Emitter Turn On Voltage	V <sub>BE(ON)</sub>	_	0.81	0.9	V	$V_{CE} = 5V, I_{C} = 1A$
SMALL SIGNAL CHARACTERISTICS						
Output Capacitance	C <sub>obo</sub>		7	10	pF	V <sub>CB</sub> = 10V, f = 1.0MHz
Current Gain-Bandwidth Product	f <sub>T</sub>	150	270	4	MHz	V <sub>CE</sub> = 10V, I <sub>C</sub> = 50mA, f = 100MHz

Notes: 4. Measured under pulsed conditions. Pulse width =  $300\mu$ s. Duty cycle  $\leq 2\%$ .



# DECES

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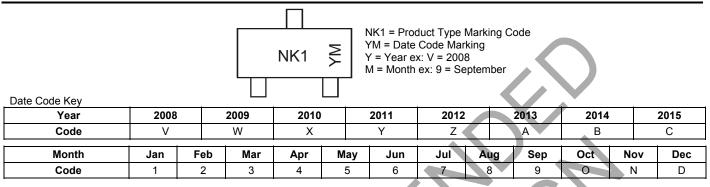
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# Ordering Information (Note 5)

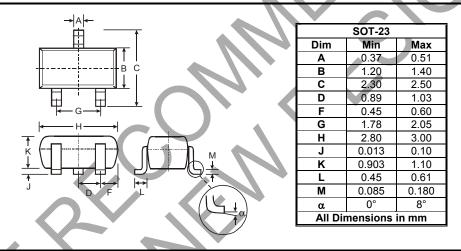
Device	Packaging	Shipping
DNLS160-7	SOT-23	3000/Tape & Reel

#### Notes: 5. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

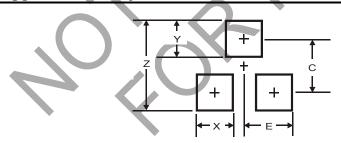
# **Marking Information**



# **Package Outline Dimensions**



# Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
Е	1.35

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