



N-CHANNEL ENHANCEMENT MODE MOSFET

Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2) Halogen and Antimony Free. "Green" Device (Note 3) Qualified to AEC-Q101 Standards for High Reliability

Case Material: Molded Plastic, "Green" Molding Compound.

UL Flammability Classification Rating 94V-0

Moisture Sensitivity: Level 1 per J-STD-020 Terminal Connections: See Diagram Below

Weight: 0.072 grams (approximate)

Features and Benefits
Low On-Resistance
Low Input Capacitance
Fast Switching Speed

Mechanical Data

Case: SO-8

Product Summary

V _{(BR)DSS}	R _{DS(ON)} max	l _D max T _A = +25°C		
	14mΩ @ V _{GS} = 10V	10A		
30V	20mΩ @ V _{GS} = 4.5V	8A		

Description and Applications

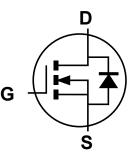
SO-8

Top View

This MOSFET has been designed to minimize the on-state resistance ($R_{DS(on)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Backlighting
- Power Management Functions
- DC-DC Converters





Equivalent circuit

Ordering Information (Note 4)

Part Number	Case	Packaging
DMN4468LSS-13	SO-8	2500 / Tape & Reel

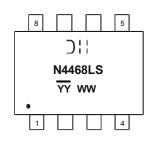
Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

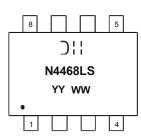
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



Chengdu A/T Site



Shanghai A/T Site

);; = Manufacturer's Marking
N4468LS = Product Type Marking Code
YYWW = Date Code Marking
YY or YY = Year (ex: 13 = 2013)
WW = Week (01 - 53)
YY = Date Code Marking for SAT (Shanghai Assembly/ Test site)
YY = Date Code Marking for CAT (Chengdu Assembly/ Test site)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteri	stic		Symbol	Value	Unit
Drain-Source Voltage			Vdss	30	V
Gate-Source Voltage			Vgss	±20	V
Continuous Drain Current (Note 5)	Steady State	TA = +25°C TA = +70°C	lD	10 9	A
Pulsed Drain Current (10µs pulse, duty cycle = 1%)			ldм	50	A

Thermal Characteristics

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	PD	1.52	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{ heta}$ JA	82	°C/W
Thermal Resistance, Junction to Case (Note 6)	$R_{\theta Jc}$	8.2	°C/W
Operating and Storage Temperature Range	T _{J,} T _{STG}	-55 to +150	°C

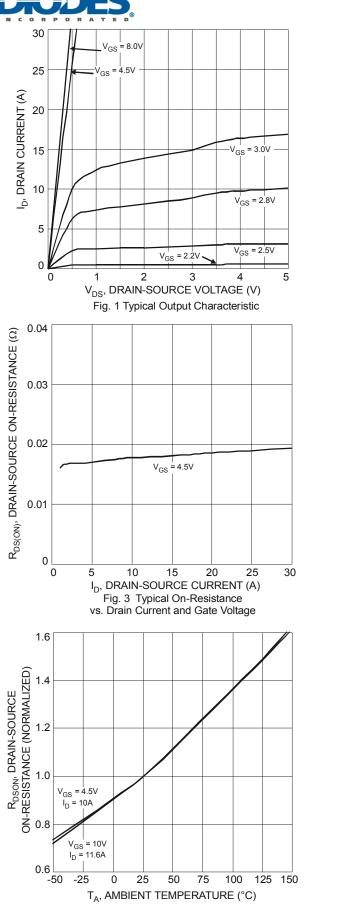
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage	BV _{DSS}	30	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}		_	1.0	μA	V _{DS} = 30V, V _{GS} = 0V	
Gate-Source Leakage	I _{GSS}		_	±100	nA	V_{GS} = ±20V, V_{DS} = 0V	
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage	V _{GS(th)}	1.05	-	1.95	V	V_{DS} = V_{GS} , I_D = 250 μ A	
Static Drain-Source On-Resistance	Proven		11	14	mΩ	V _{GS} = 10V, I _D = 11.6A	
	RDS (ON)	R _{DS (ON)} —	15	20	11152	V _{GS} = 4.5V, I _D = 10A	
Forward Transfer Admittance	Y _{fs}		8	_	S	V _{DS} = 5V, I _D = 11.6A	
Diode Forward Voltage	V _{SD}		0.73	0.95	V	$V_{GS} = 0V, I_{S} = 1A$	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	Ciss	_	867	—	pF	− V _{DS} = 10V, V _{GS} = 0V, − f = 1.0MHz	
Output Capacitance	Coss	_	85		pF		
Reverse Transfer Capacitance	C _{rss}		81		pF		
Gate Resistance	R _g		1.39	—	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1MHz	
Total Gate Charge	Qg		18.85	_	nC	V _{GS} = 10V, V _{DS} = 15V, I _D =11.6A	
Gate-Source Charge	Q _{gs}		2.59	—	nC		
Gate-Drain Charge	Q _{gd}		6.15	—	nC		
Turn-On Delay Time	t _{D(on)}		5.46	_	ns	V _{DD} = 15V, V _{GS} = 10V, R _L = 1.3Ω, R _G = 3Ω, I _D = 1A	
Turn-On Rise Time	tr		14.53		ns		
Turn-Off Delay Time	t _{D(off)}		18.84	_	ns		
Turn-Off Fall Time	t _f	_	6.01	_	ns		

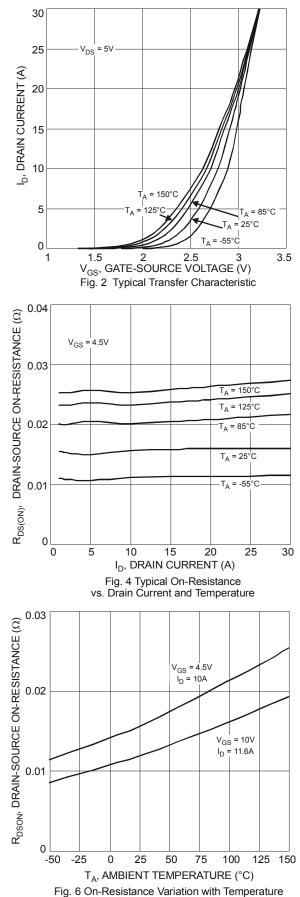
5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate. Notes:

6. Short duration pulse test used to minimize self-heating effect.
 7. Guaranteed by design. Not subject to product testing.

DMN4468LSS

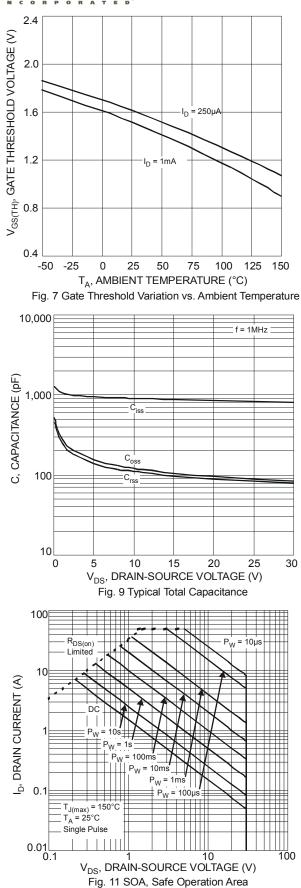






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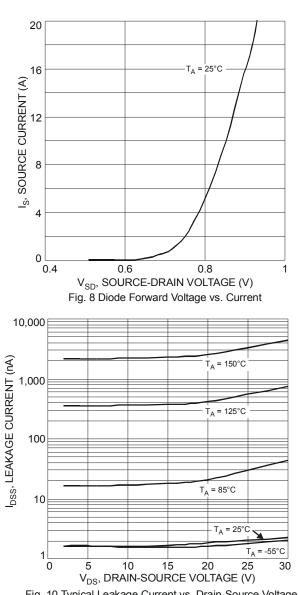
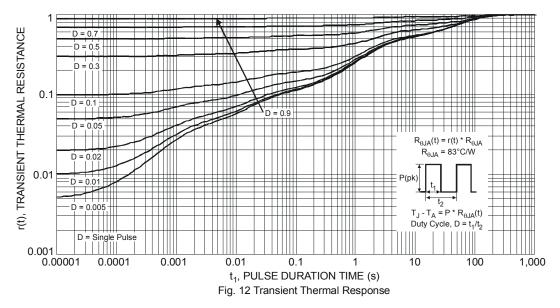


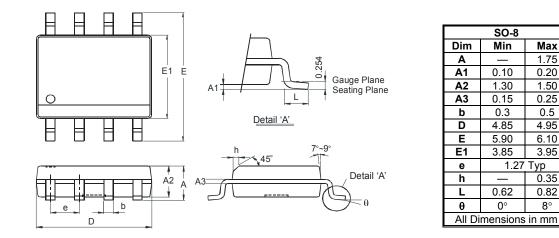
Fig. 10 Typical Leakage Current vs. Drain-Source Voltage





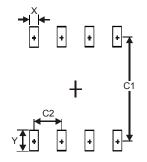
Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
X	0.60
Y	1.55
C1	5.4
C2	1.27



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