



Product Summary

V _{(BR)DSS}	R _{DS(ON)} max	l _D max T _A = +25°C
	28mΩ @ V _{GS} = 10V	5.8A
30V	42mΩ @ V _{GS} = 4.5V	4.8A
	82mΩ @ V _{GS} = 3V	2.0A

Description

This MOSFET has been designed to minimize the on-state resistance $(R_{DS(ON)})$ and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Battery Charging
- Power Management Functions
- DC-DC Converters
- Portable Power Adaptors



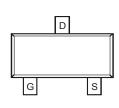
N-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 (23)
- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (approximate)



Top View

Internal Schematic

Top View

Ordering Information (Note 4 & 5)

Part Number	Compliance	Case	Packaging
DMN3404L-7	Standard	SOT23	3000/Tape & Reel
DMN3404LQ-7	Automotive	SOT23	3000/Tape & Reel

Drain

Gate

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

 See http://www.diodes.com/quality/lead_free.htmlfor more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

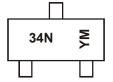
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive and standard products are electrically and thermally

- the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product_compliance_definitions/.
- 5. For packaging details, go to Diodes website at http://www.diodes.com/products/packages.html.

Marking Information

		_
34N	ΨM	
		1



Shanghai A/T Site

34N = Product Type Marking Code

 $\frac{YM}{YM} = \text{Date Code Marking for SAT (Shanghai Assembly/ Test site)} \\ \frac{YM}{YM} = \text{Date Code Marking for CAT (Chengdu Assembly/ Test site)} \\ Y \text{ or } \overline{Y} = \text{Year (ex: A = 2013)} \\ M = \text{Month (ex: 9 = September)} \\ \end{cases}$

Chengdu A/T Site

Date Code Key												
Year	200	9	2010		2011	20)12	2013		2014	1	2015
Code	W		Х		Y		Z	А		В		С
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	Ν	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units	
Drain-Source Voltage (Note	6 & 7)		V _{DSS}	30	v	
Gate-Source Voltage		V _{GSS}	±20	V		
Continuous Drain Current (Note 6) V_{GS} = 10V	Steady State	$T_A = -40^{\circ}C$ $T_A = +25^{\circ}C$ $T_A = +85^{\circ}C$	۱ _D	4.6 4.2 3.0	A	
Continuous Drain Current (Note 7) V_{GS} = 10V	Steady State	$T_A = -40^{\circ}C$ $T_A = +25^{\circ}C$ $T_A = +85^{\circ}C$	ID	6.2 5.8 4.0	A	
Continuous Drain Current (Note 7) V_{GS} = 4.5V	Steady State	$T_A = -40^{\circ}C$ $T_A = +25^{\circ}C$ $T_A = +85^{\circ}C$	ID	5.2 4.8 3.2	A	
Continuous Drain Current (Note 7) V_{GS} = 3V	Steady State	$T_A = -40^{\circ}C$ $T_A = +25^{\circ}C$ $T_A = +85^{\circ}C$	ID	2.2 2.0 1.0	A	
Pulsed Drain Current	·	I _{DM}	30	А		

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 6)	PD	0.72	W
Thermal Resistance, Junction to Ambient @T _A = +25°C	R _{0JA}	173	°C/W
Power Dissipation (Note 7)	PD	1.4	W
Thermal Resistance, Junction to Ambient @T _A = +25°C	R _{0JA}	90	°C/W
Operating and Storage Temperature Range	T _{J,} T _{STG}	-55 to +150	°C

Notes:

6. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.7. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.

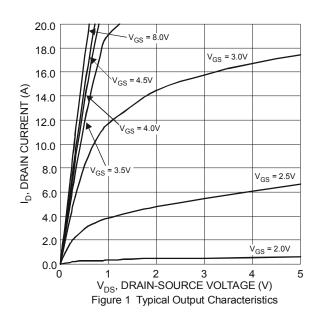


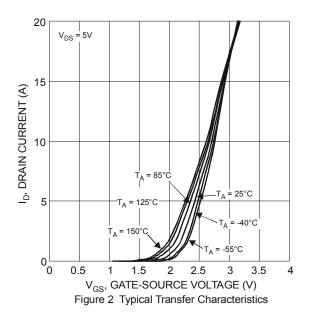
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	30		_	V	V _{GS} = 0V, I _D = 250µA
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	_		1.0	μA	V _{DS} = 30V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	_		±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(th)}	1.0	1.5	2.0	V	V_{DS} = V_{GS} , I_D = 250 μ A
Static Drain-Source On-Resistance T _{.1} = -40°C (Note 9)	D	—	23	27	_	V _{GS} = 4.5V, I _D = 4.8A
Static Drain-Source On-Resistance $T_J = -40^{\circ} \text{C}$ (Note 9)	R _{DS(ON)}	_	57	74	_	V _{GS} =3V, I _D =2A
		_	24	28		V _{GS} = 10V, I _D = 5.8A
Static Drain-Source On-Resistance TJ = +25°C	R _{DS(ON)}	_	33	42	mΩ	V _{GS} = 4.5V, I _D = 4.8A
		_	63	82		V _{GS} =3V, I _D =2A
Static Drain-Source On-Resistance T _J = +85°C (Note 9)	R _{DS(ON)}	_	71	95	mΩ	V _{GS} =3V, I _D =2A
Forward Transfer Admittance	Y _{fs}	_	10	—	S	V _{DS} = 5V, I _D = 5.8A
Diode Forward Voltage	V _{SD}	_	0.75	1.0	V	V _{GS} = 0V, I _S = 1A
DYNAMIC CHARACTERISTICS (Note 10)						
Input Capacitance	Ciss	—	498	—	pF	
Output Capacitance	Coss	—	52	—	pF	V _{DS} = 15V, V _{GS} = 0V, f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	—	45	—	pF	
Gate Resistance	Rg	—	1.75	2.8	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1MHz
Total Gate Charge (V _{GS} = 3V)	Qg	_	3.8	5.3	nC	V_{GS} = 3V, V_{DS} = 15V, I_{D} = 1A
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	5.3	7.5	nC	
Total Gate Charge (V _{GS} = 10V)	Qg	_	11.3	16	nC	V _{GS} = 10V/4.5V, V _{DS} = 15V,
Gate-Source Charge	Qgs	_	1.4	_	nC	I _D = 5.8A
Gate-Drain Charge	Q _{gd}	_	2.1	_	nC	
Turn-On Delay Time	t _{D(on)}	_	3.41	10	ns	
Turn-On Rise Time	tr	_	6.18	13	ns	V _{DD} = 15V, V _{GS} = 10V,
Turn-Off Delay Time	t _{D(off)}	_	13.92	28	ns	R_L = 2.6Ω, R_G = 3Ω
Turn-Off Fall Time	t _f	_	2.84	10	ns	7

Notes:

8. Short duration pulse test used to minimize self-heating effect.
9. Guaranteed by design and 25°C data. Not subject to production testing
10. Guaranteed by design. Not subject to production testing.

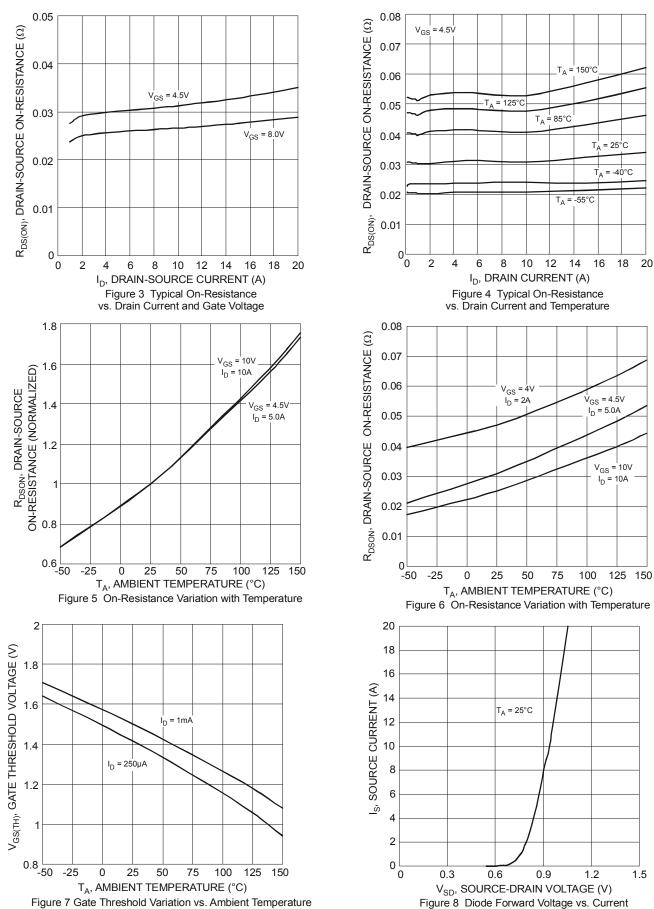




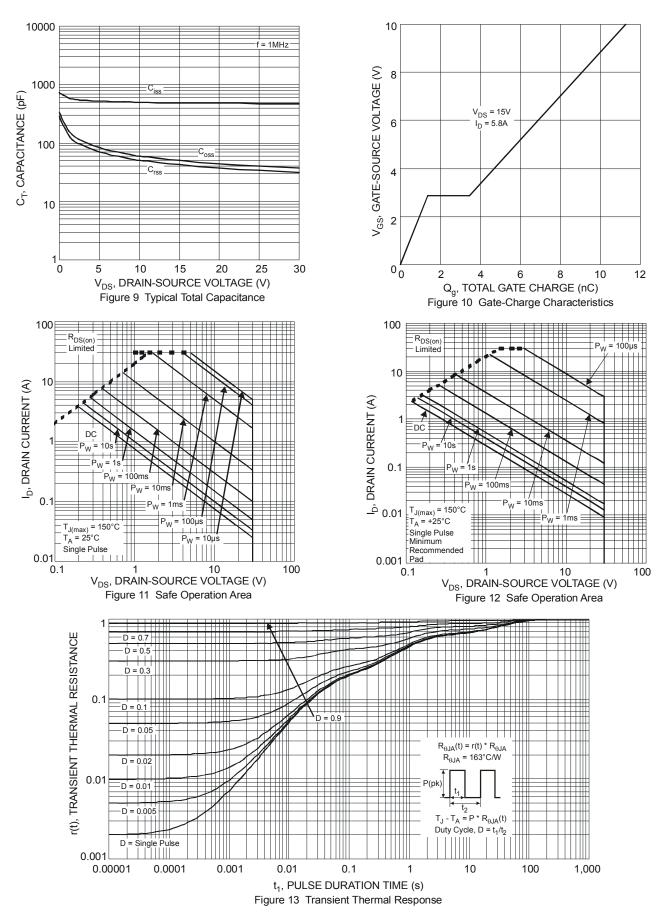




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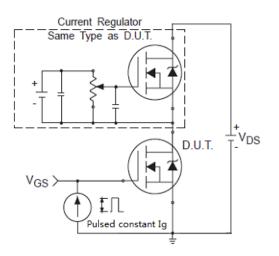


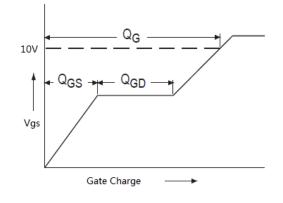




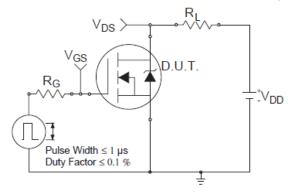


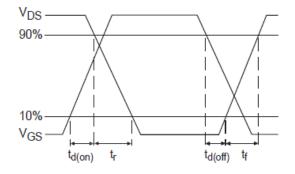
Gate Charge Test Circuit and Waveform





Switching Test Circuit and Waveform

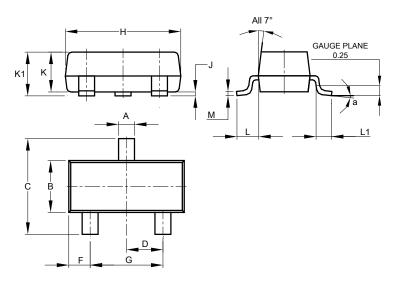






Package Outline Dimensions

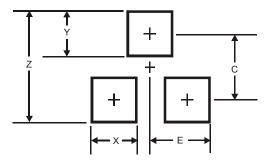
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



	SOT23							
Dim	Min	Max	Тур					
Α	0.37	0.51	0.40					
В	1.20	1.40	1.30					
С	2.30	2.50	2.40					
D	0.89	1.03	0.915					
F	0.45	0.60	0.535					
G	1.78	2.05	1.83					
Н	2.80	3.00	2.90					
J	0.013	0.10	0.05					
К	0.890	1.00	0.975					
K1	0.903	1.10	1.025					
L	0.45	0.61	0.55					
L1	0.25	0.55	0.40					
М	0.085	0.150	0.110					
α	8°							
All	All Dimensions in mm							

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for latest version.



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
E	1.35



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