



P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(ON)} max	I _D max T _A = 25°C
	54mΩ @ V _{GS} = -4.5V	-2.5A
-20V	90mΩ @ V _{GS} = -1.8V	-1.8A

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **ESD Protected Up To 3kV**
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device, Halogen and Antimony Free (Note 2)
 - Qualified to AEC-Q101 Standards for High Reliability

Description and Applications

This MOSFET has been designed to minimize the on-state resistance (RDS(on)) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Backlighting
- **Power Management Functions**
- DC-DC Converters

Mechanical Data

- Case: X2-DFN2015-3
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (approximate)

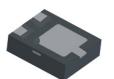
X2-DFN2015-3



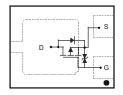
ESD PROTECTED TO 3kV



Top View



Bottom View



Internal Schematic

Ordering Information (Note 3)

Part Number	Case	Packaging
DMP2069UFY4-7	X2-DFN2015-3	3000/Tape & Reel

Notes:

- 1. No purposefully added lead.
- 2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com.
- 3. For packaging details, go to our website at http://www.diodes.com.

Marking Information

29P **YM**

29P = Marking Code YM = Date Code Marking Y = Year (ex: W = 2009)M = Month (ex: 9 = September)

Date Code Key

	Year	2009	9	2010		2011	20	12	2013		2014	2	2015
	Code	W		Х		Υ	Z	7	Α		В		С
Ī	Manth												
	Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings @TA = 25°C unless otherwise specified

Characte	eristic		Symbol	Value	Units
Drain-Source Voltage			V_{DSS}	-20	V
Gate-Source Voltage			V _{GSS}	±8	V
Continuous Drain Current (Note 4) Steady $T_A = 25^{\circ}C$ State $T_A = 70^{\circ}C$			I _D	-2.5 -2.2	А
Pulsed Drain Current (Note 5)			I _{DM}	-12	А

Thermal Characteristics

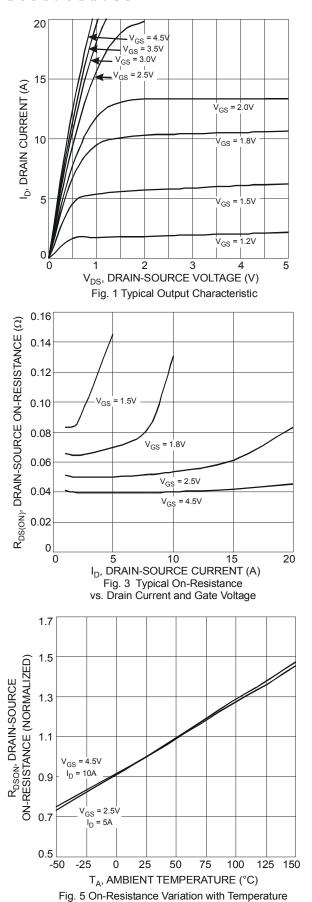
Characteristic	Symbol	Value	Unit
Power Dissipation (Note 4)	P_{D}	0.53	W
Thermal Resistance, Junction to Ambient @T _A = 25°C	$R_{\theta JA}$	231	°C/W
Operating and Storage Temperature Range	$T_{J_1}T_{STG}$	-55 to +150	°C

Electrical Characteristics @TA = 25°C unless otherwise specified

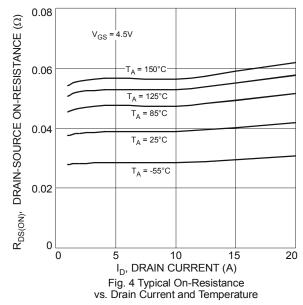
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 6)					ā.			
Drain-Source Breakdown Voltage	BV_{DSS}	-20	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$		
Zero Gate Voltage Drain Current T _J = 25°C	I _{DSS}	_	_	-1.0	μΑ	$V_{DS} = -20V, V_{GS} = 0V$		
Gate-Source Leakage	I_{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 8V, V_{DS} = 0V$		
ON CHARACTERISTICS (Note 6)								
Gate Threshold Voltage	V _{GS(th)}	-0.3	-0.55	-1.0	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$		
			36	54		$V_{GS} = -4.5V$, $I_D = -2.5A$		
Static Drain-Source On-Resistance	R _{DS} (ON)	_	46	69	mΩ	$V_{GS} = -2.5V$, $I_D = -2.2A$		
			60	90		$V_{GS} = -1.8V$, $I_D = -2.0A$		
Forward Transfer Admittance	Y _{fs}	_	8	_	S	$V_{DS} = -5V$, $I_{D} = -2.5A$		
DYNAMIC CHARACTERISTICS (Note 7)					_			
Input Capacitance	C _{iss}	_	214	_	pF	10)/)/ 0)/		
Output Capacitance	Coss	_	104	_	pF	$V_{DS} = -10V, V_{GS} = 0V$ - f = 1.0MHz		
Reverse Transfer Capacitance	C_{rss}	_	25	_	pF	1 = 1.0MHZ		
Gate Resistnace	R_g	_	250	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$		
SWITCHING CHARACTERISTICS (Note 7)					ā.			
Total Gate Charge	Q_{g}	_	9.1	_	nC			
Gate-Source Charge	Q_{gs}	_	1.5	_	nC	$V_{GS} = -4.5V$, $V_{DS} = -10V$, $I_{D} = -4A$		
Gate-Drain Charge	Q_{gd}	_	1.7	_	nC			
Turn-On Delay Time	t _{D(on)}	_	80.4	160	ns			
Turn-On Rise Time	t _r	_	155.1	210	ns	$V_{DS} = -10V, V_{GS} = -4.5V,$		
Turn-Off Delay Time	$t_{D(off)}$	_	688.1	1376	ns	$R_D = 2.5\Omega, R_G = 3.0\Omega$		
Turn-Off Fall Time	t _f	_	423.8	848	ns			

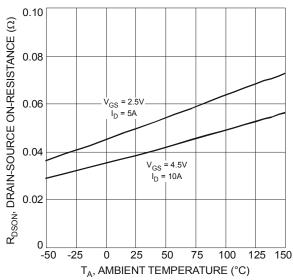
- 4. Device mounted on FR-4 PCB with minimum recommended pad layout.
- 5. Repetitive rating, pulse width limited by junction temperature.
- Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to production testing.



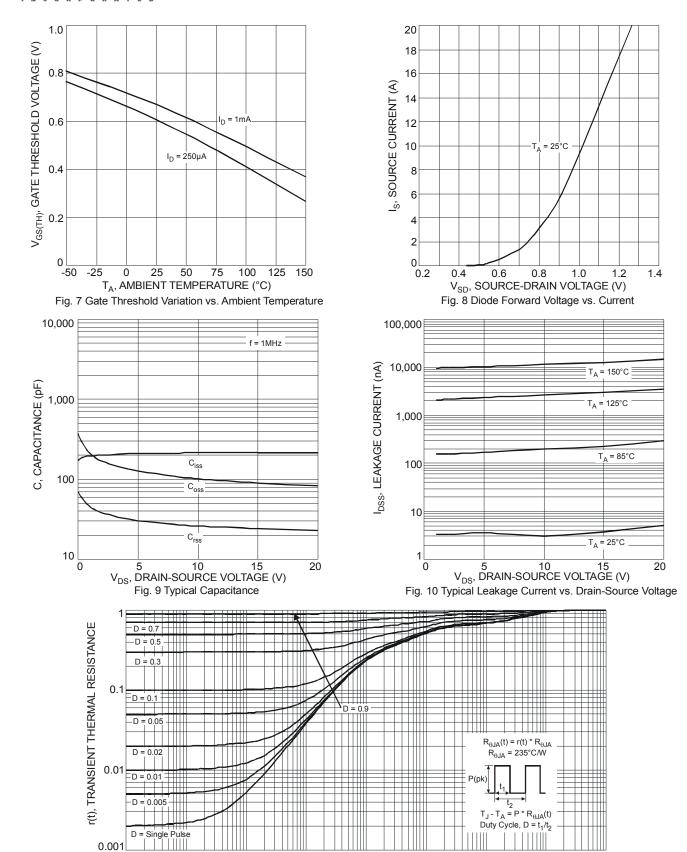


20 $V_{DS} = 5V$ 15 ID, DRAIN CURRENT (A) 10 5 $T_A = 150^\circ$ = 85°C T_A = 25°C = -55°C 0 5 1 1.5 2 2.5 3 3 $V_{\rm GS}$, GATE-SOURCE VOLTAGE (V) 0 0.5 3.5 Fig. 2 Typical Transfer Characteristic









t₁, PULSE DURATION TIME (s) Fig. 11 Transient Thermal Response

0.1

10

100

1,000

0.00001

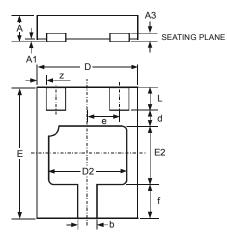
0.0001

0.001

0.01

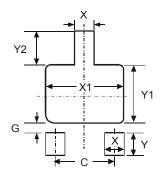


Package Outline Dimensions



>	X2-DFN2015-3							
Dim	Min	Max	Тур					
Α	_	0.40	-					
A1	0	0.05	0.02					
A3	- 1	- 1	0.13					
b	0.20	0.30	0.25					
d	_	_	0.30					
D	1.45	1.575	1.50					
D2	1.00	1.20	1.10					
е	ı	ı	0.50					
Е	1.95	2.075	2.00					
E2	0.70	0.90	0.80					
f	_	_	0.60					
١	0.25	0.35	0.30					
z	_	_	0.125					
All Dimensions in mm								

Suggested Pad Layout



Dimensions	Value (in mm)
С	1.00
G	0.15
Х	0.31
X1	1.30
Y	0.50
Y1	1.00
Y2	0.65



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