

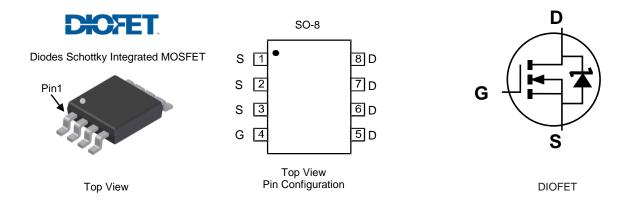
DMG4712SSS N-CHANNEL ENHANCEMENT MODE MOSFET WITH SCHOTTKY DIODE

Features

- High Density UMOS with Schottky Barrier Diode
- Low Leakage Current at High Temperature
- High Conversion Efficiency
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Utilizes Diodes' Monolithic DIOFET Technology to Increase
 Conversion Efficiency
- UIS Tested, R_G Tested
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Terminals: Finish Tin Finish Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 🕄
- Weight: 0.074 grams (Approximate)



Ordering Information (Note 4)

Part Number	Case	Packaging
DMG4712SSS-13	SO-8	2,500 / Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

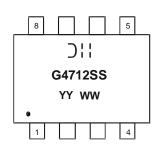
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

Notes:



)'' = Manufacturer's Marking G4712SS = Product Type Marking Code YYWW = Date Code Marking YY or YY = Year (ex: 15= 2015) WW = Week (01 - 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	30	V
Gate-Source Voltage			V _{GSS}	±12	V
Continuous Drain Current (Note 5)	Steady State	TA = +25°C TA = +85°C	ID	11.2 6.6	А
Pulsed Drain Current (Note 6)			I _{DM}	63	А
Avalanche Current (Notes 6 & 7)			I _{AR}	30	А
Repetitive Avalanche Energy (Notes 6 & 7) L = 0.1mH			E _{AR}	45	mJ

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	PD	1.55	W
Thermal Resistance, Junction to Ambient $@T_A = +25^{\circ}C$ (Note 5)	R _{0JA}	81.3	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@ T_A = +25°C, unless otherwise stated.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)						·	
Drain-Source Breakdown Voltage	BV _{DSS}	30	-	-	V	$V_{GS} = 0V, I_D = 1mA$	
Zero Gate Voltage Drain Current	I _{DSS}	-	-	100	μA	$V_{DS} = 30V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	-	-	±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(th)}	1.0	-	2.2	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance			10 11	14.0) mΩ	V _{GS} = 10V, I _D = 11.2A	
Static Drain-Source On-Resistance	R _{DS} (ON)	-		15.4	11122	$V_{GS} = 4.5 V, I_D = 10 A$	
Forward Transfer Admittance	Y _{fs}	-	23	-	S	V _{DS} = 5V, I _D = 11.2A	
Diode Forward Voltage	V _{SD}	-	0.37	0.55	V	$V_{GS} = 0V, I_{S} = 1A$	
Maximum Body-Diode + Schottky Continuous Current	Is	-	-	5	Α	-	
DYNAMIC CHARACTERISTICS (Note 9)						-	
Input Capacitance	Ciss	-	2,296	-	pF		
Output Capacitance	Coss	-	164	-	pF	V _{DS} = 15V, V _{GS} = 0V, f = 1.0MHz	
Reverse Transfer Capacitance	Crss	-	120	-	pF	T = T.000112	
Gate Resistance	Rq	-	1.3	-	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge (V _{GS} = 10V)	Qg	-	45.7	-	nC	$V_{DS} = 15V, V_{GS} = 10V, I_D = 11.2A$	
Total Gate Charge (V _{GS} = 4.5V)	Qq	-	19.3	-	nC		
Gate-Source Charge	Q _{qs}	-	5.0	-	nC		
Gate-Drain Charge	Q _{qd}	-	2.9	-	nC		
Turn-On Delay Time	t _{D(on)}	-	5.5	-	ns		
Turn-On Rise Time	tr	-	24.4	-	ns	$V_{GS} = 10V, V_{DS} = 15V,$	
Turn-Off Delay Time	t _{D(off)}	-	33.1	-	ns	$R_G = 3\Omega, R_L = 1.2\Omega$	
Turn-Off Fall Time	t _f	-	6.6	-	ns		

5. Device mounted on FR-4 PCB with minimum recommended pad layout. The value in any given application depends on the user's specific board design. Notes:

6. Repetitive rating, pulse width limited by junction temperature. 7. I_{AR} and E_{AR} rating are based on low frequency and duty cycles to keep $T_J = 25^{\circ}$ C. L = 0.1mH, $V_{DD} = 0$ V, $R_G = 0\Omega$, rated $V_{DS} = 30$ V, and $V_{GS} = 10$ V.

8. Short duration pulse test used to minimize self-heating effect.

9. Guaranteed by design. Not subject to product testing.



′_{GS} = 25°C

2.5

 $T_A = 150^{\circ}C$

T_A = 125°C $T_A = 85^{\circ}C$

 $T_A = 25^{\circ}C$

 $T_A = -55^{\circ}C$

25

30

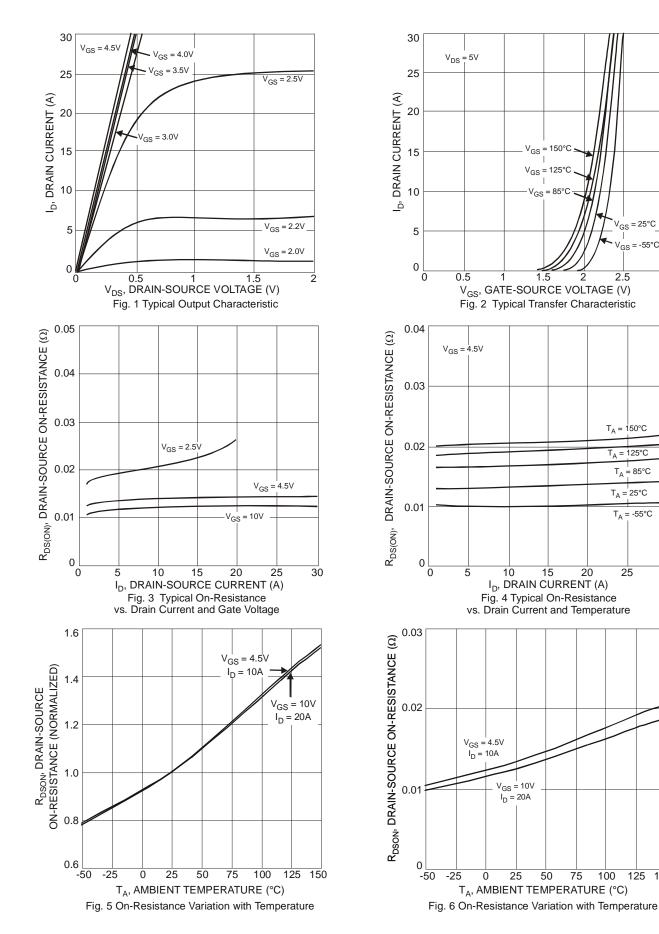
20

75

100

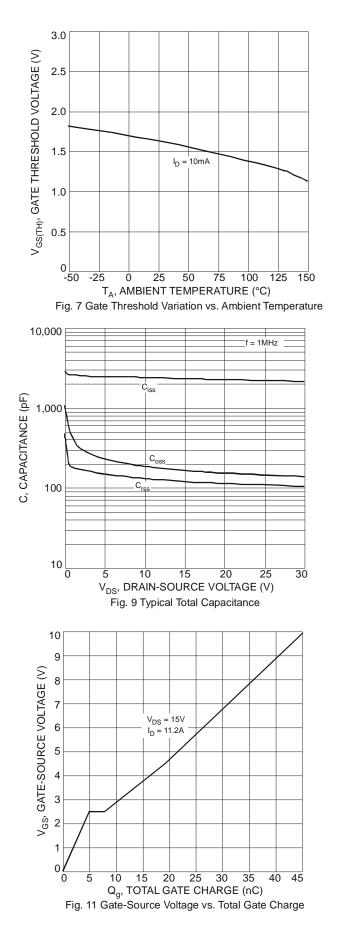
-55°C ′_{GS} =

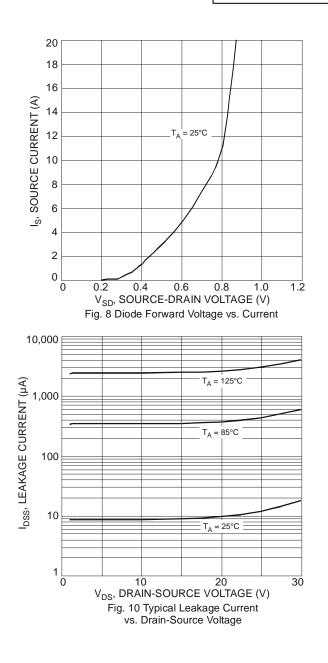
3



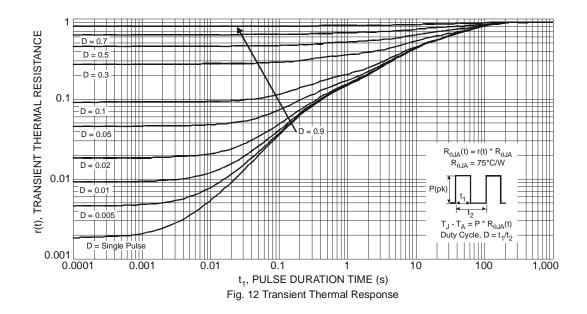
125 150





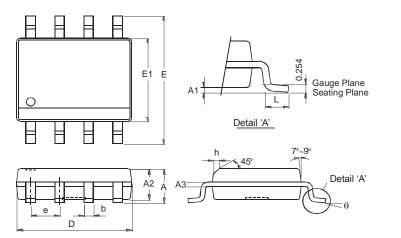






Package Outline Dimensions

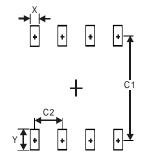
Please see AP02001 at http://www.diodes.com/_files/datasheets/ap02001.pdf for the latest version.



SO-8					
Dim	Min	Max			
Α	-	1.75			
A1	0.10	0.20			
A2	1.30	1.50			
A3	0.15	0.25			
b	0.3	0.5			
D	4.85	4.95			
Е	5.90	6.10			
E1	3.85	3.95			
е	1.27 Typ				
h	-	0.35			
L	0.62	0.82			
θ	0°	8°			
All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/_files/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Х	0.60
Y	1.55
C1	5.4
C2	1.27



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