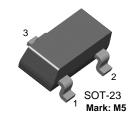


### **BSR57**

# N-Channel Low-Frequency Low-Noise Amplifier

• This device is designed for low-power chopper or switching application sourced from process 51



1. Drain 2. Source 3. Gate

### Absolute Maximum Ratings $T_C=25^{\circ}C$ unless otherwise noted

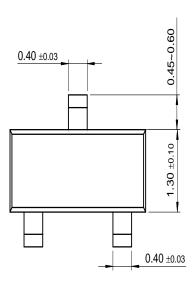
Symbol	Parameter	Value	Units
$V_{DGO}$	Drain-Gate Voltage	40	V
V <sub>GSO</sub>	Gate-Source Voltage	- 40	V
I <sub>GF</sub>	Forward Gate Current	50	mA
P <sub>tot</sub>	Total Power Dissipation up to T <sub>amb</sub> =40°C	250	mW
T <sub>STG</sub>	Storage Temperature Range	- 55 ~ 150	°C
T <sub>J</sub>	Junction Temperature	150	°C

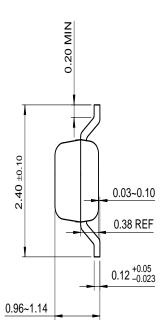
### Electrical Characteristics $\rm T_{C}{=}25^{\circ}C$ unless otherwise noted

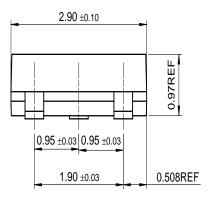
Symbol	Parameter	Test Condition	Min.	Max.	Units
BV <sub>GSS</sub>	Gate-Source Voltage	$V_{DS} = 0V, I_{C} = 1.0 \mu A$	40		V
I <sub>GSS</sub>	Gate Reverse Current	$V_{GS} = 20V, V_{DS} = 0V$		1.0	nA
I <sub>DSS</sub>	Zero-Gate Voltage Drain Current	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V	20	100	mA
V <sub>GS</sub> (off)	Gate-Source Cut-off Voltage	$V_{DS} = 15V, I_{D} = 0.5nA$	2.0	6.0	V
V <sub>DS</sub> (on)	Drain-Source On Voltage	$V_{GS} = 0V, I_{D} = 10mA$		0.5	V
r <sub>ds</sub> (on)	Drain-Source On Reverse	$V_{GS} = 0V, I_{D} = 0$		40	Ω
C <sub>rss</sub>	Reverse Transfer Capacitance	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 10V		5.0	pF
t <sub>d</sub>	Delay Time	$V_{DD} = 10V, V_{GS}(on) = 0V$		6.0	ns
t <sub>r</sub>	Rise Time	$I_D = 10 \text{mA}, V_{GS}(\text{off}) = 6.0 \text{V}$		4.0	ns
t <sub>off</sub>	Turn-off Time			50	ns

# **Package Dimensions**

## **SOT-23**







Dimensions in Millimeters

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E <sup>2</sup> CMOS™	HiSeC™	MSXPro™	Quiet Series™	TruTranslation™
EnSigna™	$I^2C^{TM}$	$OCX^{TM}$	RapidConfigure™	UHC™
Across the board.	. Around the world.™	OCXPro™	RapidConnect™	UltraFET <sup>®</sup>
The Power Franchise™		OPTOLOGIC <sup>®</sup>	SILENT SWITCHER®	VCX™
Programmable Active Droop™		OPTOPLANAR™	SMART START™	

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