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FCP7N60 / FCPF7N60 N-Channel SuperFET[®] MOSFET 600 V, 7 A, 600 mΩ

Features

- 650 V @ T_J = 150^oC
- Typ. R_{DS(on)} = 530 mΩ
- Ultra Low Gate Charge (Typ. Q_q = 23 nC)
- Low Effective Output Capacitance (Typ. C_{oss(eff.)} = 60 pF)
- 100% Avalanche Tested
- RoHS Compliant

Application

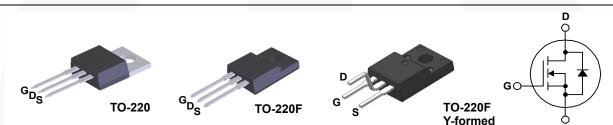
- LCD/LED/PDP TV
- Solar Inverter
- AC-DC Power Supply

November 2013

FCP7N60 / FCPF7N60 — N-Channel SuperFET[®] MOSFET

Description

SuperFET[®] MOSFET is Fairchild Semiconductor's first generation of high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low onresistance and lower gate charge performance. This technology is tailored to minimize conduction loss, provide superior switching performance, dv/dt rate and higher avalanche energy. Consequently, SuperFET MOSFET is very suitable for the switching power applications such as PFC, server/telecom power, FPD TV power, ATX power and industrial power applications.



Absolute Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol		Parameter		FCP7N60	FCPF7N60 / FCPF7N60YDTU	Unit
V _{DSS}	Drain-Source Voltage			600		
ID	Drain Current - Continuous ($T_C = 25^{\circ}C$) - Continuous ($T_C = 100^{\circ}C$)			7 7* 4.4 4.4*		A A
I _{DM}	Drain Current	- Pulsed	(Note 1)	21	21*	Α
V _{GSS}	Gate-Source voltage			± 30		V
E _{AS}	Single Pulsed Avalanche Energy		(Note 2)	230		mJ
I _{AR}	Avalanche Current		(Note 1)	7		А
E _{AR}	Repetitive Avalanche Energy (Note		(Note 1)	8.3		mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		(Note 3)	4.5		V/ns
P _D	Power Dissipation $(T_C = 25^{\circ}C)$ - Derate Above $25^{\circ}C$			83 0.67	31 0.25	W W/∘C
T _{J,} T _{STG}	Operating and Storage Temperature Range			-55 to +150		°C
TL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds			300		

*Drain current limited by maximum junction temperature.

Thermal Characteristics

Symbol	Parameter	FCP7N60	FCPF7N60 / FCPF7N60YDTU	Unit
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction-to-Case, Max.	1.5	4.0	°C/W
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	62.5	0/11

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Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FCP7N60	FCP7N60	TO220	Tube	N/A	N/A	50 units
FCPF7N60	FCPF7N60	TO220F	Tube	N/A	N/A	50 units
FCPF7N60YDTU	FCPF7N60	TO-220F (Y-formed)	Tube	N/A	N/A	50 units

Electrical Characteristics $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Off Charac	teristics			1		1
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA, T _J = 25°C				V
		V _{GS} = 0 V, I _D = 250 μA, T _J = 150°C		650		V
ΔBV_{DSS} / ΔT_{J}	Breakdown Voltage Temperature Coefficient	I_D = 250 µA, Referenced to 25°C		0.6		V/∘C
BV _{DS}	Drain-Source Avalanche Breakdown Voltage	V _{GS} = 0 V, I _D = 7 A		700		V
I _{DSS}	Zero Gate Voltage Drain Current $V_{DS} = 600 \text{ V}, V_{GS} = 0 \text{ V}$ $V_{DS} = 480 \text{ V}, T_{C} = 125^{\circ}\text{C}$				1 10	μΑ μΑ
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30 V, V _{DS} = 0 V			-100	nA
On Charac	teristics					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \ \mu A$	3.0		5.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 3.5 A		0.53	0.6	Ω
9 _{FS}	Forward Transconductance	V _{DS} = 40 V, I _D = 3.5 A		6		S
Dynamic C	Characteristics					
C _{iss}	Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V,		710	920	pF
C _{oss}	Output Capacitance	f = 1 MHz		380	500	pF
C _{rss}	Reverse Transfer Capacitance			34		pF
C _{oss}	Output Capacitance	V_{DS} = 480 V, V_{GS} = 0 V, f = 1 MHz		22	29	pF
Coss(eff.)	Effective Output Capacitance $V_{DS} = 0 V$ to 400 V, $V_{GS} = 0 V$		-	60		pF
Switching	Characteristics					
t _{d(on)}	Turn-On Delay Time	V _{DD} = 300 V, I _D = 7 A,		35	80	ns
t _r	Turn-On Rise Time	V_{GS} = 10 V, R_{G} = 25 Ω		55	120	ns
t _{d(off)}	Turn-Off Delay Time			75	160	ns
t _f	Turn-Off Fall Time	(Note 4)		32	75	ns
Qg	Total Gate Charge	V _{DS} = 480 V, I _D = 7 A,		23	30	nC
Q _{gs}	Gate-Source Charge	V _{GS} = 10 V		4.2	5.5	nC
Q _{gd}	Gate-Drain Charge	(Note 4)	/	11.5		nC
Drain-Sou	rce Diode Characteristics and Maximur	n Ratings				
I _S	Maximum Continuous Drain-Source Diode Forward Current				7	Α
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current				21	Α
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 7 A			1.4	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _S = 7 A,		360		ns
Q _{rr}	Reverse Recovery Charge	dI _F /dt =100 A/µs		4.5		μC

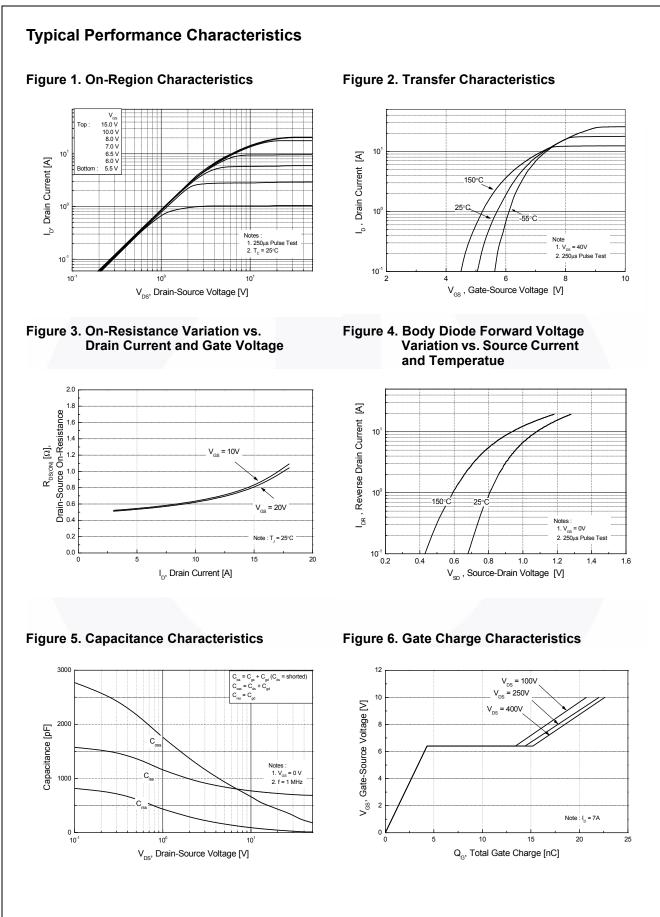
Notes:

1. Repetitive rating: pulse-width limited by maximum junction temperature.

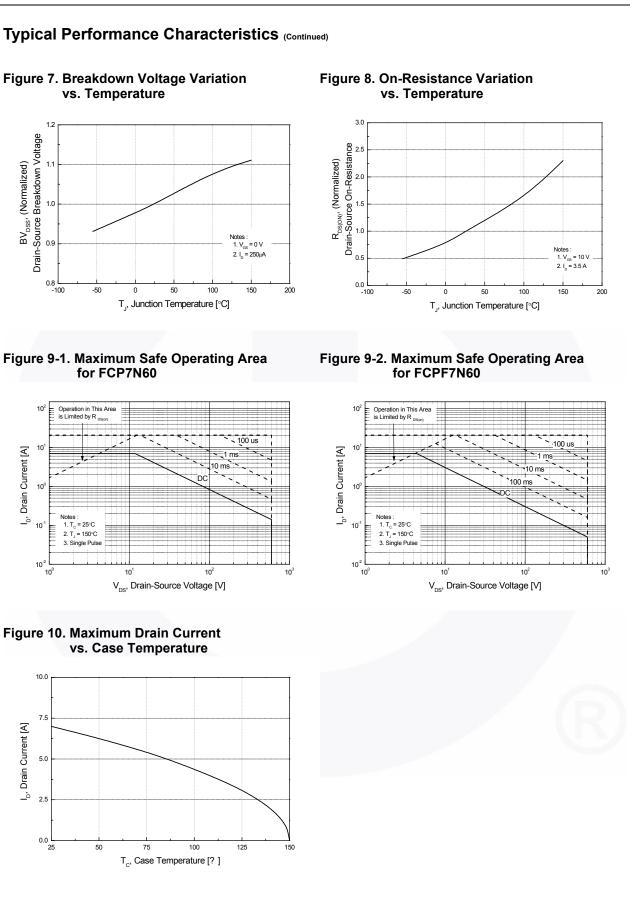
2. I_{AS} = 3.5 A, V_{DD} = 50 V, R_G = 25 $\Omega,$ starting T_J = 25°C.

3. I_{SD} \leq 7 A, di/dt \leq 200 A/µs, V_{DD} \leq BV_{DSS}, starting T_J = 25°C.

4. Essentially independent of operating temperature typical characteristics.

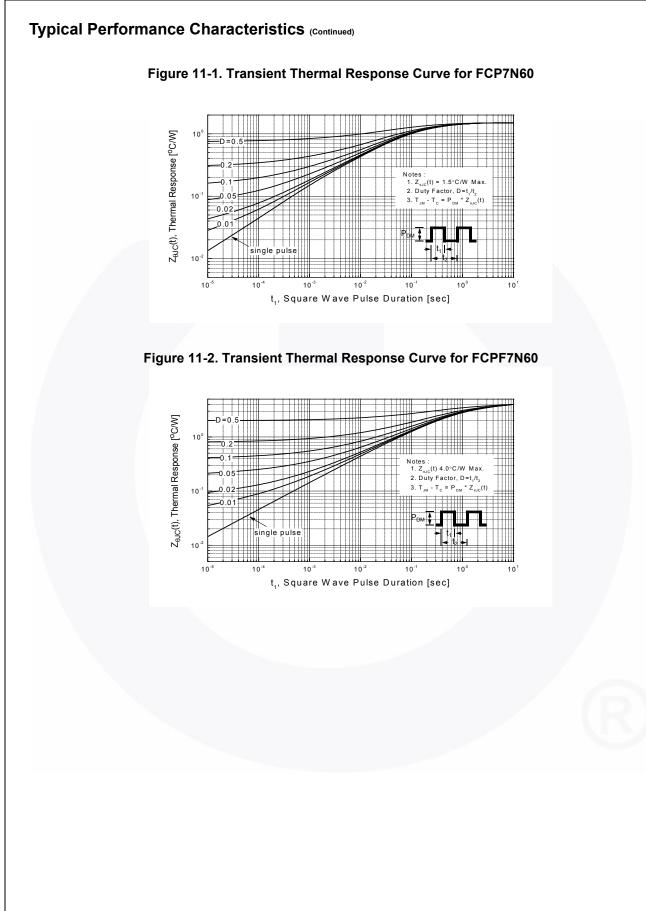


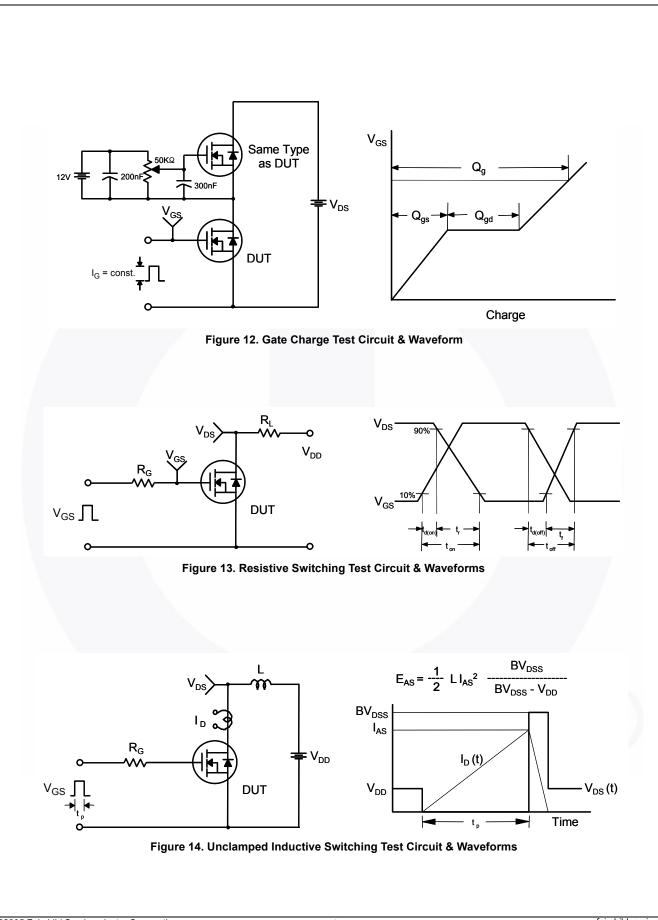
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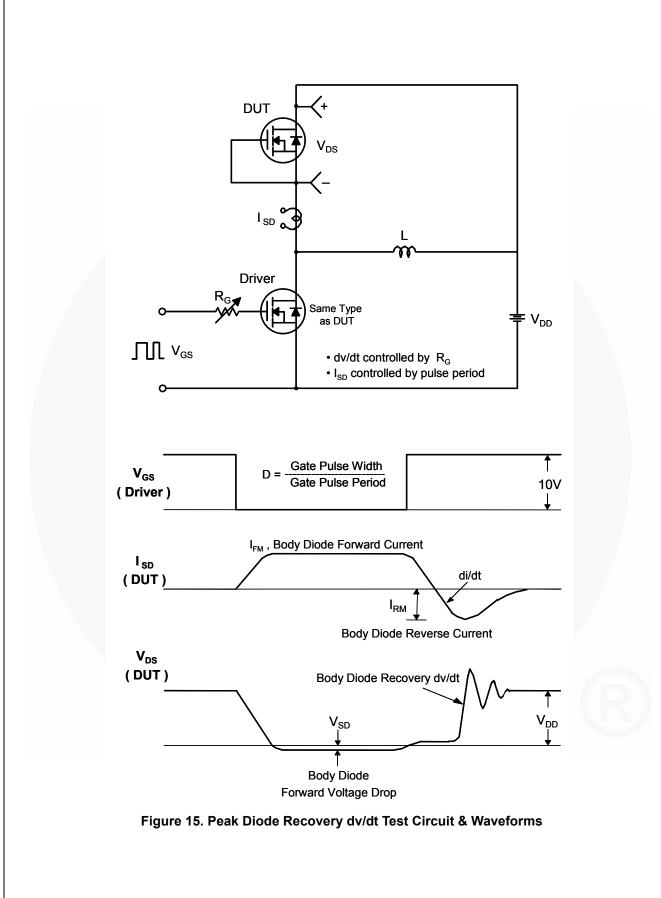
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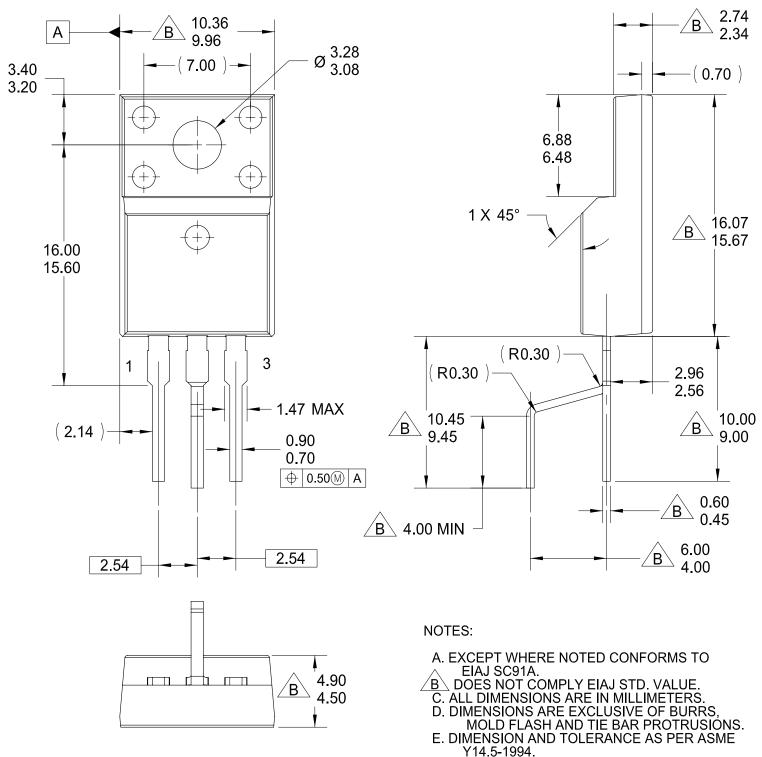
Drain Current [A]



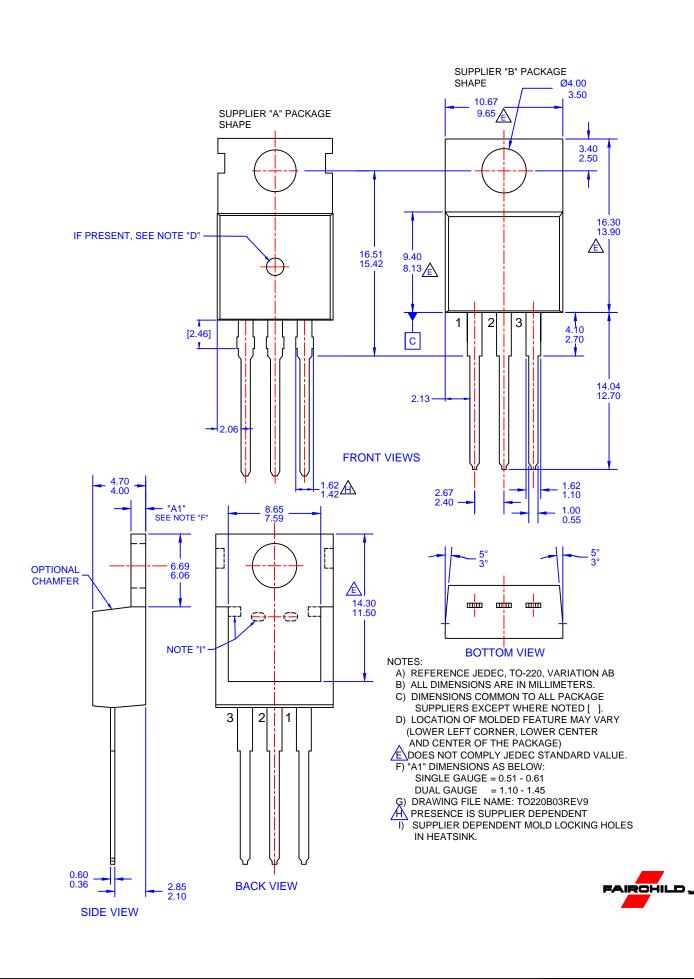


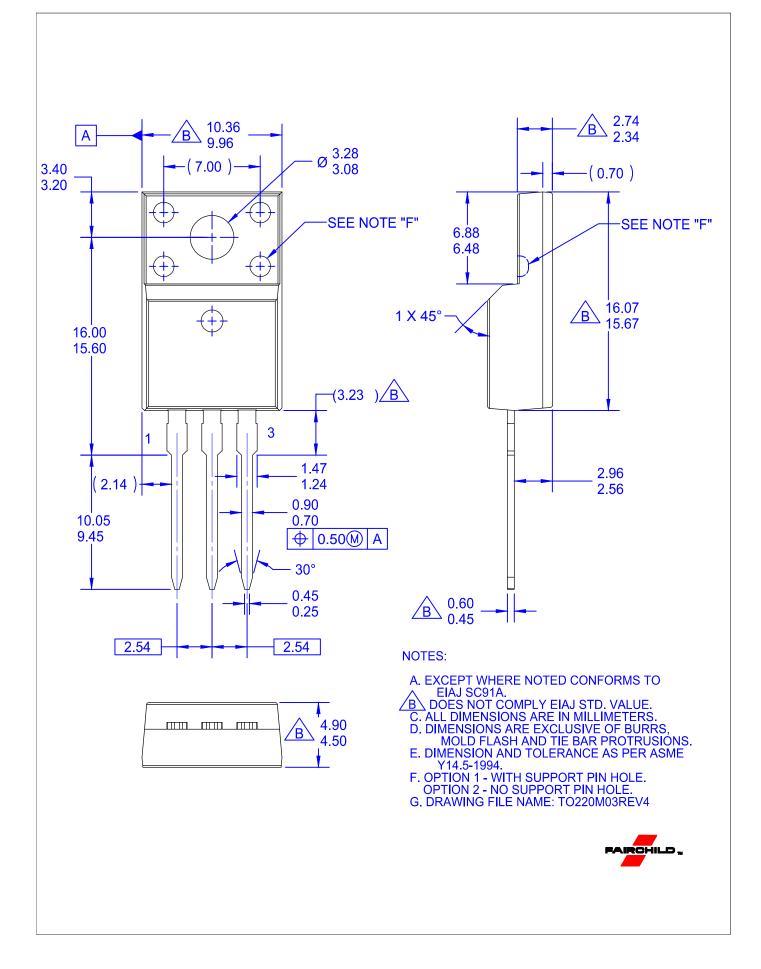
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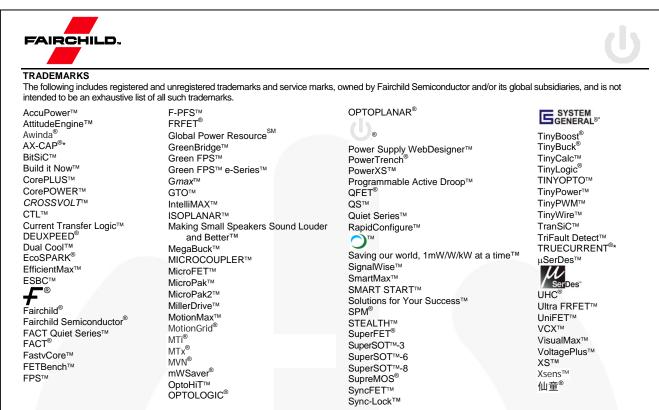




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