## FAIRCHILD

SEMICONDUCTOR

### FCPF380N60E\_F152 N-Channel SuperFET<sup>®</sup> II MOSFET 600 V, 10.2 A, 380 mΩ

#### Features

- 650 V @T<sub>J</sub> = 150°C
- Max. R<sub>DS(on)</sub> = 380 mΩ
- Ultra Low Gate Charge (Typ. Q<sub>g</sub> = 34 nC)
- Low Effective Output Capacitance (Typ. C<sub>oss</sub>.eff = 97 pF)
- 100% Avalanche Tested

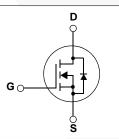
#### Aplications

- LCD / LED / PDP TV Lighting
- Solar Inverter
- AC-DC Power Supply



### Description

SuperFET<sup>®</sup>II MOSFET is Fairchild Semiconductor<sup>®</sup>, s first generation of high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This advanced technology is tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy. Consequently, SuperFET<sup>®</sup>II MOSFET is suitable for various AC/DC power conversion for system miniaturization and higher efficiency.



#### MOSFET Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted

Symbol	Parameter			FCPF380N60E_F152	Unit	
V <sub>DSS</sub>	Drain to Source Voltage			600	V	
V <sub>GSS</sub>		- DC	- DC		V	
	Gate to Source Voltage	- AC	(f > 1Hz)	±30	V	
I <sub>D</sub> C		-Continuous (T <sub>C</sub> = 25°C)	-Continuous (T <sub>C</sub> = 25°C)			
	Drain Current	-Continuous ( $T_c = 100^{\circ}C$ )		6.4*	— A	
I <sub>DM</sub>	Drain Current	- Pulsed	(Note 1)	30.6*	Α	
E <sub>AS</sub>	Single Pulsed Avalanche Energy (Note 2)		211.6	mJ		
I <sub>AR</sub>	Avalanche Current		(Note 1)	2.3	Α	
E <sub>AR</sub>	Repetitive Avalanche Energy		(Note 1)	1.06	mJ	
	Peak Diode Recovery dv/dt (Note 3)		(Note 3)	20	1//20	
dv/dt MOSFET dv/dt				100	V/ns	
P <sub>D</sub>	Device Dissisction	$(T_{C} = 25^{\circ}C)$		31	W	
	Power Dissipation	- Derate above 25°C		0.25	W/ºC	
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temp	erature Range		-55 to +150	°C	
TL	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds		300	°C		

\*Drain current limited by maximum junction temperature

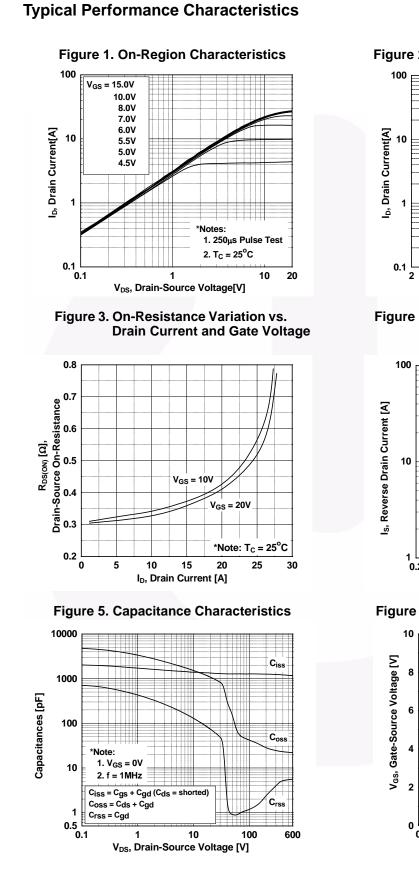
### **Thermal Characteristics**

Symbol	Parameter	FCPF380N60E_F152	Unit
$R_{ ext{ heta}JC}$	Thermal Resistance, Junction to Case	4	
$R_{\theta CS}$	Thermal Resistance, Case to Heat Sink (Typical)	0.5	°C/W
$R_{\thetaJA}$	Thermal Resistance, Junction to Ambient	62.5	

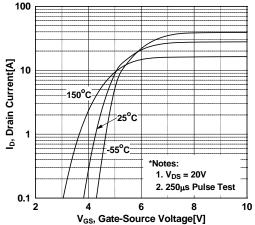
Device Marking FCPF380N60E		Device FCPF380N60E_F152	Package TO-220F	Eco Status Green ⊘	Packaging Type Tube			Quantity 50	
For Fairchild's d	efinition o	f "green" Eco Status, please visit:	http://www.fairc	hildsemi.com/company/g	green/ro	hs_green.	<u>html</u> .		
Electrica	I Cha	racteristics T <sub>C</sub> = 25°C	unless otherwis	se noted					
Symbol	 	Parameter		Test Conditions		Min.	Тур.	Max.	Unit
Off Charac	teristi	 CS			I			I	
			Vcs =	0V, I <sub>D</sub> = 10mA, T <sub>J</sub> = 25°	C.	600	-	_	V
BV <sub>DSS</sub>	Drain f	to Source Breakdown Voltage		$0V, I_D = 10mA, T_J = 150$		650	-	-	V
ΔBV <sub>DSS</sub> ΔT.I	Break	down Voltage Temperature cient		000, 10 = 1000, 10 = 100		-	0.67	-	V/°C
BV <sub>DS</sub>		Source Avalanche Breakdown	V <sub>GS</sub> =	0V, I <sub>D</sub> = 10A		-	700	-	V
	Zaro (	Sata Valtaga Drain Current	V <sub>DS</sub> =	480V, V <sub>GS</sub> = 0V		-	-	10	
DSS	Zeru e	Sate Voltage Drain Current	$V_{DS} =$	480V, T <sub>C</sub> = 125 <sup>o</sup> C		-	-	10	μA
I <sub>GSS</sub>	Gate t	o Body Leakage Current	V <sub>GS</sub> =	$\pm 20V, V_{DS} = 0V$		-	-	±100	nA
On Charac	teristi	cs							
V <sub>GS(th)</sub>	Gate 7	Threshold Voltage	V <sub>GS</sub> =	V <sub>DS</sub> , I <sub>D</sub> = 250μA		2.5	-	3.5	V
R <sub>DS(on)</sub>		Drain to Source On Resistanc		$10V, I_D = 5A$		-	0.32	0.38	Ω
9FS	Forwa	rd Transconductance		$20V, I_D = 5A$		-	10	-	S
Dynamic C	harac	teristics			I				1
C <sub>iss</sub>	-	Capacitance		$V_{DS} = 25V, V_{GS} = 0V$		-	1330	1770	pF
C <sub>oss</sub>		t Capacitance				-	945	1260	pF
C <sub>rss</sub>		se Transfer Capacitance	f = 1M	Hz		-	60	90	pF
C <sub>oss</sub>		t Capacitance	V <sub>DS</sub> =	380V, V <sub>GS</sub> = 0V, f = 1MH	Hz	-	25	-	pF
C <sub>oss</sub> eff.	Effecti	ve Output Capacitance		$0V \text{ to } 480V, V_{GS} = 0V$		-	97	-	pF
Q <sub>g(tot)</sub>		Gate Charge at 10V		V <sub>DS</sub> = 380V, I <sub>D</sub> = 5A		-	34	45	nC
Q <sub>gs</sub>	Gate to	o Source Gate Charge				-	5.3	-	nC
Q <sub>gd</sub>	Gate to	o Drain "Miller" Charge	V <sub>GS</sub> =		(Note 4)	-	13	-	nC
ESR	Equiva	alent Series Resistance	f = 1M		1010 4)	-	6	-	Ω
Switching									
	-	Dn Delay Time				-	17	44	ns
t <sub>d(on)</sub> t <sub>r</sub>		On Rise Time		$V_{DD} = 380V, I_D = 5A$ $V_{GS} = 10V, R_G = 4.7\Omega$		_	9	28	ns
		Off Delay Time				-	64	138	ns
t <sub>d(off)</sub> t <sub>f</sub>		Off Fall Time		(	(Note 4)	-	10	30	ns
		ode Characteristics			1010 17				
		um Continuous Drain to Source	o Diodo Forwa	rd Curront		-	·	10.2	A
l <sub>S</sub>		um Pulsed Drain to Source Di				-	-	30.6	A
I <sub>SM</sub> V <sub>SD</sub>		o Source Diode Forward Volta		0V, I <sub>SD</sub> = 5A		_	_	1.2	V
t <sub>rr</sub>		se Recovery Time		$0V, I_{SD} = 5A$		-	240	-	ns
Q <sub>rr</sub>		se Recovery Charge		= 100A/µs		-	3	-	μC

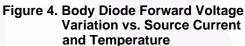
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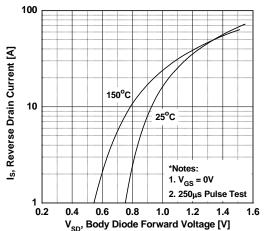
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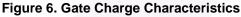


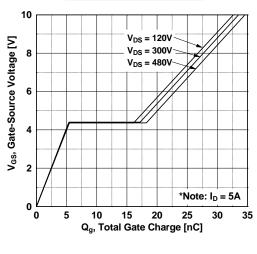
**Figure 2. Transfer Characteristics** 

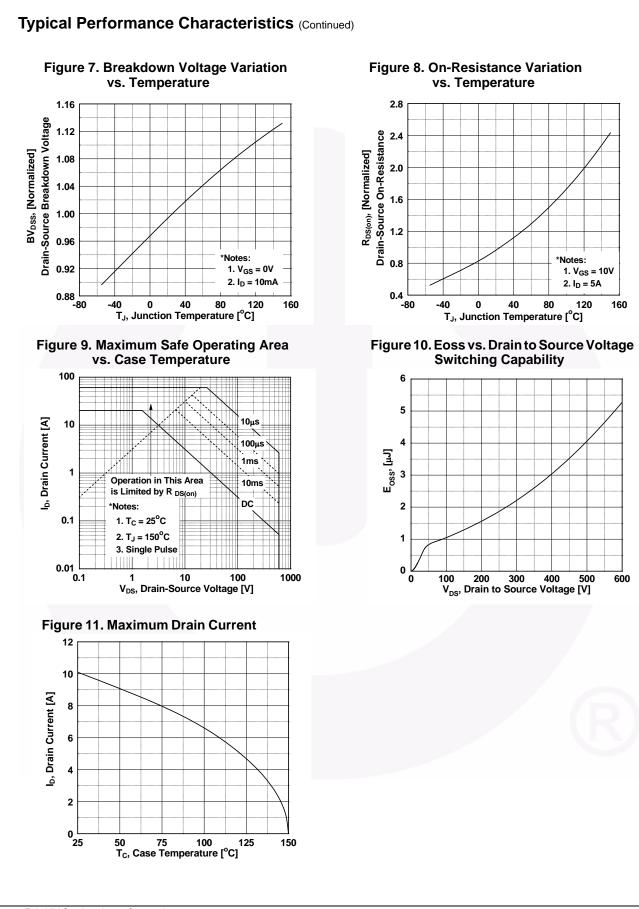




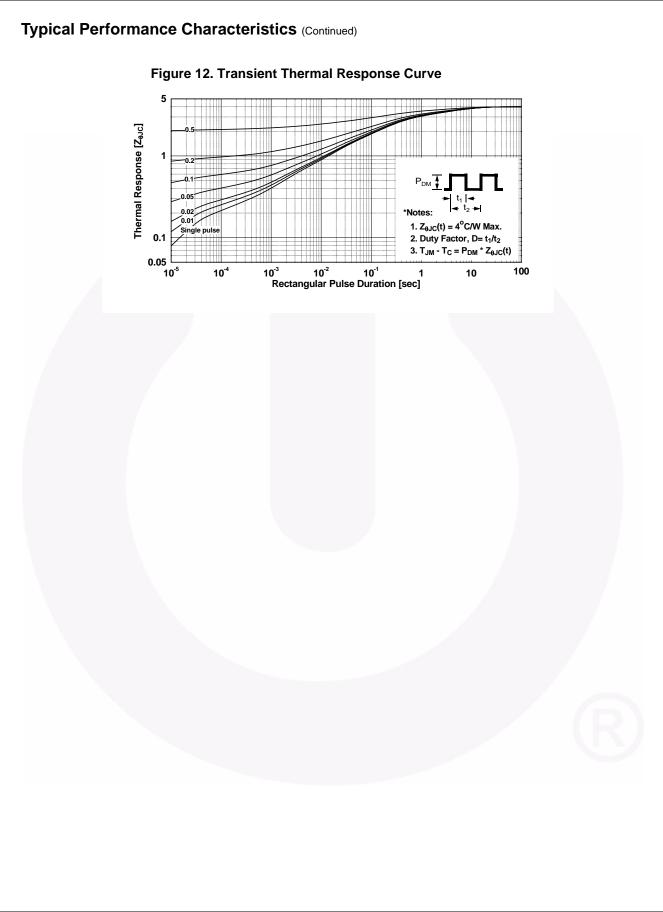




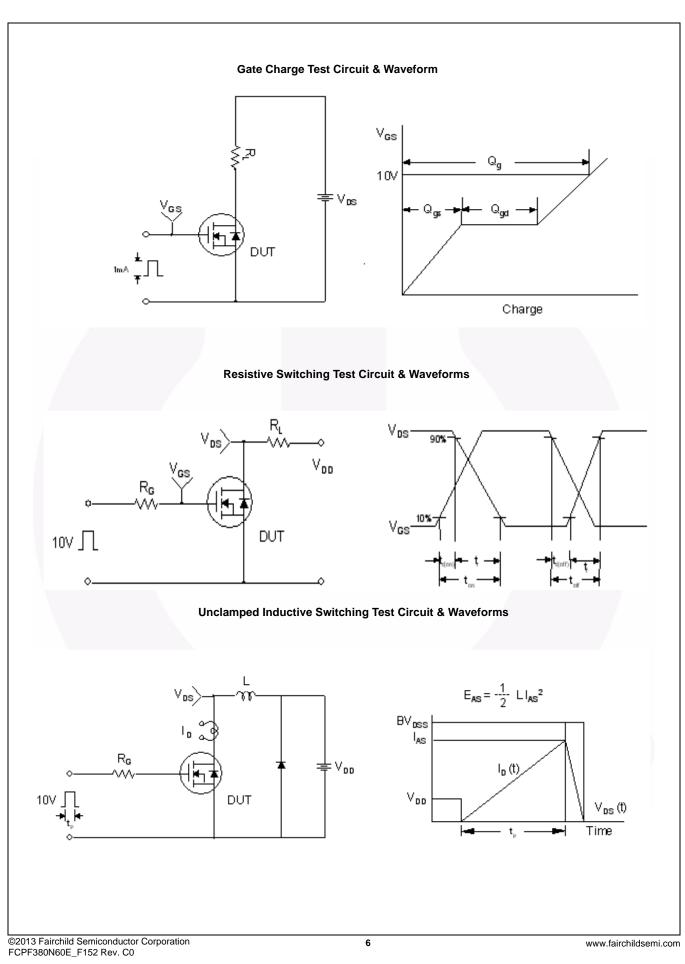




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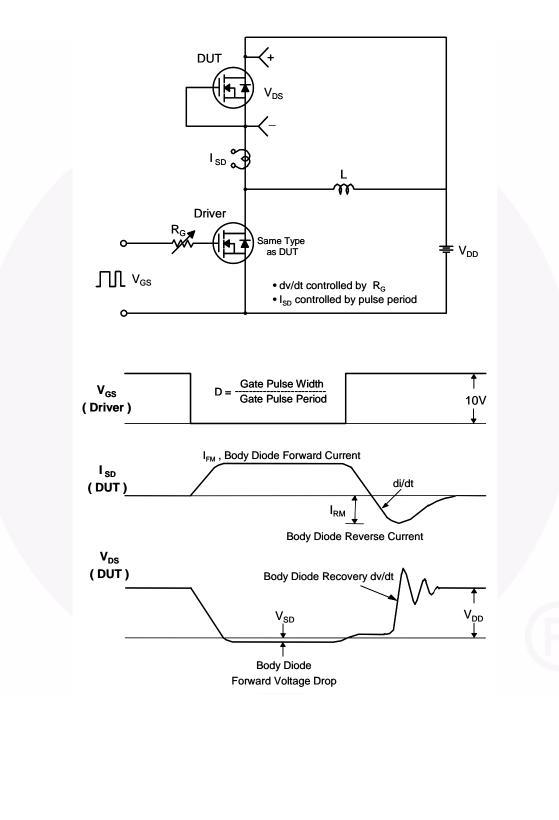


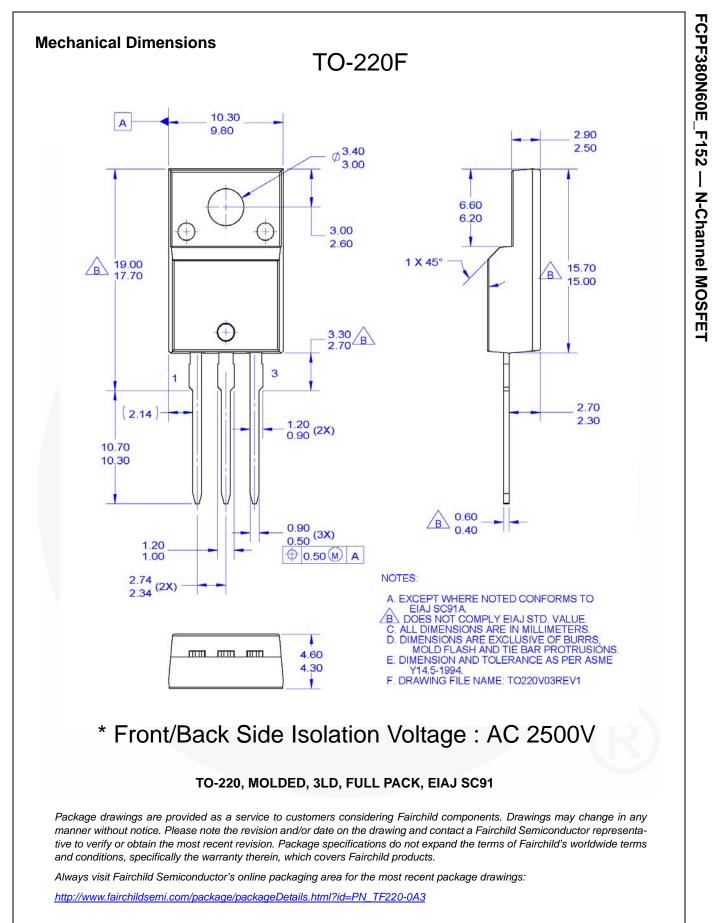
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