

October 2015

# FFB2222A / FMB2222A / MMPQ2222A NPN Multi-Chip General-Purpose Amplifier

## **Description**

This device is for use as a medium power amplifier and switch requiring collector currents up to 500 mA. Sourced from process 19.

## **Block Diagram**

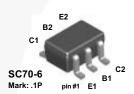


Figure 1. FFB2222A Device Package

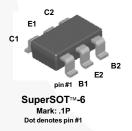


Figure 3. FMB2222A Device Package



Figure 5. MMPQ2222A Device Package

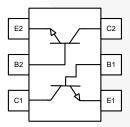


Figure 2. FFB2222A Internal Connection

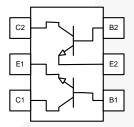


Figure 4. FMB2222A Internal Connection

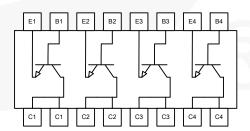


Figure 6. MMPQ2222A Internal Connection

## **Ordering Information**

Part Number	Top Mark	Package	Packing Method
FFB2222A	.1P	SC70 6L	Tape and Reel
FMB2222A	.1P	SSOT 6L	Tape and Reel
MMPQ2222A	MMPQ2222A	SOIC 16L	Tape and Reel

## **Absolute Maximum Ratings**(1)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}\text{C}$  unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>CEO</sub>	Collector-Emitter Voltage	45	V
V <sub>CBO</sub>	Collector-Base Voltage	75	V
V <sub>EBO</sub>	Emitter-Base Voltage	5.0	V
I <sub>C</sub>	Collector Current - Continuous	500	mA
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range	-55 to +150	°C

#### Note:

1. These ratings are based on a maximum junction temperature of 150°C. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty cycle operations.

#### Thermal Characteristics(2)

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Max.			Unit
Symbol		FFB2222A	FMB2222A	MMPQ2222A	O.III
P <sub>D</sub>	Total Device Dissipation	300	700	1,000	mW
	Derate Above 25°C	2.4	5.6	8.0	mW/°C
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient	415	180		
	Thermal Resistance, Junction-to-Ambient, Effective 4 Dies			125	°C/W
	Thermal Resistance, Junction-to-Ambient, Each Die			240	

#### Note:

2. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

## **Electrical Characteristics**

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage <sup>(3)</sup>	I <sub>C</sub> = 10 mA, I <sub>B</sub> = 0	40			V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	$I_C = 10 \mu A, I_E = 0$	75			V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	$I_E = 10 \mu A, I_C = 0$	5.0			V
I <sub>CBO</sub>	Collector Cut-Off Current	V <sub>CB</sub> = 60 V, I <sub>E</sub> = 0			10	nA
I <sub>EBO</sub>	Emitter Cut-Off Current	$V_{EB} = 3.0 \text{ V}, I_{C} = 0$			10	nA
		I <sub>C</sub> = 0.1 mA, V <sub>CE</sub> = 10 V	35			
		I <sub>C</sub> = 1.0 mA, V <sub>CE</sub> = 10 V	50			
h	DC Current Coin	I <sub>C</sub> = 10 mA, V <sub>CE</sub> = 10 V	75			
h <sub>FE</sub>	DC Current Gain	I <sub>C</sub> = 150 mA, V <sub>CE</sub> = 10 V <sup>(3)</sup>	100		300	
		$I_C$ = 150 mA, $V_{CE}$ = 1.0 $V^{(3)}$	50			
		I <sub>C</sub> = 500 mA, V <sub>CE</sub> = 10 V <sup>(3)</sup>	40			
\/ (a at)	Collector Emitter Seturation Voltage(3)	I <sub>C</sub> = 150 mA, I <sub>B</sub> = 15 mA			0.3	V
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage <sup>(3)</sup>	I <sub>C</sub> = 500 mA, I <sub>B</sub> = 50 mA	V.		1.0	
\/ (oot)	Base-Emitter Saturation Voltage <sup>(3)</sup>	I <sub>C</sub> = 150 mA, I <sub>B</sub> = 15 mA			1.2	V
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage	I <sub>C</sub> = 500 mA, I <sub>B</sub> = 50 mA			2.0	V
$f_T$	Current Gain - Bandwidth Product	I <sub>C</sub> = 20 mA, V <sub>CE</sub> = 20 V, f = 100 MHz		300		MHz
C <sub>obo</sub>	Output Capacitance	V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0, f = 100 kHz		4.0		pF
C <sub>ibo</sub>	Input Capacitance	V <sub>EB</sub> = 0.5 V, I <sub>C</sub> = 0, f = 100 kHz		20		pF
NF	Noise Figure	$I_C$ = 100 μA, $V_{CE}$ = 10 V, $R_S$ = 1.0 kΩ, f = 1.0 kHz		2.0		dB
t <sub>d</sub>	Delay Time	$V_{CC} = 30 \text{ V}, V_{BE(OFF)} = 0.5 \text{ V},$		8		ns
t <sub>r</sub>	Rise Time	I <sub>C</sub> = 150 mA, I <sub>B1</sub> = 15 mA	1	20		ns
t <sub>s</sub>	Storage Time	$V_{CC} = 30 \text{ V, } I_{C} = 150 \text{ mA,}$		180		ns
t <sub>f</sub>	Fall Time	I <sub>B1</sub> = I <sub>B2</sub> = 15 mA		40		ns

#### Note:

3. Pulse test: pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2.0%.

## **Typical Performance Characteristics**

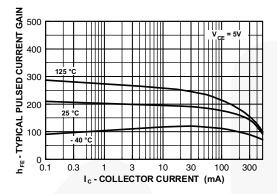


Figure 7. Typical Pulsed Current Gain vs. Collector Current

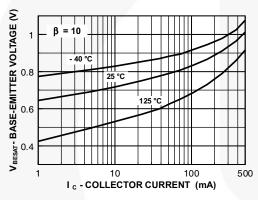


Figure 9. Base-Emitter Saturation Voltage vs. Collector Current

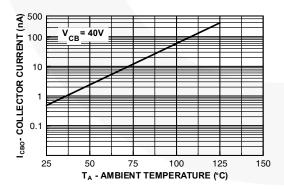


Figure 11. Collector Cut-Off Current vs.
Ambient Temperature

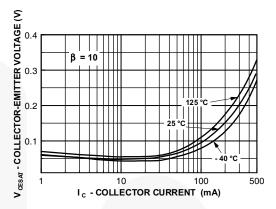


Figure 8. Collector-Emitter Saturation Voltage vs. Collector Current

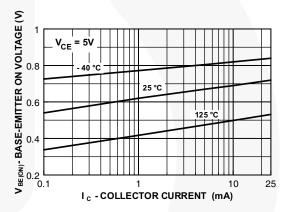


Figure 10. Base-Emitter On Voltage vs. Collector Current

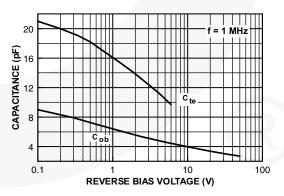


Figure 12. Emitter Transition and Output Capacitance vs. Reverse Bias Voltage

## **Typical Performance Characteristics** (Continued)

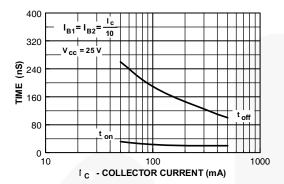


Figure 13. Turn-On and Turn-Off Times vs. Collector Current

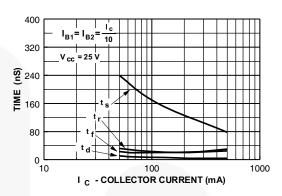


Figure 14. Switching Time vs. Collector Current

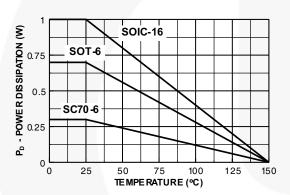


Figure 15. Power Dissipation vs. Ambient Temperature

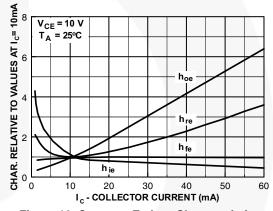


Figure 16. Common Emitter Characteristics

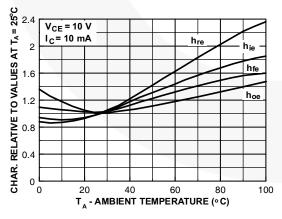


Figure 17. Common Emitter Characteristics

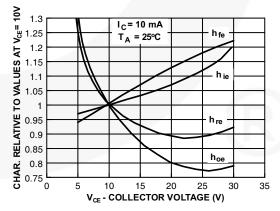


Figure 18. Common Emitter Characteristics

## **Test Circuits**

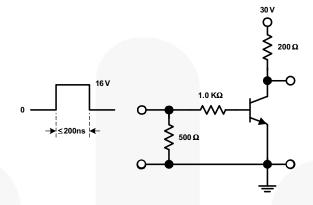


Figure 19. Saturated Turn-On Switching Time

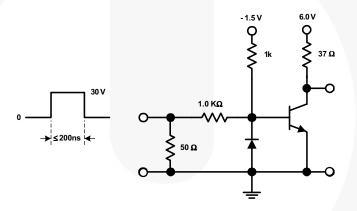
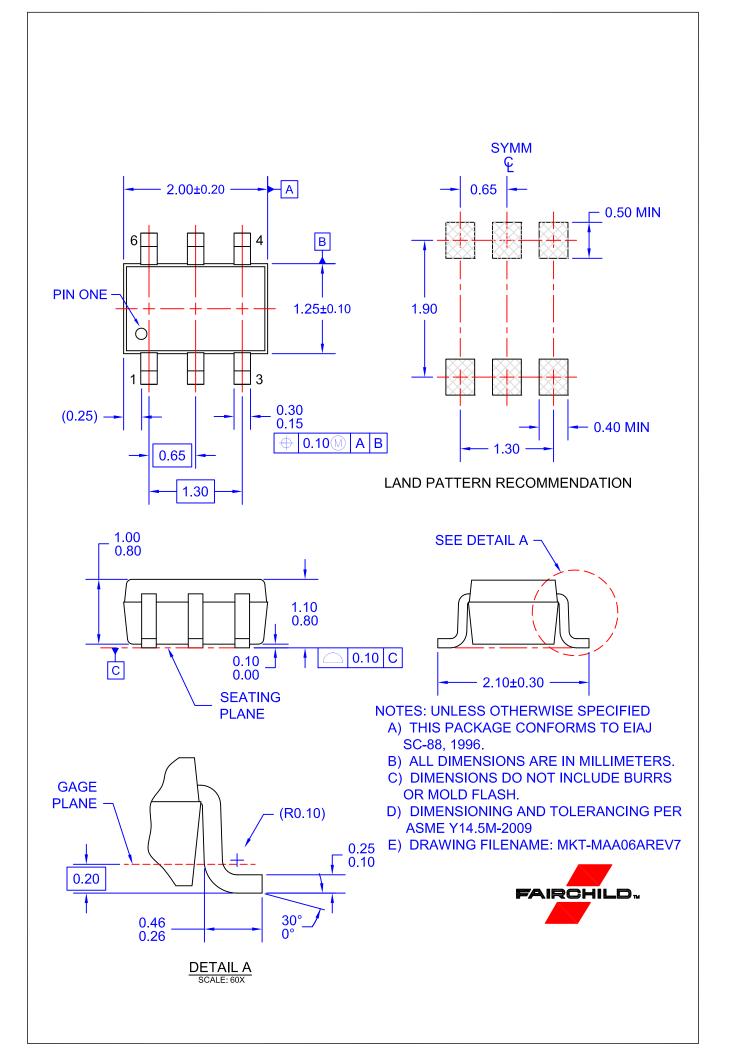
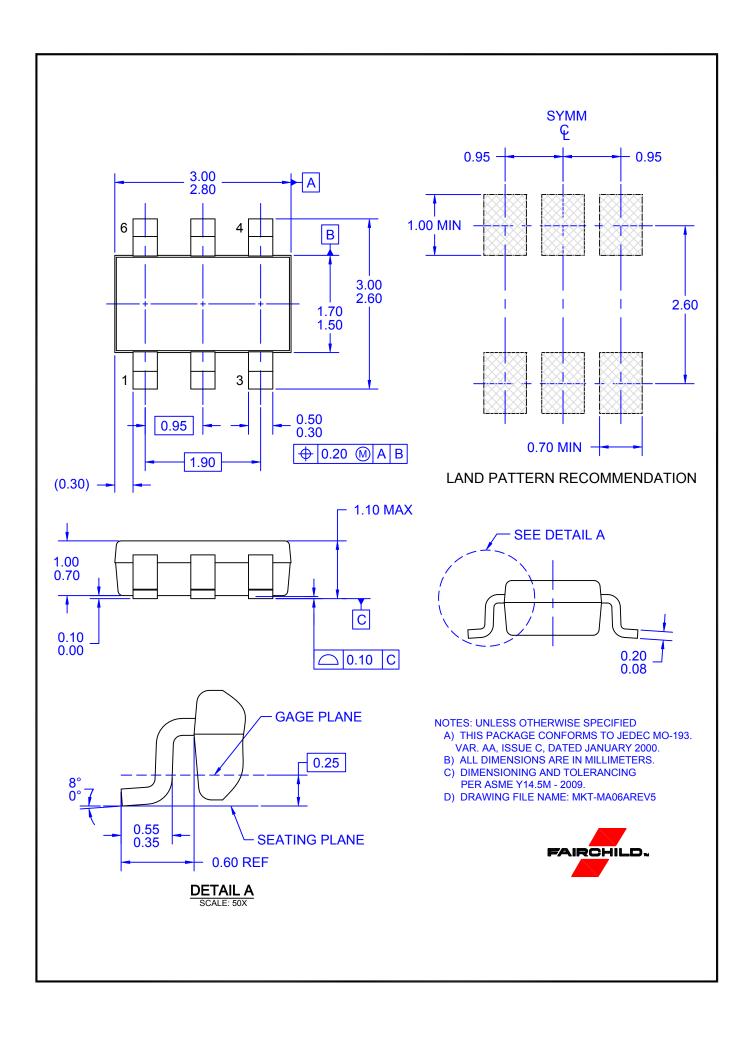
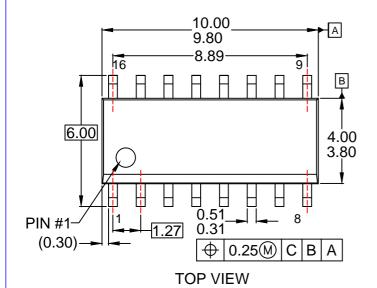
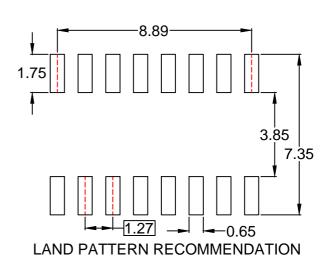


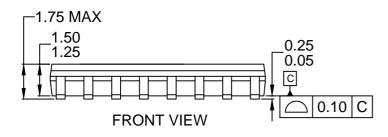
Figure 20. Saturated Turn-Off Switching Time

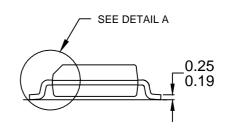


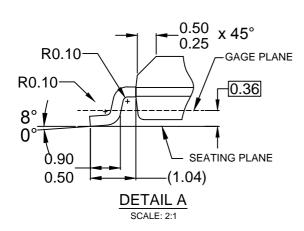












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