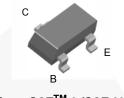


September 2015

# FSB619 NPN Low-Saturation Transistor

#### **Features**

 This device is designed with high-current gain and low-saturation voltage with collector currents up to 3 A continuous.



SuperSOT<sup>TM</sup>-3 (SOT-23)

# **Ordering Information**

Part Number	Marking	Package	Packing Method
FSB619	619	SSOT 3L	Tape and Reel

# **Absolute Maximum Ratings**(1),(2)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>CEO</sub>	Collector-Emitter Voltage	50	V
V <sub>CBO</sub>	Collector-Base Voltage	50	V
V <sub>EBO</sub>	Emitter-Base Voltage	5	V
I <sub>C</sub>	Collector Current - Continuous	2	Α
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range -55 to +150		°C

#### Notes:

- 1. These ratings are based on a maximum junction temperature of 150°C.
- 2. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty-cycle operations.

## **Thermal Characteristics**

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Max.	Unit
В	Total Device Dissipation <sup>(3)</sup>	500	mW
P <sub>D</sub>	Derate Above 25°C	4	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	250	°C/W

#### Note:

3. Device mounted on FR-4 PCB 4.5" X 5"; mounting pad 0.02 in<sup>2</sup> of 2oz copper.

## **Electrical Characteristics**

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = 10 mA, I <sub>B</sub> = 0	50		V
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	$I_C = 100 \mu A, I_E = 0$	50		V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	$I_E = 100 \mu A, I_C = 0$	5		V
I <sub>CBO</sub>	Collector Cut-Off Current	V <sub>CB</sub> = 40 V, I <sub>E</sub> = 0		100	nA
I <sub>EBO</sub>	Emitter Cut-Off Current	$V_{EB} = 4 \text{ V}, I_{C} = 0$		100	nA
I <sub>CES</sub>	Collector Emitter Cut-Off Current	V <sub>CES</sub> = 40 V		100	nA
	DC Current Gain <sup>(4)</sup>	I <sub>C</sub> = 10 mA, V <sub>CE</sub> = 2 V	200		
h <sub>FE</sub>		I <sub>C</sub> = 200 mA, V <sub>CE</sub> = 2 V	300		
		I <sub>C</sub> = 1 A, V <sub>CE</sub> = 2 V	200		
		I <sub>C</sub> = 2 A, V <sub>CE</sub> = 2 V	100		
	Collector-Emitter Saturation Voltage <sup>(4)</sup>	I <sub>C</sub> = 100 mA, I <sub>B</sub> = 10 mA		20	
V <sub>CE</sub> (sat)		I <sub>C</sub> = 1 A, I <sub>B</sub> = 10 mA		235	mV
	rollago	I <sub>C</sub> = 2 A, I <sub>B</sub> = 50 mA		320	
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage <sup>(4)</sup>	I <sub>C</sub> = 2 A, I <sub>B</sub> = 50 mA		1	V
V <sub>BE</sub> (on)	Base-Emitter On Voltage <sup>(4)</sup>	I <sub>C</sub> = 2 A, V <sub>CE</sub> = 2 V		1	V
C <sub>obo</sub>	Output Capacitance	V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0, f = 1 MHz	/	30	pF
f <sub>T</sub>	Transition Frequency	I <sub>C</sub> = 50 mA, V <sub>CE</sub> = 10 V, f = 100 MHz	100		

#### Note:

4. Pulse test: pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2.0%

# **Typical Performance Characteristics**

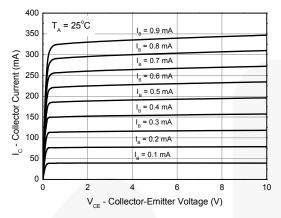


Figure 1. Static Characteristics

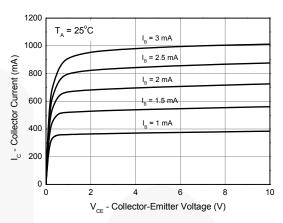


Figure 2. Static Characteristics

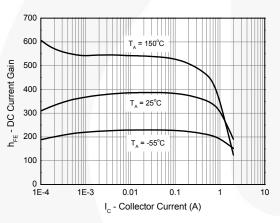


Figure 3. DC Current Gain vs. Collector Current

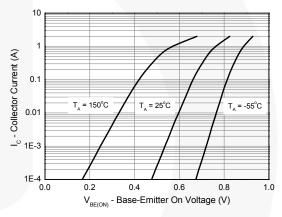


Figure 4. Base-Emitter On Voltage vs. Collector Voltage

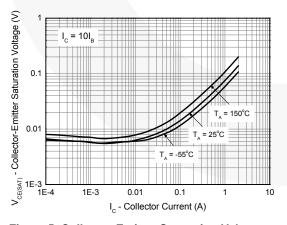


Figure 5. Collector-Emitter Saturation Voltage vs. Collector Voltage

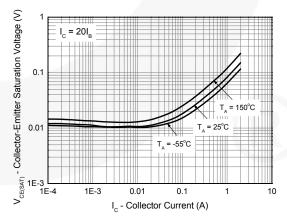


Figure 6. Collector-Emitter Saturation Voltage vs. Collector Voltage

## **Typical Performance Characteristics** (Continued)

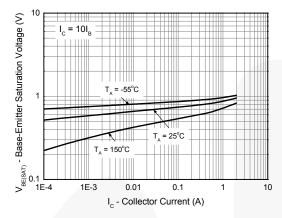


Figure 7. Base-Emitter Saturation Voltage vs. Collector Current

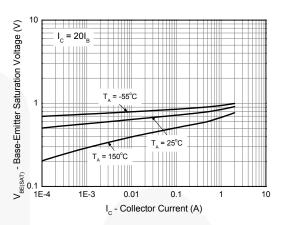


Figure 8. Base-Emitter Saturation Voltage vs. Collector Current

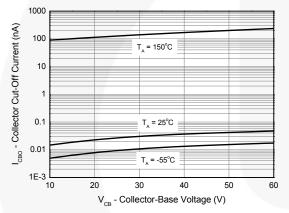


Figure 9. Collector Cut-Off Current vs. Collector-Base Voltage

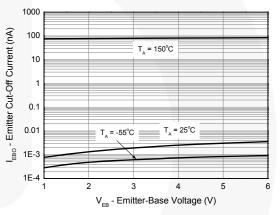


Figure 10. Emitter Cut-Off Current vs. Emitter-Base Voltage

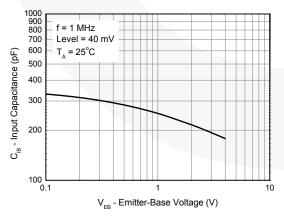


Figure 11. Typical Input Capacitance

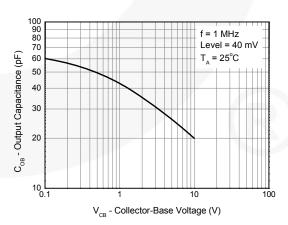
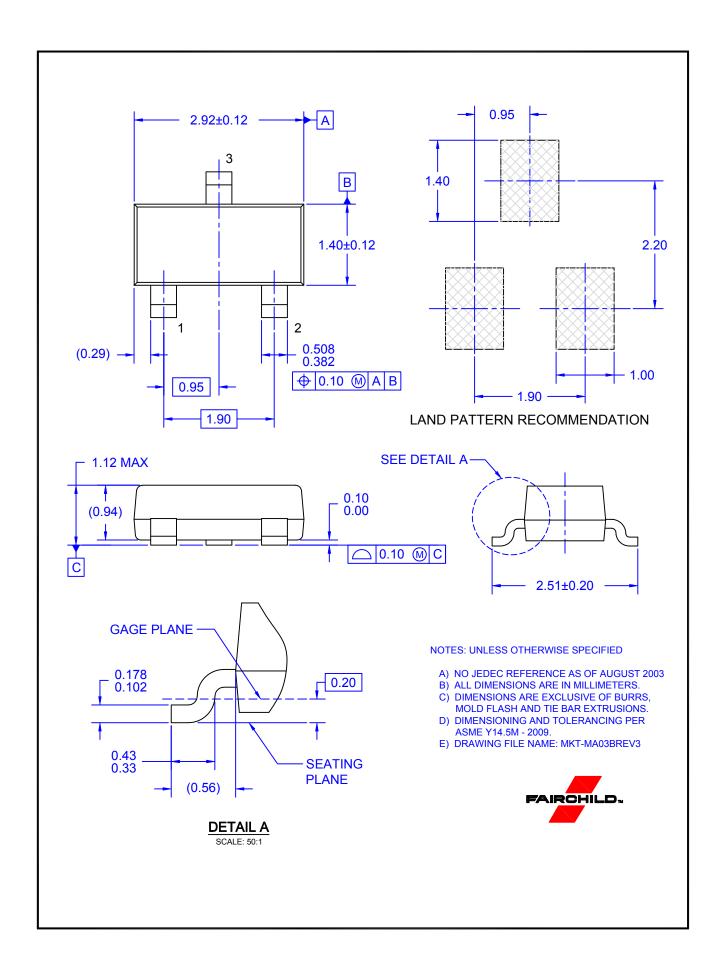


Figure 12. Typical Output Capacitance







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Definition of Terms			
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.	
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