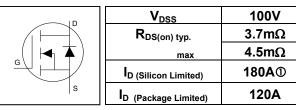
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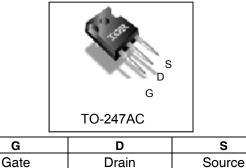
Features

- Advanced Process Technology
- Ultra Low On-Resistance
- Enhanced dV/dT and dI/dT capability
- 175°C Operating Temperature
- Fast Switching
- Repetitive Avalanche Allowed up to Tjmax
- Lead-Free, RoHS Compliant •
- Automotive Qualified *

Description

Specifically designed for Automotive applications, this HEXFET® Power MOSFETs utilizes the latest processing techniques to achieve low on-resistance per silicon area. Additional features of this design are a 175°C junction operating temperature, fast switching speed and improved repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in Automotive applications and a wide variety of other applications.





Ordering Information

Bass nort number	Dookogo Tupo	Standard Pack		Complete Part Number	
Base part number	Раскаде Туре	Form	Quantity		
AUIRFP4110	TO-247AC	Tube	25	AUIRFP4110	

Absolute Maximum Ratings

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only; and functional operation of the device at these or any other condition beyond those indicated in the specifications is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions. Ambient temperature (T_A) is 25°C, unless otherwise specified.

	Parameter	Max.	Units
I _D @ T _C = 25°C	Continuous Drain Current, V _{GS} @ 10V (Silicon Limited)	180 ①	
I _D @ T _C = 100°C	Continuous Drain Current, V _{GS} @ 10V (Silicon Limited)	130 ①	^
I _D @ T _C = 25°C	Continuous Drain Current, V _{GS} @ 10V (Package Limited)	120	A
I _{DM}	Pulsed Drain Current ②	670	
P _D @T _C = 25°C	Maximum Power Dissipation	370	W
	Linear Derating Factor	2.5	W/°C
V _{GS}	Gate-to-Source Voltage	± 20	V
EAS (Thermally limited)	Single Pulse Avalanche Energy 3	190	mJ
I _{AR}	Avalanche Current ②	108	Α
E _{AR}	Repetitive Avalanche Energy S	37	mJ
dv/dt	Peak Diode Recovery ④	5.3	V/ns
T _J T _{STG}	Operating Junction and Storage Temperature Range	-55 to + 175	°C
	Soldering Temperature, for 10 seconds (1.6mm from case)	300	7
	Mounting Torque, 6-32 or M3 Screw	10 lbf·in (1.1 N·m)	

Thermal Resistance

	Parameter	Тур.	Max.	Units
$R_{ ext{ heta}JC}$	Junction-to-Case ®		0.402	
$R_{ ext{ heta}CS}$	Case-to-Sink, Flat Greased Surface	0.24		°C/W
$R_{ ext{ heta}JA}$	Junction-to-Ambient		40	

HEXFET® is a registered trademark of International Rectifier. *Qualification standards can be found at http://www.irf.com/

HEXFET[®] Power MOSFET

International **IOR** Rectifier



Static @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Тур.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source Breakdown Voltage	100			V	V _{GS} = 0V, I _D = 250µA
$\Delta V_{(BR)DSS} / \Delta T_J$	Breakdown Voltage Temp. Coefficient		0.108		V/°C	Reference to 25°C, $I_D = 5mA$
R _{DS(on)}	Static Drain-to-Source On-Resistance		3.7	4.5	mΩ	V _{GS} = 10V, I _D = 75A
V _{GS(th)}	Gate Threshold Voltage	2.0		4.0	V	V _{DS} = V _{GS} , I _D = 250µA
gfs	Forward Trans conductance	160			S	V _{DS} = 50V, I _D = 75A
-				20		V _{DS} =100 V, V _{GS} = 0V
I _{DSS}	Drain-to-Source Leakage Current			250	μA	V _{DS} =100V,V _{GS} = 0V,T _J =125°C
1	Gate-to-Source Forward Leakage Gate-to-Source Reverse Leakage			100	nA	V _{GS} = 20V
I _{GSS}				-100	ΠA	V _{GS} = -20V
R _G	Gate Resistance		1.3		Ω	

Dynamic Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

-	C • • •	-			
Qg	Total Gate Charge	 150	210		I _D = 75A
Q_gs	Gate-to-Source Charge	 35		nC	V _{DS} = 50V
Q_{gd}	Gate-to-Drain Charge	 43			V _{GS} = 10V⑤
t _{d(on)}	Turn-On Delay Time	 25			V _{DD} = 65V
t _r	Rise Time	 67			I _D = 75A
t _{d(off)}	Turn-Off Delay Time	 78		ns	R _G = 2.6Ω
t _f	Fall Time	 88			V _{GS} = 10V⑤
C _{iss}	Input Capacitance	 9620			V _{GS} = 0V
C _{oss}	Output Capacitance	 670			V _{DS} = 50V
C _{rss}	Reverse Transfer Capacitance	 250		pF	f = 1.0MHz
$C_{\text{oss eff.}(\text{ER})}$	Effective Output Capacitance (Energy Related)	 820		•	V_{GS} = 0V, V_{DS} = 0V to 80V \odot
Coss eff.(TR)	Output Capacitance (Time Related)	 950			V_{GS} = 0V, V_{DS} = 0V to 80V [©]

Diode Characteristics

	Parameter	Min.	Тур.	Max.	Units	Conditions
I _S	Continuous Source Current (Body Diode)			180①		MOSFET symbol showing the
I _{SM}	Pulsed Source Current (Body Diode) ②			670		integral reverse
V_{SD}	Diode Forward Voltage			1.3	V	$T_{J} = 25^{\circ}C, I_{S} = 75A, V_{GS} = 0V$ (S)
+			50	75		$T_{\rm J} = 25^{\circ}C \qquad V_{\rm DD} = 85V$
trr	Reverse Recovery Time		60	90	ns	<u>T_J = 125°C</u> I _F = 75A,
0	Boyoroo Boooyory Chargo		94	140	20	<u>T, = 25°C</u> di/dt = 100A/µs ⑤
Q _{rr}	Reverse Recovery Charge		140	210	nC	<u>T_J = 125°C</u>
I _{RRM}	Reverse Recovery Current		3.5		Α	T _J = 25°C

Notes:

- ① Calculated continuous current based on maximum allowable junction temperature. Bond wire current limit is 120A. Note that current limitations arising from heating of the device leads may occur with some lead mounting arrangements.
- $\ensuremath{\mathbb{Q}}$ $\ensuremath{\mathbb{R}}$ Repetitive rating; pulse width limited by max. junction temperature.
- Limited by T_{Jmax}, starting T_J = 25°C, L = 0.033mH, R_G = 25 Ω , I_{AS} = 108A, V_{GS} =10V. Part not recommended for use above this value.
- $\label{eq:ISD} \textcircled{0.5mu}{0.5mu} I_{SD} \leq 75 A, \ di/dt \leq 630 A/\mu s, \ V_{DD} \leq V_{(BR)DSS}, \ T_J \leq 175^\circ C.$
- (5) Pulse width \leq 400µs; duty cycle \leq 2%.
- 6 C_{oss} eff. (TR) is a fixed capacitance that gives the same charging time as C_{oss} while V_{DS} is rising from 0 to 80% V_{DSS}.
- \odot C_{oss} eff. (ER) is a fixed capacitance that gives the same energy as C_{oss} while V_{DS} is rising from 0 to 80% V_{DSS}.
- [®] R_{θ} is measured at TJ approximately 90°C.



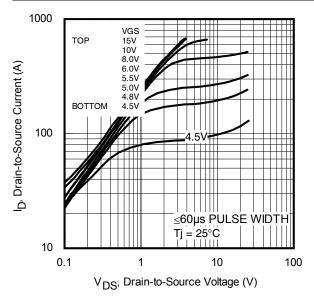
Qualification Information[†]

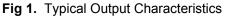
			Automotive (per AEC-Q101)				
Qualification	Level	Comments: This part number(s) passed Automotive qualification. IR's Industrial and Consumer qualification level is granted by extension of the higher Automotive level.					
Moisture Sensitivity Level		TO-247AC	N/A				
	Machine Model	Class M4 (+/- 800) ^{††}					
			AEC-Q101-002				
500	Human Body Model	Class H3A (+/- 6000V) ^{††}					
ESD		AEC-Q101-001					
	Charged Device Model		Class C5 (+/- 2000) ^{††}				
			AEC-Q101-005				
RoHS Compli	iant	Yes					

† Qualification standards can be found at International Rectifier's web site: <u>http://www.irf.com/</u>

†† Highest passing voltage.







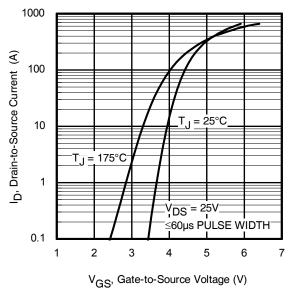
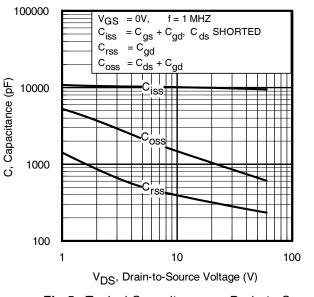
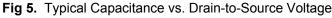


Fig 3. Typical Transfer Characteristics





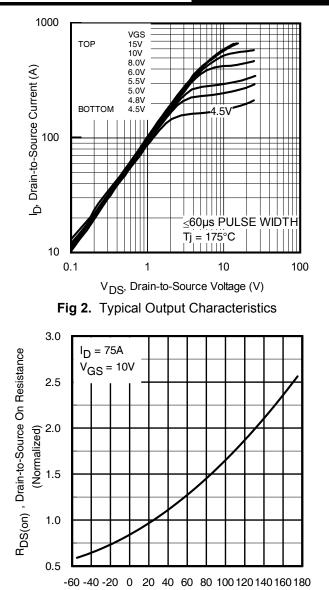


Fig 4. Normalized On-Resistance vs. Temperature

T_J, Junction Temperature (°C)

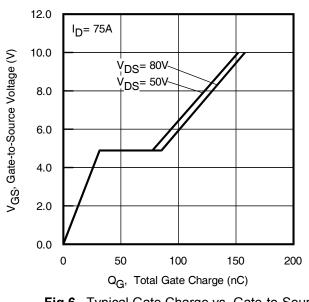
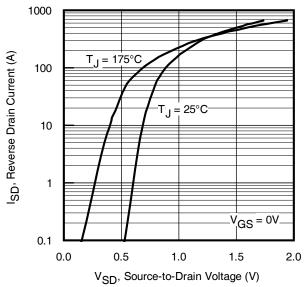
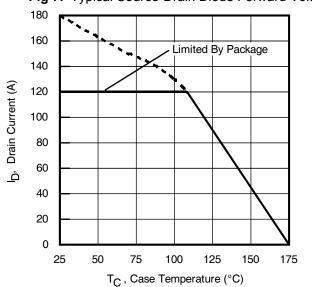


Fig 6. Typical Gate Charge vs. Gate-to-Source Voltage

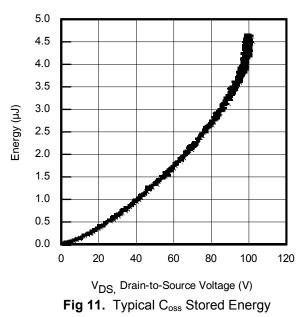












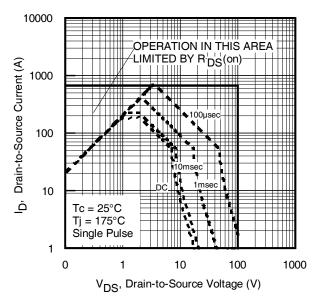


Fig 8. Maximum Safe Operating Area

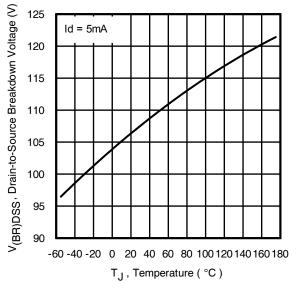
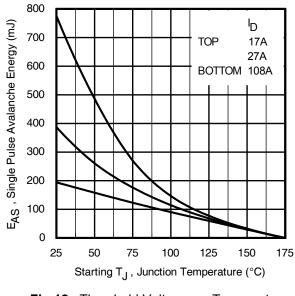


Fig 10. Drain-to-Source Breakdown Voltage



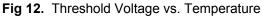


Fig 7. Typical Source-Drain Diode Forward Voltage

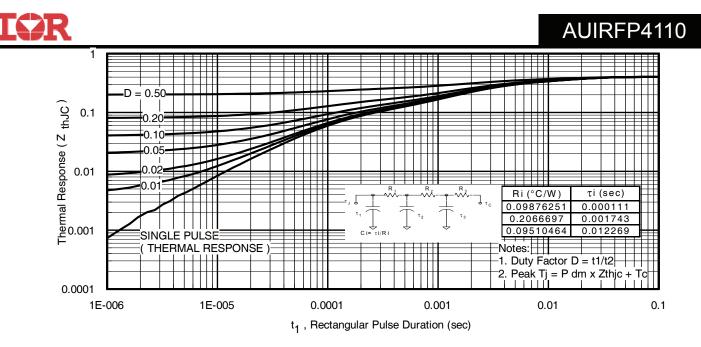
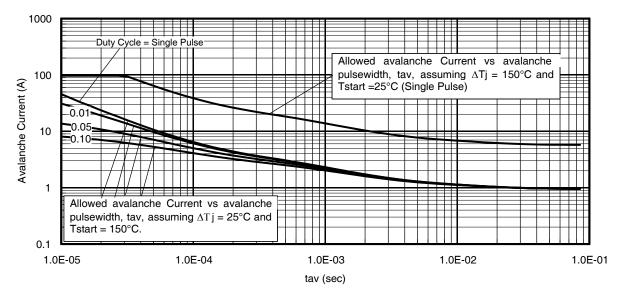


Fig 13. Maximum Effective Transient Thermal Impedance, Junction-to-Case





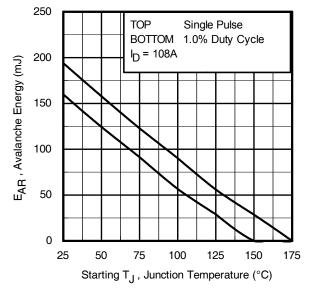


Fig 15. Maximum Avalanche Energy vs. Temperature

Notes on Repetitive Avalanche Curves , Figures 14, 15: (For further info, see AN-1005 at www.irf.com) 1.Avalanche failures assumption:

Purely a thermal phenomenon and failure occurs at a temperature far in excess of Tjmax. This is validated for every part type.

- Safe operation in Avalanche is allowed as long asTjmax is not exceeded.
- 3. Equation below based on circuit and waveforms shown in Figures 22a,22b.
- 4. PD (ave) = Average power dissipation per single avalanche pulse.
- 5. BV = Rated breakdown voltage (1.3 factor accounts for voltage increase during avalanche).
- 6. lav = Allowable avalanche current.
- 7. ΔT = Allowable rise in junction temperature, not exceed T_{jmax} (assumed as 25°C in figure 14 , 15).
- tav = Average time in avalanche.
- D = Duty cycle in avalanche = tav f
- ZthJC (D, tav) = Transient thermal resistance, see Figures 13)

```
 \begin{array}{l} \mathsf{P}_{\mathsf{D}\;(ave)} = 1/2\;(\;1.3{\cdot}\mathsf{BV}{\cdot}\mathsf{I}_{av}) = \Delta\mathsf{T}/\;\mathsf{Z}_{thJC}\\ \mathsf{I}_{av} = 2\Delta\mathsf{T}/\;[1.3{\cdot}\mathsf{BV}{\cdot}\mathsf{Z}_{th}]\\ \mathsf{E}_{\mathsf{AS}\;(\mathsf{AR})} = \mathsf{PD}\;_{(ave)}{\cdot}t_{av} \end{array}
```



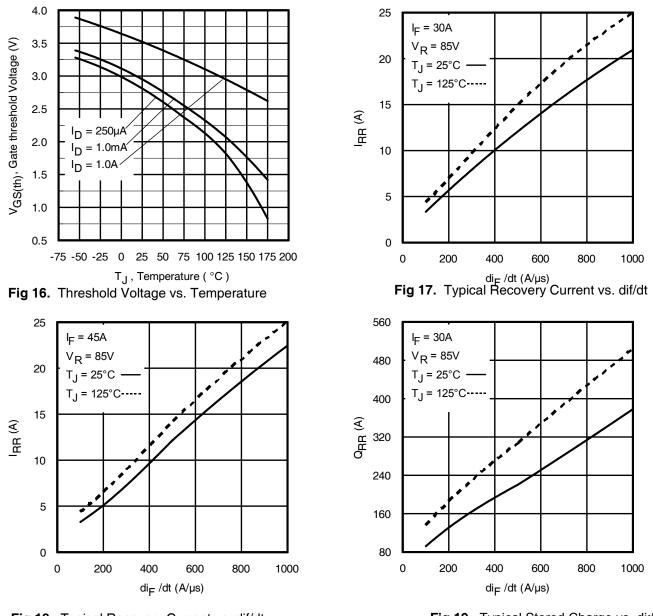


Fig 18. Typical Recovery Current vs. dif/dt

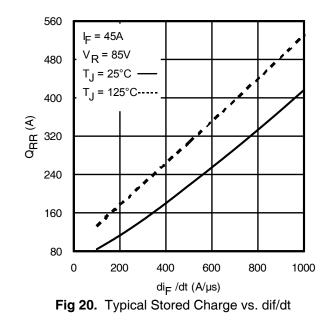


Fig 19. Typical Stored Charge vs. dif/dt



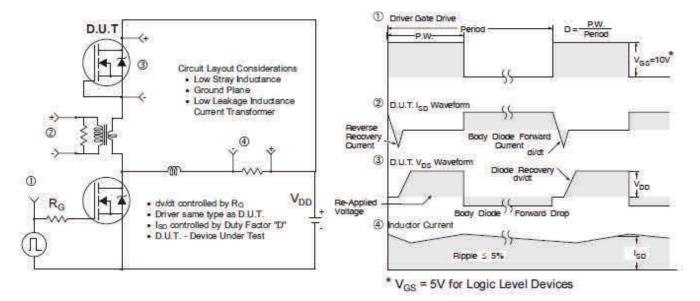


Fig 21. Peak Diode Recovery dv/dt Test Circuit for N-Channel HEXFET[®] Power MOSFETs

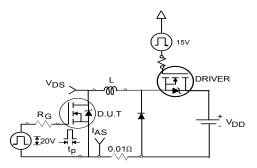


Fig 22a. Unclamped Inductive Test Circuit

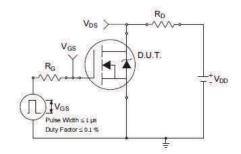


Fig 23a. Switching Time Test Circuit

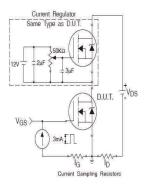
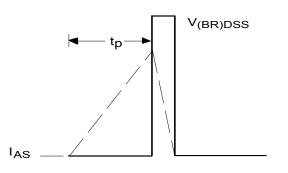
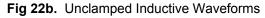


Fig 24a. Gate Charge Test Circuit

8





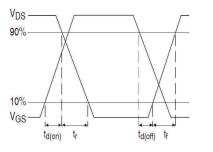


Fig 23b. Switching Time Waveforms

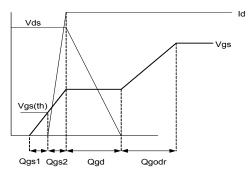
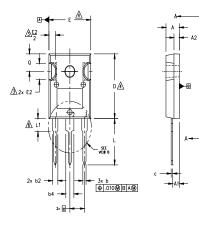


Fig 24b. Gate Charge Waveform

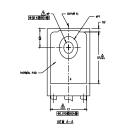


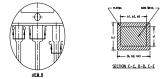
TO-247AC Package Outline

Dimensions are shown in millimeters (inches)





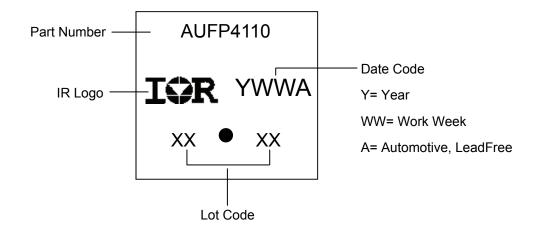




TO-247AC Part Marking Information

- NOTES:
- 1, DIMENSIONING AND TOLERANCING AS PER ASME Y14.5M 1994.
- DIMENSIONS ARE SHOWN IN INCHES.
- CONTOUR OF SLOT OPTIONAL.
- \mathbb{A} DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED .005" (0.127) PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY.
- THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSIONS D1 & E1.
- LEAD FINISH UNCONTROLLED IN L1.
- A A OP TO HAVE A MAXIMUM DRAFT ANGLE OF 1.5 ' TO THE TOP OF THE PART WITH A MAXIMUM HOLE DIAMETER OF .154 INCH.
- OUTLINE CONFORMS TO JEDEC OUTLINE TO-247AC . 8

	DIMENSIONS					
SYMBOL	INC	HES	Millin	ETERS	1	
	Min.	MAX.	Min.	MAX.	NOTES	
A	.183	.209	4.65	5.31		
A1	.087	.102	2.21	2.59		
A2	.059	.098	1.50	2.49		
b	.039	.055	0.99	1.40		
b1	.039	.053	0.99	1.35		LEAD ASSIGNMENTS
b2	.065	.094	1.65	2.39		
b3	.065	.092	1.65	2.34		HEXFET
b4	.102	.135	2.59	3.43		
b5	.102	.133	2.59	3.38		1 GATE
с	.015	.035	0.38	0.89		2 DRAIN
c1	.015	.033	0.38	0.84		3 SOURCE
D	.776	.815	19.71	20.70	4	4.– DRAIN
D1	.515	-	13.08	-	5	
D2	.020	.053	0.51	1.35		
Е	.602	.625	15.29	15.87	4	IGBTs, CoPACK
E1	.530	-	13.46	-		1 GATE
E2	.178	.216	4.52	5.49		2 COLLECTOR
е	.215	BSC	5.46	BSC		3 EMITTER
Øk	.0	.010		0.25		4 COLLECTOR
L	.559	.634	14.20	16.10		
L1	.146	.169	3.71	4.29		
øP	.140	.144	3.56	3.66		DIODES
øP1	-	.291	-	7.39		
Q	.209	.224	5.31	5,69		1 ANODE/OPEN
S	.217	BSC	5.51	BSC		2 CATHODE
		3.– ANODE				



TO-247AC package is not recommended for Surface Mount Application.

Note: For the most current drawing please refer to IR website at http://www.irf.com/package/

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