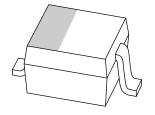
DISCRETE SEMICONDUCTORS

DATA SHEET



BAP51-03General purpose PIN diode

Product specification Supersedes data of 1999 Aug 16 2004 Feb 11



General purpose PIN diode

BAP51-03

FEATURES

- Low diode capacitance
- Low diode forward resistance.

APPLICATIONS

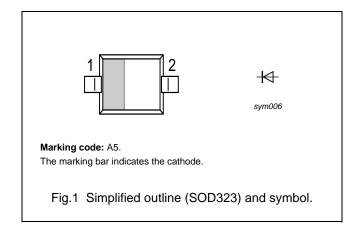
· General RF applications.

DESCRIPTION

General purpose PIN diode in a SOD323 small plastic SMD package.

PINNING

PIN	DESCRIPTION
1	cathode
2	anode



ORDERING INFORMATION

TYPE		PACKAGE				
NUMBER	NAME	DESCRIPTION VERSION VE				
BAP51-03	_	plastic surface mounted package; 2 leads	SOD323			

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_R	continuous reverse voltage		_	50	V
I _F	continuous forward current		_	50	mA
P _{tot}	total power dissipation	T _S = 90 °C	_	500	mW
T _{stg}	storage temperature		-65	+150	°C
T _j	junction temperature		-65	+150	°C

General purpose PIN diode

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ELECTRICAL CHARACTERISTICS

 $T_j = 25$ °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _F	forward voltage	I _F = 50 mA	_	0.95	1.1	V
V _R	reverse voltage	I _R = 10 μA	50	_	_	V
I _R	reverse current	V _R = 50 V	_	_	100	nA
C _d	diode capacitance	V _R = 0; f = 1 MHz	_	0.4	_	pF
		V _R = 1 V; f = 1 MHz	_	0.3	0.55	pF
		V _R = 5 V; f = 1 MHz	_	0.2	0.35	pF
r _D	diode forward resistance	I _F = 0.5 mA; f = 100 MHz; note 1	_	5.5	9	Ω
		I _F = 1 mA; f = 100 MHz; note 1	_	3.6	6.5	Ω
		I _F = 10 mA; f = 100 MHz; note 1	_	1.5	2.5	Ω
τ∟	charge carrier life time	when switched from I_F = 10 mA to I_R = 6 mA; R_L = 100 Ω ; measured at I_R = 3 mA	_	550	_	ns

Note

1. Guaranteed on AQL basis: inspection level S4, AQL 1.0.

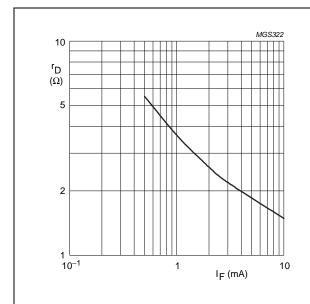
THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	VALUE	UNIT
$R_{th(j-s)}$	thermal resistance from junction to soldering point		K/W

General purpose PIN diode

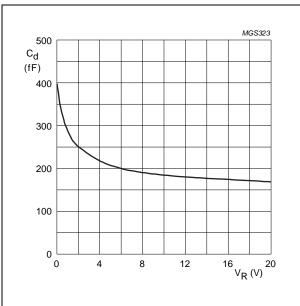
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GRAPHICAL DATA



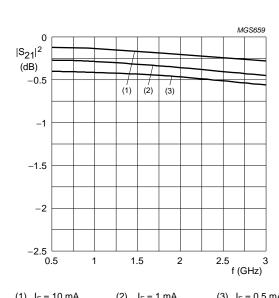
 $T_i = 25 \,^{\circ}C$; f = 100 MHz.

Fig.2 Forward resistance as a function of forward current; typical values.



 $T_j = 25$ °C; f = 1 MHz.

Fig.3 Diode capacitance as a function of reverse voltage; typical values.



(2) $I_F = 1 \text{ mA}.$

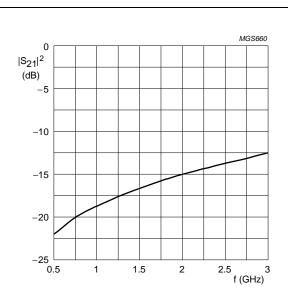
(3) $I_F = 0.5 \text{ mA}.$

4

Diode inserted in series with a 50 Ω stripline circuit and biased via the analyzer Tee network.

 $T_{amb} = 25 \, ^{\circ}C.$

Fig.4 Insertion loss ($|S_{21}|^2$) of the diode as a function of frequency; typical values.



Diode zero biased and inserted in series with a 50 Ω stripline circuit. $T_{amb} = 25 \, ^{\circ}C.$

Fig.5 Isolation $(|S_{21}|^2)$ of the diode as a function of frequency; typical values.

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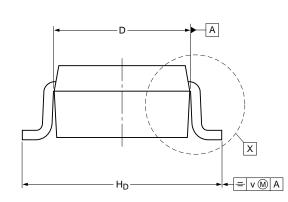
General purpose PIN diode

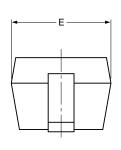
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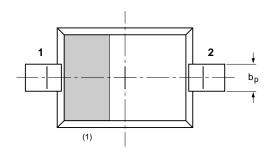
PACKAGE OUTLINE

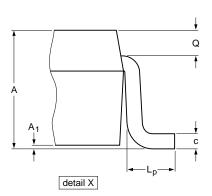
Plastic surface-mounted package; 2 leads

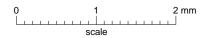
SOD323











DIMENSIONS (mm are the original dimensions)

UNIT	Α	A ₁ max	bp	С	D	E	H _D	Lp	Q	v
mm	1.1 0.8	0.05	0.40 0.25	0.25 0.10	1.8 1.6	1.35 1.15	2.7 2.3	0.45 0.15	0.25 0.15	0.2

Note

1. The marking bar indicates the cathode

OUTLINE	REFERENCES				EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	JEITA		PROJECTION	
SOD323			SC-76			03-12-17 06-03-16

General purpose PIN diode

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DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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General purpose PIN diode

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This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

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