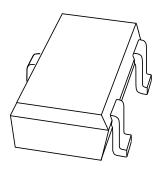
DISCRETE SEMICONDUCTORS

DATA SHEET



BAP64-06W Silicon PIN diode

Product specification Supersedes data of 2001 Feb 02 2001 Apr 17



Silicon PIN diode BAP64-06W

FEATURES

- High voltage, current controlled
- RF resistor for RF attenuators and switches
- Low diode capacitance
- Low diode forward resistance
- Low series inductance
- For applications up to 3 GHz.

APPLICATIONS

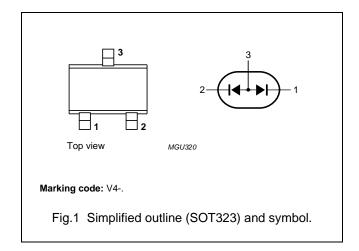
• RF attenuators and switches.

DESCRIPTION

Two planar PIN diodes in common anode configuration in a SOT323 small SMD plastic package.

PINNING

| PIN | DESCRIPTION |
|-----|-------------------|
| 1 | cathode 1 |
| 2 | cathode 2 |
| 3 | common connection |



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

| SYMBOL | PARAMETER | CONDITIONS | MIN. | MAX. | UNIT |
|------------------|----------------------------|------------------------|------|------|------|
| Per diode | | | | | |
| V _R | continuous reverse voltage | | _ | 100 | V |
| IF | continuous forward current | | _ | 100 | mA |
| P _{tot} | total power dissipation | T _s = 90 °C | _ | 240 | mW |
| T _{stg} | storage temperature | | -65 | +150 | °C |
| Tj | junction temperature | | -65 | +150 | °C |

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ELECTRICAL CHARACTERISTICS

 $T_j = 25$ °C unless otherwise specified.

| SYMBOL | PARAMETER | CONDITIONS | TYP. | MAX. | UNIT | | |
|--------------------------------|--------------------------|---|------|------|------|--|--|
| Per diode | | | | | | | |
| V _F | forward voltage | I _F = 50 mA | 0.95 | 1.1 | V | | |
| I _R | reverse current | V _R = 100 V | _ | 10 | μА | | |
| | | V _R = 20 V | _ | 1 | μΑ | | |
| C _d | diode capacitance | V _R = 0; f = 1 MHz | 0.52 | _ | pF | | |
| | | V _R = 1 V; f = 1 MHz | 0.37 | _ | pF | | |
| | | V _R = 20 V; f = 1 MHz | 0.23 | 0.35 | pF | | |
| r _D | diode forward resistance | I _F = 0.5 mA; f = 100 MHz; note 1 | 20 | 40 | Ω | | |
| | | I _F = 1 mA; f = 100 MHz; note 1 | 10 | 20 | Ω | | |
| | | I _F = 10 mA; f = 100 MHz; note 1 | 2 | 3.8 | Ω | | |
| | | I _F = 100 mA; f = 100 MHz; note 1 | 0.7 | 1.35 | Ω | | |
| $ s_{21} ^2$ | isolation | V _R = 0; f = 900 MHz | 18.5 | _ | dB | | |
| | | V _R = 0; f = 1800 MHz | 13.5 | _ | dB | | |
| | | V _R = 0; f = 2450 MHz | 10.9 | _ | dB | | |
| $ s_{21} ^2$ | insertion loss | I _F = 0.5 mA; f = 900 MHz | 1.86 | _ | dB | | |
| | | I _F = 0.5 mA; f = 1800 MHz | 2.06 | _ | dB | | |
| | | I _F = 0.5 mA; f = 2450 MHz | 2.23 | _ | dB | | |
| s ₂₁ ² | insertion loss | I _F = 1 mA; f = 900 MHz | 1.01 | _ | dB | | |
| | | I _F = 1 mA; f = 1800 MHz | 1.06 | _ | dB | | |
| | | I _F = 1 mA; f = 2450 MHz | 1.10 | _ | dB | | |
| $ s_{21} ^2$ | insertion loss | I _F = 10 mA; f = 900 MHz | 0.19 | _ | dB | | |
| | | I _F = 10 mA; f = 1800 MHz | 0.21 | _ | dB | | |
| | | I _F = 10 mA; f = 2450 MHz | 0.27 | _ | dB | | |
| $ s_{21} ^2$ | insertion loss | I _F = 100 mA; f = 900 MHz | 0.08 | _ | dB | | |
| | | I _F = 100 mA; f = 1800 MHz | 0.10 | _ | dB | | |
| | | I _F = 100 mA; f = 2450 MHz | 0.16 | _ | dB | | |
| τ∟ | charge carrier life time | when switched from I_F = 10 mA to I_R = 6 mA; R_L = 100 Ω ; measured at I_R = 3 mA | 1.55 | - | μs | | |
| L _S | series inductance | I _F = 100 mA; f = 100 MHz | 1.6 | | nH | | |

Note

1. Guaranteed on AQL basis: inspection level S4, AQL 1.0.

THERMAL CHARACTERISTICS

| SYMBOL | PARAMETER | VALUE | UNIT |
|---------------------|---|-------|------|
| R _{th j-s} | thermal resistance from junction to soldering point | 250 | K/W |

Silicon PIN diode **BAP64-06W**

GRAPHICAL DATA

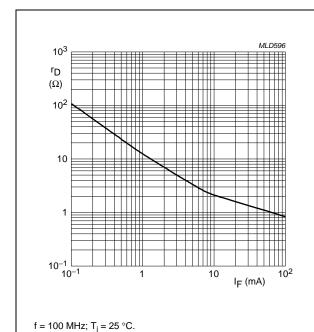


Fig.2 Forward resistance as a function of forward current; typical values.

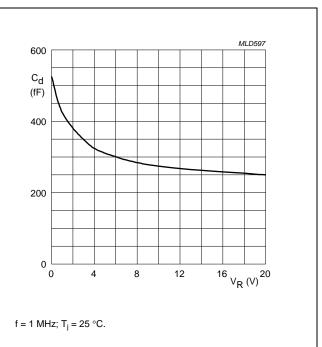
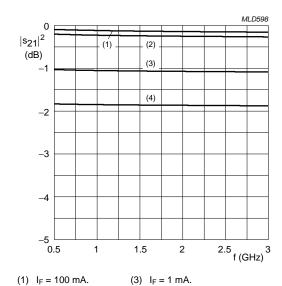


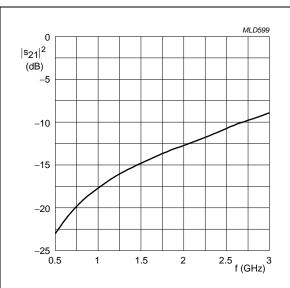
Fig.3 Diode capacitance as a function of reverse voltage; typical values.



- (3) $I_F = 1 \text{ mA}.$
- (2) $I_F = 10 \text{ mA}.$
- (4) $I_F = 0.5 \text{ mA}.$

Diode inserted in series with a 50 Ω stripline circuit and biased via the analyzer Tee network; T_{amb} = 25 °C.

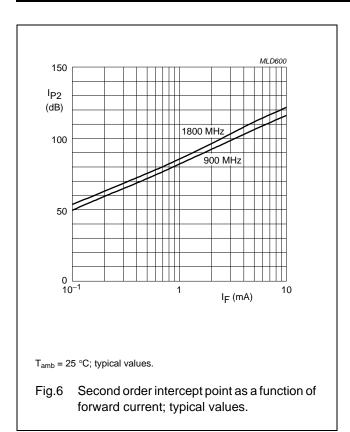
Insertion loss ($|s_{21}|^2$) of the diode as a function of frequency; typical values.



Diode zero biased and inserted in series with a 50 Ω stripline circuit. $T_{amb} = 25 \, ^{\circ}C.$

Fig.5 Isolation $(|s_{21}|^2)$ of the diode as a function of frequency; typical values.

Silicon PIN diode BAP64-06W

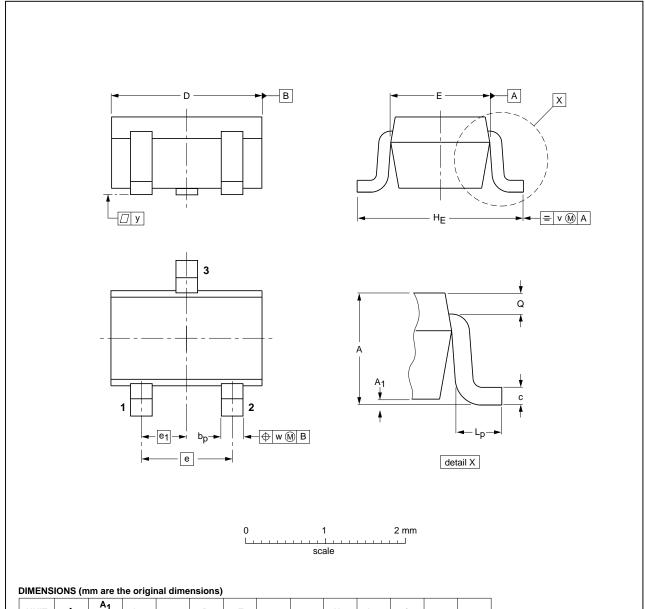


Silicon PIN diode BAP64-06W

PACKAGE OUTLINE

Plastic surface-mounted package; 3 leads

SOT323



| UNIT | Α | A ₁ max | bp | С | D | E | е | e ₁ | HE | Lp | Q | v | w |
|------|------------|-----------------------|------------|--------------|------------|--------------|-----|----------------|------------|--------------|--------------|-----|-----|
| mm | 1.1 0.8 | 0.1 | 0.4 0.3 | 0.25 0.10 | 2.2 1.8 | 1.35 1.15 | 1.3 | 0.65 | 2.2 2.0 | 0.45 0.15 | 0.23 0.13 | 0.2 | 0.2 |

| OUTLINE | | REFER | RENCES | EUROPEAN | ISSUE DATE |
|---------|-----|-------|--------|------------|----------------------------------|
| VERSION | IEC | JEDEC | JEITA | PROJECTION | ISSUE DATE |
| SOT323 | | | SC-70 | | -04-11-04 06-03-16 |

Silicon PIN diode BAP64-06W

DATA SHEET STATUS

| DOCUMENT STATUS ⁽¹⁾ | PRODUCT STATUS ⁽²⁾ | DEFINITION |
|-----------------------------------|----------------------------------|---|
| Objective data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary data sheet | Qualification | This document contains data from the preliminary specification. |
| Product data sheet | Production | This document contains the product specification. |

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2001 Apr 17

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Printed in The Netherlands R77/02/pp9 Date of release: 2001 Apr 17

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BAP64-06W,115