

# **45 V, 100 mA PNP general-purpose transistors** Rev. 1 — 21 February 2012

Product data sheet

#### 1. **Product profile**

#### 1.1 General description

PNP general-purpose transistors in a leadless ultra small SOT883B Surface-Mounted Device (SMD) plastic package.

#### Table 1. **Product overview**

Type number	Package	Package		
	NXP	JEITA	JEDEC	
BC857AMB	SOT883B	-	-	BC847AMB
BC857BMB	SOT883B	-	-	BC847BMB
BC857CMB	SOT883B	-	-	BC847CMB

#### 1.2 Features and benefits

- Leadless ultra small SMD plastic Power dissipation comparable to SOT23 package
- Low package height of 0.37 mm
- AEC-Q101 qualified

#### **1.3 Applications**

- General-purpose switching and amplification
- Mobile applications

#### 1.4 Quick reference data

Table 2.	Quick reference data					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	-45	V
I <sub>C</sub>	collector current		-	-	-100	mA
h <sub>FE</sub>	DC current gain	$V_{CE} = -5 \text{ V}; \text{ I}_{C} = -2 \text{ mA}$				
	BC857AMB		125	-	250	
	BC857BMB		220	-	475	
	BC857CMB		420	-	800	



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#### 2. Pinning information

Pinning	
Description	Simplified outline Graphic symbol
base	
emitter	
collector	
	Transparent 2
	top view 2 sym013
	Description base emitter

### 3. Ordering information

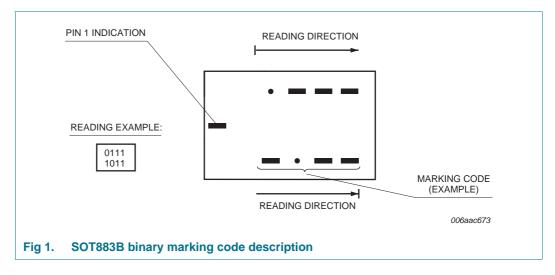
Table 4. Orderin	ng informat	tion	
Type number	Package		
	Name	Description	Version
BC857xMB series	-	leadless ultra small plastic package; 3 solder lands; body 1.0 $\times$ 0.6 $\times$ 0.37 mm	SOT883B

#### 4. Marking

Type number	Marking code <sup>[1]</sup>
BC857AMB	0100 0100
BC857BMB	0100 0101
BC857CMB	0100 0110

[1] For SOT883B binary marking code description, see Figure 1.

#### 4.1 Binary marking code description



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### 5. Limiting values

Table 6. In accorda	Limiting values nce with the Absolute Maximu	m Rating System	(IEC 60	)134).		
Symbol	Parameter	Conditions		Min	Мах	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	-50	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-45	V
$V_{\text{EBO}}$	emitter-base voltage	open collector		-	-5	V
I <sub>C</sub>	collector current			-	-100	mA
I <sub>CM</sub>	peak collector current	single pulse; $t_p \leq 1 \text{ ms}$		-	-200	mA
I <sub>BM</sub>	peak base current	single pulse; $t_p \leq 1 \text{ ms}$		-	-100	mA
P <sub>tot</sub>	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$	[1][2]	-	250	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	+150	°C
T <sub>stg</sub>	storage temperature			-65	+150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Reflow soldering is the only recommended soldering method.

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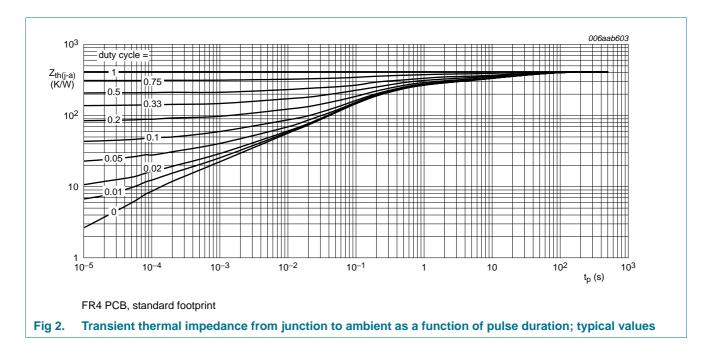
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#### 6. Thermal characteristics

Table 7.	Thermal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	<u>[1][2]</u> _	-	500	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Reflow soldering is the only recommended soldering method.



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### 7. Characteristics

$T_{amb} = 25$	$5~{}^{\circ}\!$	pecified.					
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I <sub>CBO</sub>	collector-base	$V_{CB} = -30 \text{ V}; \text{ I}_{E} = 0 \text{ A}$		-	-	-15	nA
	cut-off current	$\label{eq:VCB} \begin{array}{l} V_{CB} = -30 \ \text{V}; \ \textbf{I}_{E} = 0 \ \text{A}; \\ T_{j} = 150 \ ^{\circ}\text{C} \end{array}$		-	-	-5	μA
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = -5 \text{ V}; \text{ I}_{C} = 0 \text{ A}$		-	-	-100	nA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = -5 V; I <sub>C</sub> = -2 mA					
	BC857AMB		125	-	250		
	BC857BMB			220	-	475	
	BC857CMB			420	-	800	
V <sub>CEsat</sub>	collector-emitter	$I_{C} = -10 \text{ mA}; I_{B} = -0.5 \text{ mA}$		-	-	-200	mV
	saturation voltage	$I_{C} = -100 \text{ mA}; I_{B} = -5 \text{ mA}$	[1]	-	-	-400	mV
$V_{BE}$	base-emitter voltage	$I_C = -2 \text{ mA}; V_{CE} = -5 \text{ V}$		-600	-	-750	mV
		$I_C = -10 \text{ mA}; V_{CE} = -5 \text{ V}$		-	-	-820	mV
f <sub>T</sub>	transition frequency	$V_{CE} = -5 \text{ V}; \text{ I}_{C} = -10 \text{ mA};$ f = 100 MHz		100	-	-	MHz
Cc	collector capacitance	$\label{eq:VCB} \begin{array}{l} V_{CB}=-10 \text{ V};  I_{E}=i_{e}=0 \text{ A}; \\ f=1 \text{ MHz} \end{array}$		-	-	2.5	pF
NF	noise figure	$\label{eq:lc} \begin{array}{l} I_{C} = -200 \ \mu A; \ V_{CE} = -5 \ V; \\ R_{S} = 2 \ k\Omega; \ f = 1 \ kHz; \\ B = 200 \ Hz \end{array}$		-	-	10	dB

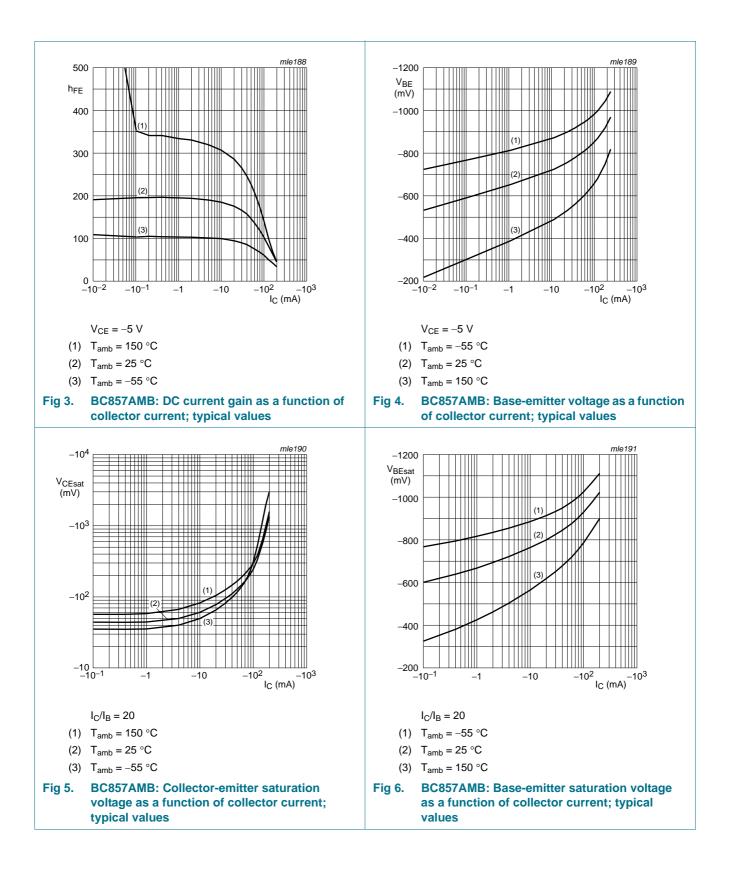
[1] Pulse test:  $t_p \le 300 \ \mu s; \ \delta \le 0.02.$ 

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### **BC857xMB** series

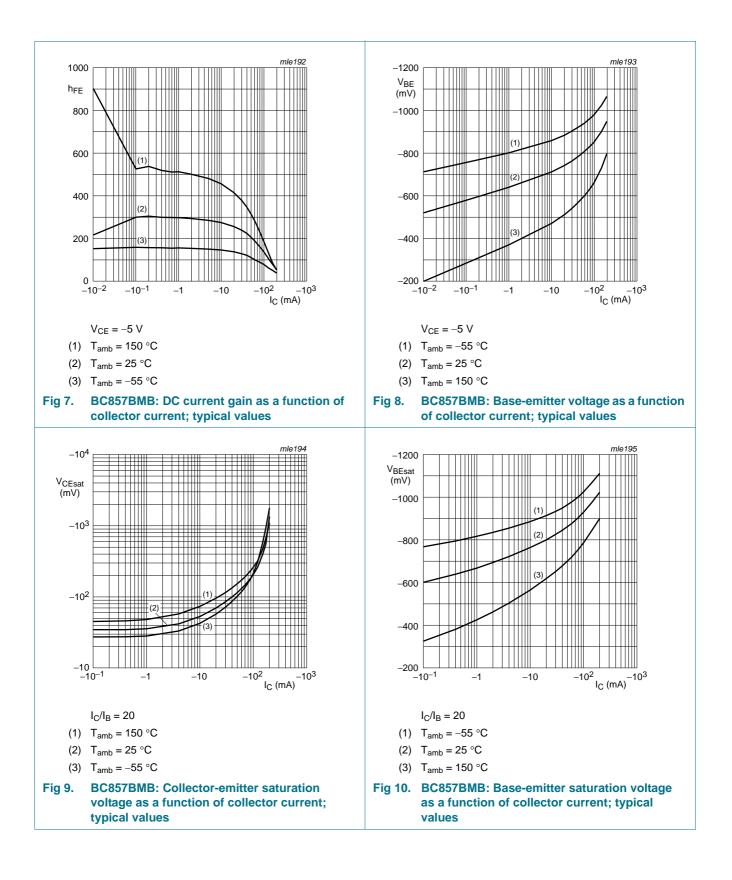
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#### **NXP Semiconductors**

### **BC857xMB series**

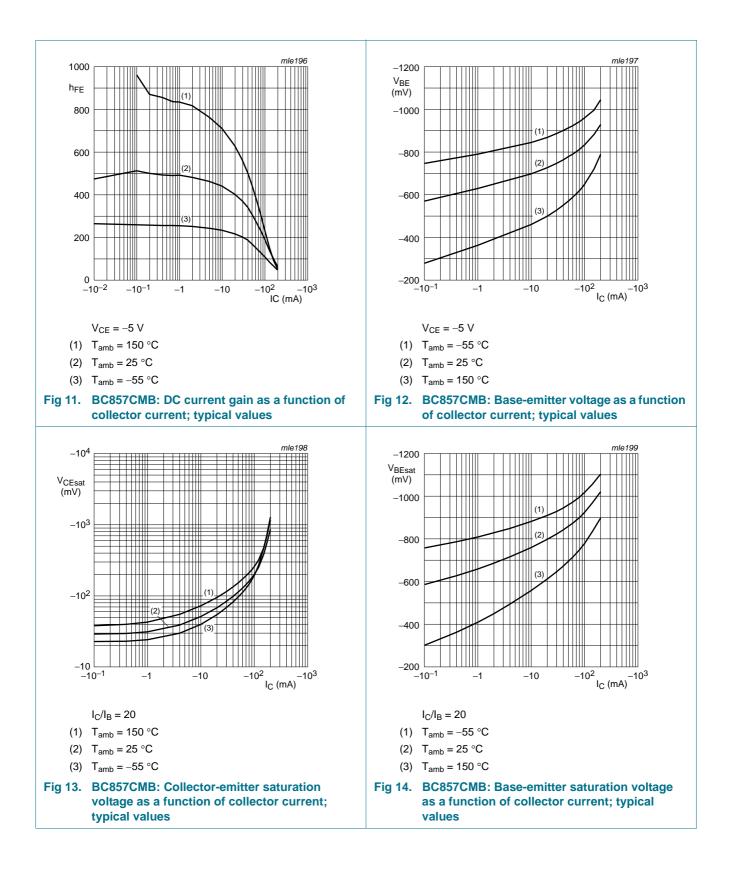
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#### **NXP Semiconductors**

### **BC857xMB series**

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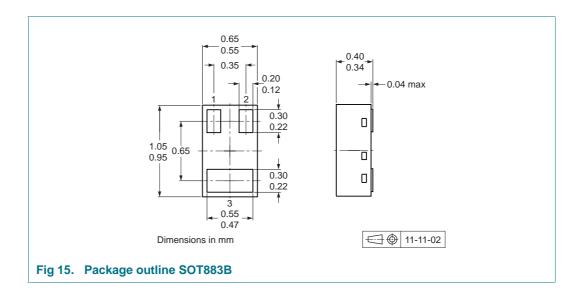
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#### 8. Test information

#### 8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

#### 9. Package outline



#### **10. Packing information**

#### Table 9. Packing methods

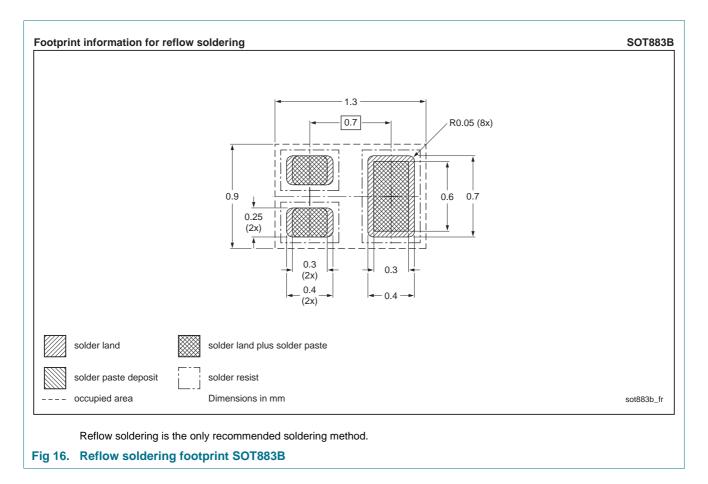
The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Type number	Package	Description	Packing quantity
			10000
BC857xMB series	SOT883B	2 mm pitch, 8 mm tape and reel	-315

[1] For further information and the availability of packing methods, see Section 14.

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#### 11. Soldering



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### **12. Revision history**

Table 10. Revision hist	ory			
Document ID	Release date	Data sheet status	Change notice	Supersedes
BC857XMB_SER v.1	20120221	Product data sheet	-	•

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#### **13. Legal information**

#### 13.1 Data sheet status

Document status[1][2]	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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