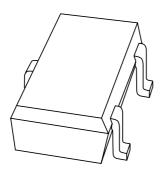
DISCRETE SEMICONDUCTORS

DATA SHEET



BC859W; BC860W PNP general purpose transistors

Product data sheet Supersedes data of 1997 Sep 03 1999 Apr 12



PNP general purpose transistors

BC859W; BC860W

FEATURES

• Low current (max. 100 mA)

• Low voltage (max. 45 V).

APPLICATIONS

• Low noise stages in tape recorders, hi-fi amplifiers and other audio-frequency equipment.

DESCRIPTION

PNP transistor in a SOT323 plastic package. NPN complements: BC849W and BC850W.

MARKING

TYPE NUMBER	MARKING CODE	TYPE NUMBER	MARKING CODE	
BC859W	4D*	BC860W	4H*	
BC859BW	4B*	BC860BW	4F*	
BC859CW	4C*	BC860CW	4G*	

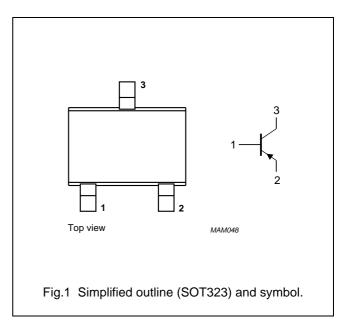
Note

1. * = -: Made in Hong Kong.

* = t : Made in Malaysia.

PINNING

PIN	DESCRIPTION	
1	base	
2	emitter	
3	collector	



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter			
	BC859W		_	-30	V
	BC860W		_	-50	V
V _{CEO}	collector-emitter voltage	open base			
	BC859W		_	-30	V
	BC860W		_	-45	V
V _{EBO}	emitter-base voltage	open collector	_	- 5	V
I _C	collector current (DC)		_	-100	mA
I _{CM}	peak collector current		_	-200	mA
I _{BM}	peak base current		_	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	200	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board.

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PNP general purpose transistors

BC859W; BC860W

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	note 1	625	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CBO}	collector cut-off current	I _E = 0; V _{CB} = -30 V	_	_	-15	nA
		$I_E = 0$; $V_{CB} = -30 \text{ V}$; $T_j = 150 ^{\circ}\text{C}$	_	_	-4	μΑ
I _{EBO}	emitter cut-off current	$I_C = 0; V_{EB} = -5 \text{ V}$	_	_	-100	nA
h _{FE}	DC current gain	$I_C = -2 \text{ mA}; V_{CE} = -5 \text{ V};$				
	BC859W; BC860W	see Figs 2 and 3	220	_	800	
	BC859BW; BC860BW		220	_	475	
	BC859CW; BC860CW		420	_	800	
V _{CEsat}	collector-emitter saturation	$I_C = -10 \text{ mA}; I_B = -0.5 \text{ mA}$	_	_	-300	mV
	voltage	$I_C = -100 \text{ mA}$; $I_B = -5 \text{ mA}$; note 1	_	_	-650	mV
V _{BE}	base-emitter voltage	$I_C = -2 \text{ mA}; V_{CE} = -5 \text{ V}$	600	_	750	mV
		$I_C = -10 \text{ mA}; V_{CE} = -5 \text{ V}$	-	_	820	mV
C _c	collector capacitance	$I_E = i_e = 0$; $V_{CB} = -10 \text{ V}$; $f = 1 \text{ MHz}$	_	-	5	pF
C _e	emitter capacitance	$I_C = I_c = 0$; $V_{EB} = -500 \text{ mV}$; $f = 1 \text{ MHz}$	_	10	_	pF
f _T	transition frequency	$I_C = -10 \text{ mA}; V_{CE} = -5 \text{ V}; f = 100 \text{ MHz}$	100	_	_	MHz
F	noise figure; BC859W; BC860W;	$I_{C} = -200 \ \mu A; \ V_{CE} = -5 \ V; \ R_{S} = 2 \ k\Omega;$ f = 10 Hz to 15.7 kHz	_	-	4	dB
BC859BW; BC860BW; BC859CW; BC860CW		$\begin{split} I_C = -200 \; \mu\text{A}; \; V_{CE} = -5 \; V; \; R_S = 2 \; k\Omega; \\ f = 1 \; k\text{Hz}; \; B = 200 \; \text{Hz} \end{split}$	_	_	4	dB

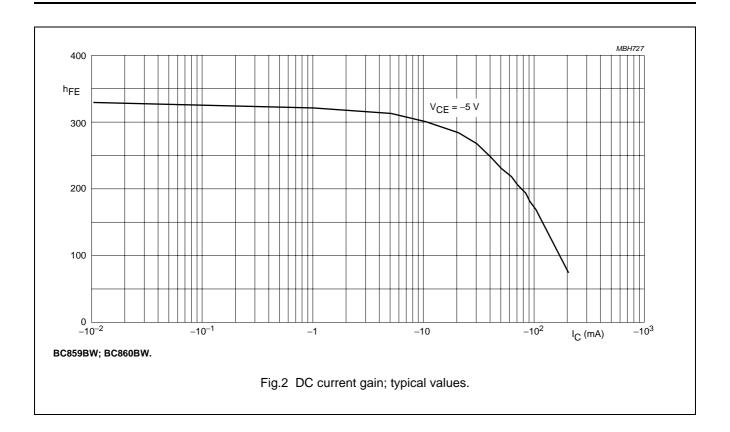
Note

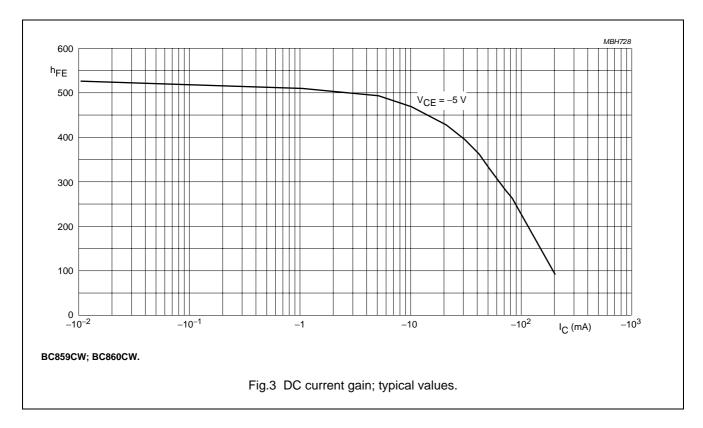
1. Pulse test: $t_p \leq 300~\mu s;~\delta \leq 0.02.$

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PNP general purpose transistors

BC859W; BC860W





4

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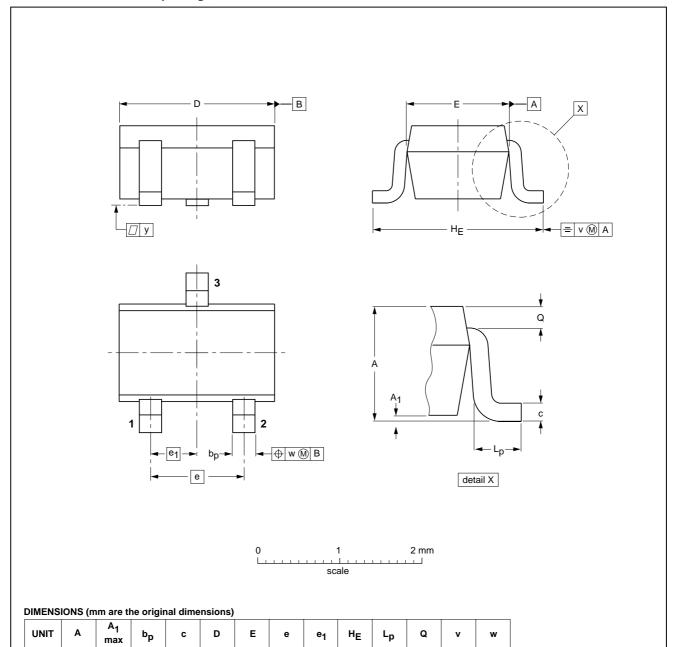
PNP general purpose transistors

BC859W; BC860W

PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT323



OUTLINE	REFERENCES		EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT323			SC-70			97-02-28

0.2

0.65

1.3

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0.4

0.1

0.25

PNP general purpose transistors

BC859W; BC860W

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

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