# BCM856BS; BCM856BS/DG BCM856DS; BCM856DS/DG

**PNP/PNP** matched double transistors

Rev. 01 — 7 August 2008

**Product data sheet** 

### 1. Product profile

#### 1.1 General description

PNP/PNP matched double transistors in small Surface-Mounted Device (SMD) plastic packages. The transistors are fully isolated internally.

#### Table 1.Product overview

Type number	Package		Package configuration
	NXP	JEITA	
BCM856BS	SOT363	SC-88	very small
BCM856BS/DG			
BCM856DS	SOT457	SC-74	small
BCM856DS/DG			

#### **1.2 Features**

- Current gain matching
- Base-emitter voltage matching
- Drop-in replacement for standard double transistors
- AEC-Q101 qualified

#### **1.3 Applications**

- Current mirror
- Differential amplifier

#### 1.4 Quick reference data

#### Table 2. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per transis	stor					
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	-65	V
I <sub>C</sub>	collector current		-	-	-100	mA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = -5 V; I <sub>C</sub> = -2 mA	200	290	450	



#### **NXP Semiconductors**

## BCM856BS; BCM856DS

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Table 2.		liinuea				
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per device						
$h_{FE1}/h_{FE2}$	h <sub>FE</sub> matching	$V_{CE} = -5 V;$ $I_C = -2 mA$	<u>[1]</u> 0.9	1	-	
$V_{BE1} - V_{BE2}$	V <sub>BE</sub> matching	$V_{CE} = -5 V;$ $I_{C} = -2 mA$	[2]	-	2	mV

 Table 2.
 Quick reference data ...continued

 $\begin{tabular}{ll} [1] & The smaller of the two values is taken as the numerator. \end{tabular}$ 

[2] The smaller of the two values is subtracted from the larger value.

### 2. Pinning information

Table 3.	Pinning	
Pin	Description	Simplified outline Graphic symbol
1	emitter TR1	
2	base TR1	
3	collector TR2	
4	emitter TR2	
5	base TR2	
6	collector TR1	001aab555
		sym018

## 3. Ordering information

Table 4.         Ordering information					
Type number	Package	Package			
	Name	Description	Version		
BCM856BS	SC-88	plastic surface-mounted package; 6 leads	SOT363		
BCM856BS/DG					
BCM856DS	SC-74	plastic surface-mounted package (TSOP6); 6 leads	SOT457		
BCM856DS/DG					

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### 4. Marking

Table 5. Marking codes	
Type number	Marking code <sup>[1]</sup>
BCM856BS	*BS
BCM856BS/DG	PB*
BCM856DS	DS
BCM856DS/DG	R9

- [1] \* = -: made in Hong Kong
  - \* = p: made in Hong Kong
  - \* = t: made in Malaysia
  - \* = W: made in China

### 5. Limiting values

#### Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
Per trans	istor				
V <sub>CBO</sub>	collector-base voltage	open emitter	-	-80	V
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-65	V
$V_{\text{EBO}}$	emitter-base voltage	open collector	-	-5	V
I <sub>C</sub>	collector current		-	-100	mA
I <sub>CM</sub>	peak collector current	single pulse; $t_p \leq 1 ms$	-	-200	mA
P <sub>tot</sub>	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$			
	BCM856BS (SOT363) BCM856BS/DG (SOT363)		<u>[1]</u>	200	mW
	BCM856DS (SOT457) BCM856DS/DG (SOT457)		<u>[1]</u> -	250	mW
Per devic	ce la				
P <sub>tot</sub>	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$			
	BCM856BS (SOT363) BCM856BS/DG (SOT363)		<u>[1]</u> _	300	mW
	BCM856DS (SOT457) BCM856DS/DG (SOT457)		<u>[1]</u> _	380	mW
Tj	junction temperature		-	150	°C
T <sub>amb</sub>	ambient temperature		-55	+150	°C
T <sub>stg</sub>	storage temperature		-65	+150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

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### 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per trans	sistor					
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air				
	BCM856BS (SOT363) BCM856BS/DG (SOT363)		<u>[1]</u> -	-	625	K/W
	BCM856DS (SOT457) BCM856DS/DG (SOT457)		<u>[1]</u> -	-	500	K/W
Per devi	се					
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air				
	BCM856BS (SOT363) BCM856BS/DG (SOT363)		<u>[1]</u> -	-	416	K/W
	BCM856DS (SOT457) BCM856DS/DG (SOT457)		<u>[1]</u> _	-	328	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

### 7. Characteristics

#### Table 8.Characteristics

 $T_{amb} = 25 \circ C$  unless otherwise specified.

dinio						
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per transi	istor					
I <sub>CBO</sub> collector-base cut- current	collector-base cut-off current	V <sub>CB</sub> = -30 V; I <sub>E</sub> = 0 A	-	-	-15	nA
		V <sub>CB</sub> = -30 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C	-	-	-5	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = -5 V;$ $I_C = 0 A$	-	-	-100	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = -5 V; I <sub>C</sub> = -10 μA	-	250	-	
		$V_{CE} = -5 V;$ $I_{C} = -2 mA$	200	290	450	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_{\rm C} = -10 \text{ mA};$ $I_{\rm B} = -0.5 \text{ mA}$	-	-50	-200	mV
		$I_{\rm C}$ = -100 mA; $I_{\rm B}$ = -5 mA	-	-200	-400	mV
V <sub>BEsat</sub>	base-emitter saturation voltage	$I_{\rm C} = -10 \text{ mA};$ $I_{\rm B} = -0.5 \text{ mA}$	<u>[1]</u> -	-760	-	mV
		l <sub>C</sub> = –100 mA; l <sub>B</sub> = –5 mA	<u>[1]</u> -	-920	-	mV

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>BE</sub>	base-emitter voltage	$V_{CE} = -5 V;$ $I_C = -2 mA$	[2] -600	-650	-700	mV
		$V_{CE} = -5 V;$ $I_{C} = -10 mA$	[2] _	-	-760	mV
C <sub>c</sub>	collector capacitance	$V_{CB} = -10 \text{ V};$ $I_E = i_e = 0 \text{ A};$ f = 1  MHz	-	-	2.2	pF
C <sub>e</sub>	emitter capacitance	$V_{EB} = -0.5 V;$ $I_{C} = i_{c} = 0 A;$ f = 1 MHz	-	10	-	pF
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = -5 V; I <sub>C</sub> = -10 mA; f = 100 MHz	100	175	-	MHz
NF	noise figure	$V_{CE} = -5 V;$ $I_C = -0.2 mA;$ $R_S = 2 k\Omega;$ f = 10 Hz to 15.7 kHz	-	1.6	-	dB
		$\label{eq:V_CE} \begin{split} V_{CE} &= -5 \ V; \\ I_C &= -0.2 \ mA; \\ R_S &= 2 \ k\Omega; \\ f &= 1 \ kHz; \\ B &= 200 \ Hz \end{split}$	-	3.1	-	dB
Per device						
h <sub>FE1</sub> /h <sub>FE2</sub>	h <sub>FE</sub> matching	$V_{CE} = -5 V;$ $I_{C} = -2 mA$	<u>3</u> 0.9	1	-	
$V_{BE1} - V_{BE2}$	V <sub>BE</sub> matching	V <sub>CE</sub> = -5 V; I <sub>C</sub> = -2 mA	[4] _	-	2	mV

#### Table 8. Characteristics ...continued

 $= 25 \,^{\circ}C$  unless otherwise specified Τ

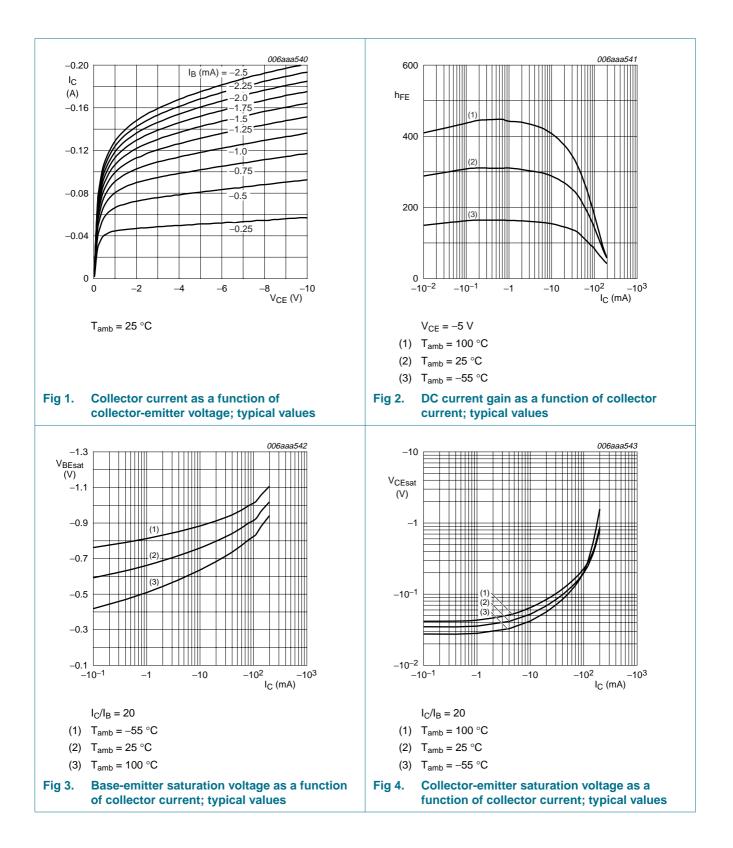
[1]  $V_{BEsat}$  decreases by about 1.7 mV/K with increasing temperature.

[2] V<sub>BE</sub> decreases by about 2 mV/K with increasing temperature.

[3] The smaller of the two values is taken as the numerator.

[4] The smaller of the two values is subtracted from the larger value.

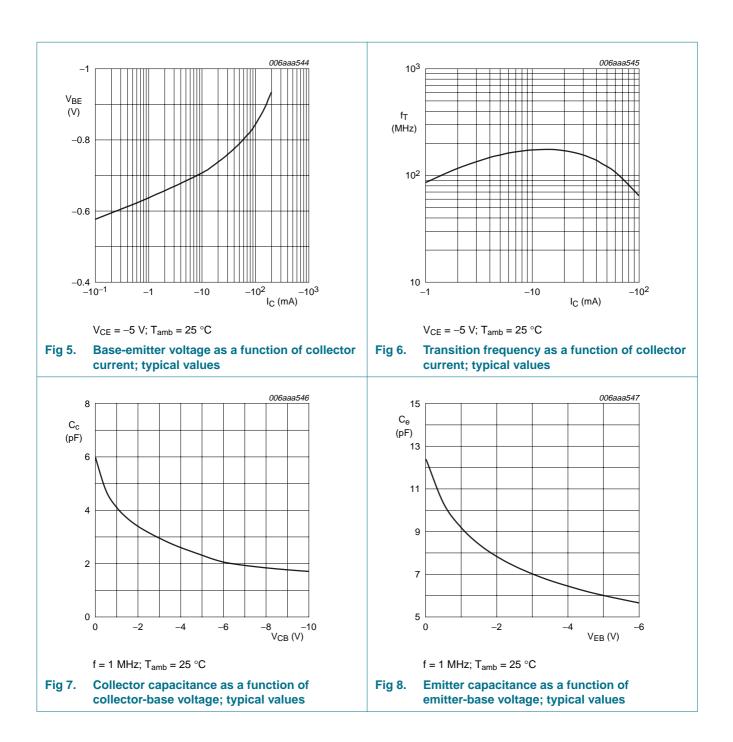
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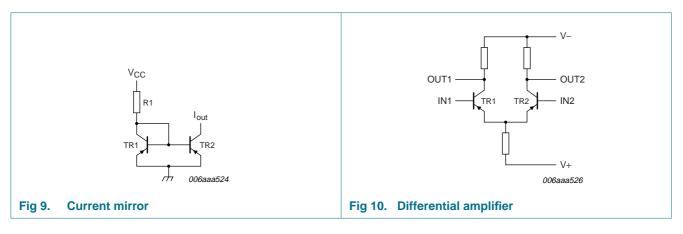
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### 8. Application information

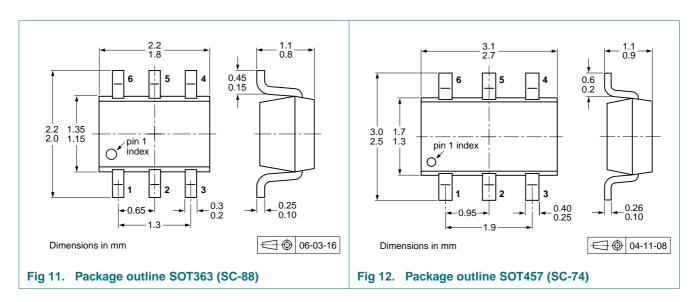


### 9. Test information

#### 9.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

### **10. Package outline**



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### **11. Packing information**

#### Table 9. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

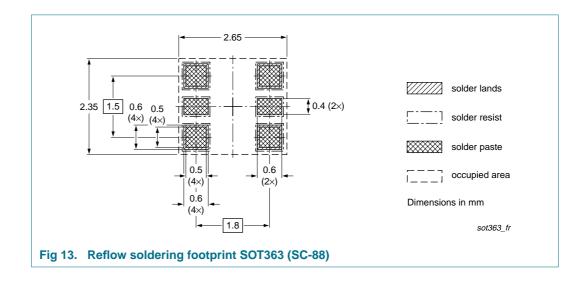
Type number Package Descrip		Description	Packing q	uantity
			3000	10000
BCM856BS	SOT363	4 mm pitch, 8 mm tape and reel; T1	-115	-135
		4 mm pitch, 8 mm tape and reel; T2	-125	-165
BCM856BS/DG	SOT363	4 mm pitch, 8 mm tape and reel; T1	-115	-135
		4 mm pitch, 8 mm tape and reel; T2	-125	-165
BCM856DS	SOT457	4 mm pitch, 8 mm tape and reel; T1	-115	-135
		4 mm pitch, 8 mm tape and reel; T2	-125	-165
BCM856DS/DG	SOT457	4 mm pitch, 8 mm tape and reel; T1	-115	-135
		4 mm pitch, 8 mm tape and reel; T2	-125	-165

[1] For further information and the availability of packing methods, see <u>Section 15</u>.

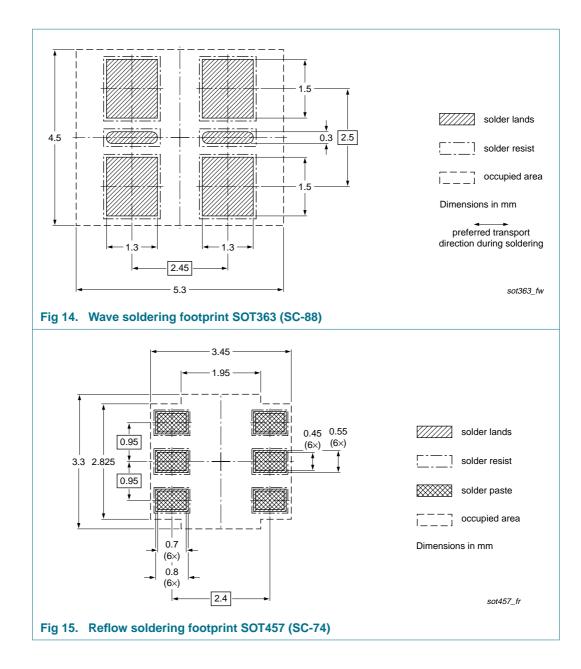
[2] T1: normal taping

[3] T2: reverse taping

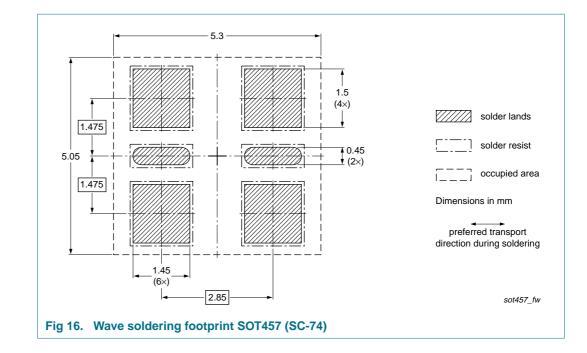
### 12. Soldering



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## **13. Revision history**

Table 10. Revision history							
Document ID	Release date	Data sheet status	Change notice	Supersedes			
BCM856BS_BCM856DS_1	20080807	Product data sheet	-	-			

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### 14. Legal information

#### 14.1 Data sheet status

Document status[1][2]	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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#### **16. Contents**

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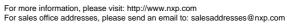
**PHILIPS** 

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