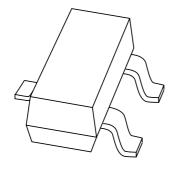
# DISCRETE SEMICONDUCTORS

# DATA SHEET



# **BSS64**NPN high voltage transistor

Product data sheet Supersedes data of 2004 Jan 16 2004 Mar 12



NXP Semiconductors Product data sheet

# NPN high voltage transistor

**BSS64** 

#### **FEATURES**

- Low current (max. 100 mA)
- High voltage (max. 80 V).

#### **APPLICATIONS**

- High-voltage general purpose and switching applications
- Intended for thick and thin-film circuit applications.

#### **DESCRIPTION**

NPN transistor in a SOT23 plastic package. PNP complement: BSS63.

#### **MARKING**

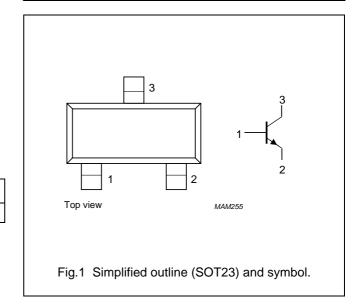
TYPE NUMBER	MARKING CODE(1)		
BSS64	60* or AM		

#### Note

- 1. \* = p: Made in Hong Kong.
  - \* = t: Made in Malaysia.
  - \* = W: Made in China.

#### **PINNING**

PIN	DESCRIPTION	
1	base	
2	emitter	
3	collector	



#### **ORDERING INFORMATION**

TYPE NUMBER	PACKAGE		
ITPE NUMBER	NAME DESCRIPTION VERSION		VERSION
BSS64	_	plastic surface mounted package; 3 leads	SOT23

#### LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	_	120	V
V <sub>CEO</sub>	collector-emitter voltage	open base	_	80	V
V <sub>EBO</sub>	emitter-base voltage	open collector	_	5	V
I <sub>C</sub>	collector current (DC)		_	100	mA
I <sub>CM</sub>	peak collector current		_	250	mA
I <sub>BM</sub>	peak base current		_	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	_	250	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C

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# NPN high voltage transistor

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#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th(j-a)}$	thermal resistance from junction to ambient	note 1	500	K/W

#### Note

1. Transistor mounted on an FR4 printed-circuit board.

#### **CHARACTERISTICS**

 $T_i = 25$  °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>CBO</sub>	collector-base cut-off current	I <sub>E</sub> = 0; V <sub>CB</sub> = 90 V	_	-	100	nA
		$I_E = 0$ ; $V_{CB} = 90 \text{ V}$ ; $T_j = 150 ^{\circ}\text{C}$	_	_	50	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	I <sub>C</sub> = 0; V <sub>EB</sub> = 5 V	_	0.5	200	nA
h <sub>FE</sub>	DC current gain	I <sub>C</sub> = 1 mA; V <sub>CE</sub> = 1 V	_	60	_	
		$I_C = 10 \text{ mA}; V_{CE} = 1 \text{ V}$	20	80	_	
		$I_C = 20 \text{ mA}; V_{CE} = 1 \text{ V}$	_	55	_	
V <sub>CEsat</sub>	collector-emitter saturation	$I_C = 4 \text{ mA}; I_B = 400 \mu\text{A}$	_	_	150	mV
	voltage	$I_C = 50 \text{ mA}; I_B = 15 \text{ mA}$	_	_	200	mV
C <sub>c</sub>	collector capacitance	I <sub>E</sub> = I <sub>e</sub> = 0; V <sub>CB</sub> = 10 V; f = 1 MHz	_	3	_	pF
f <sub>T</sub>	transition frequency	$I_C = 4 \text{ mA}; V_{CE} = 10 \text{ V}; f = 100 \text{ MHz}$	60	100	_	MHz

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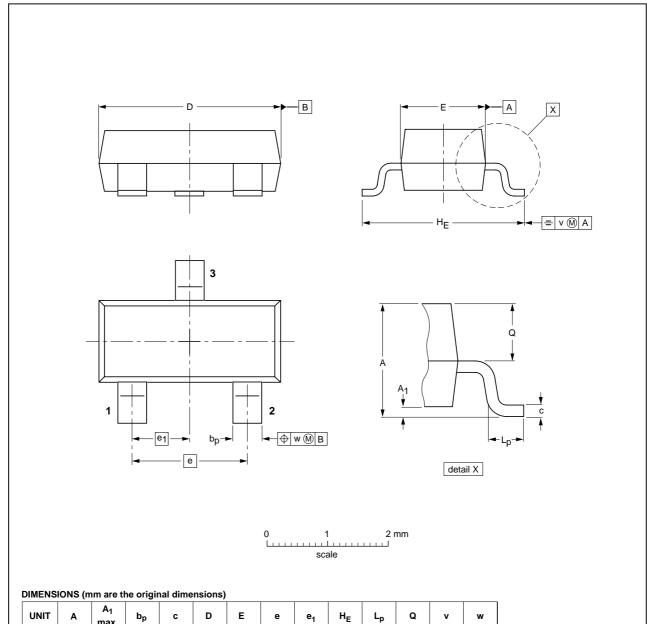
# NPN high voltage transistor

BSS64

#### **PACKAGE OUTLINE**

#### Plastic surface-mounted package; 3 leads

SOT23



OUTLINE	REFERENCES		REFERENCES		EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT23		TO-236AB				<del>04-11-04</del> 06-03-16

0.45

0.55

0.1

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max

0.38

0.9

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### NPN high voltage transistor

**BSS64** 

#### **DATA SHEET STATUS**

DOCUMENT STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)</sup>	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

#### **Notes**

- 1. Please consult the most recently issued document before initiating or completing a design.
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#### **Customer notification**

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

#### **Contact information**

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