

100 V, 1 A low leakage current Schottky barrier rectifier 7 May 2015 **Product data sheet**

1. **General description**

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD123W small and flat lead Surface-Mounted Device (SMD) plastic package.

Features and benefits 2.

- Average forward current: $I_{F(AV)} \le 1 A$ •
- Reverse voltage: V_R ≤ 100 V •
- Low forward voltage: V_F = 710 mV
- High power capability due to clip-bonding technology
- Extremely low leakage current I_R = 40 nA •
- High temperature T_i ≤ 175 °C •
- AEC-Q101 gualified

Applications 3.

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Reverse polarity protection
- Low power consumption applications

Quick reference data 4.

| Table 1. Qui | ck reference data | | | | | |
|--------------------|-------------------------|--|-----|-----|-----|------|
| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
| I _{F(AV)} | average forward current | δ = 0.5; f = 20 kHz; T _{sp} ≤ 170 °C; square wave | - | - | 1 | A |
| V _R | reverse voltage | T _j = 25 °C | - | - | 100 | V |
| V _F | forward voltage | I_F = 1 A; $t_p \le 300$ μs; δ ≤ 0.02 ; T_j = 25 °C | - | 710 | 770 | mV |
| I _R | reverse current | V_R = 100 V; $t_p \le 300 \ \mu$ s; $\delta \le 0.02$; T_j = 25 °C | - | 40 | 150 | nA |





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5. Pinning information

| Table 2. | Pinning | information | | |
|----------|---------|-------------|--------------------|----------------|
| Pin | Symbol | Description | Simplified outline | Graphic symbol |
| 1 | К | cathode[1] | 1 2 | 1 🛃 2 |
| 2 | A | anode | SOD123W | sym001 |

[1] The marking bar indicates the cathode.

6. Ordering information

| Table 3. Ordering inf | formation | | |
|-----------------------|-----------|--|---------|
| Type number | Package | | |
| | Name | Description | Version |
| PMEG10010ELR | SOD123W | plastic surface mounted package; 2 leads | SOD123W |

7. Marking

| Table 4. Marking codes | |
|------------------------|--------------|
| Type number | Marking code |
| PMEG10010ELR | К7 |

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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol | Parameter | Conditions | | Min | Max | Unit |
|--------------------|-------------------------------------|---|-----|-----|------|------|
| V _R | reverse voltage | T _j = 25 °C | | - | 100 | V |
| l _F | forward current | T _{sp} = 165 °C; δ = 1 | | - | 1.4 | А |
| I _{F(AV)} | average forward current | δ = 0.5; f = 20 kHz; T _{amb} ≤ 135 °C; square wave | [1] | - | 1 | A |
| | | δ = 0.5; f = 20 kHz; T _{sp} ≤ 170 °C; square wave | | - | 1 | A |
| I _{FSM} | non-repetitive peak forward current | t_p = 8 ms; $T_{j(init)}$ = 25 °C; square wave | | - | 50 | A |
| P _{tot} | total power dissipation | T _{amb} ≤ 25 °C | [2] | - | 680 | mW |
| | | | [3] | - | 1150 | mW |
| | | | [1] | - | 2140 | mW |
| Tj | junction temperature | | | - | 175 | °C |
| T _{amb} | ambient temperature | | | -55 | 175 | °C |
| T _{stg} | storage temperature | | | -65 | 175 | °C |

[1] Device mounted on a ceramic Printed-Circuit Board (PCB), Al₂O₃, standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

9. Thermal characteristics

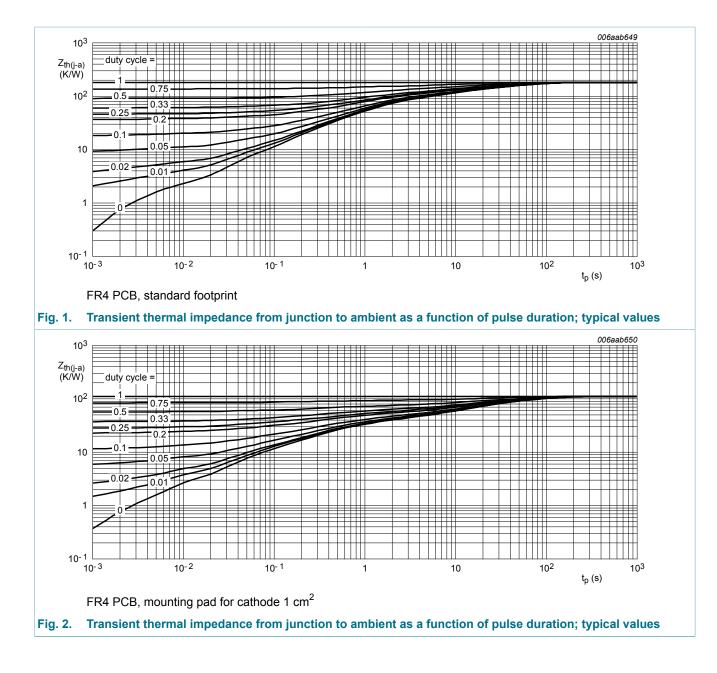
| Table 6. T | hermal characteristics | | | | | | |
|--|--|------------|--------|-----|-----|-----|------|
| Symbol | Parameter | Conditions | | Min | Тур | Max | Unit |
| R _{th(j-a)} thermal resistance from junction to ambient | in free air | [1][2] | - | - | 220 | K/W | |
| | | [1][3] | - | - | 130 | K/W | |
| | ambient | | [1][4] | - | - | 70 | K/W |
| R _{th(j-sp)} | thermal resistance from junction to solder point | | [5] | - | - | 18 | K/W |

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- ^[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².
- [4] Device mounted on a ceramic PCB, AI_2O_3 , standard footprint.
- [5] Soldering point of cathode tab.

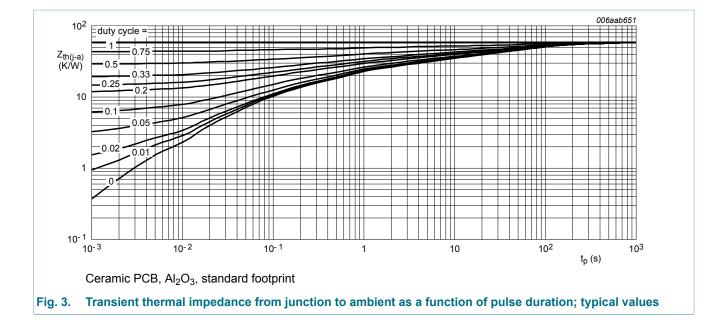
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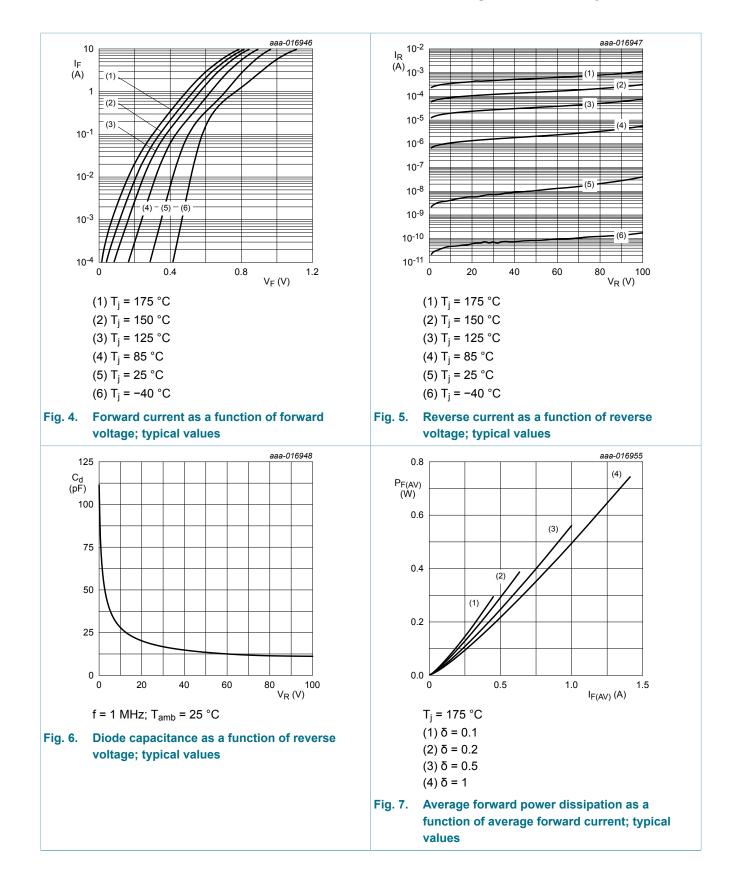
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10. Characteristics

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|--------------------|-------------------------------|---|-----|-----|-----|------|
| V _{(BR)R} | reverse breakdown voltage | I_R = 1 mA; T _j = 25 °C; t _p = 300 μs; δ = 0.02 | 100 | - | - | V |
| V _F | forward voltage | $I_F = 0.1$ A; $t_p \le 300$ μs; δ ≤ 0.02; $T_j = 25$ °C | - | 505 | 565 | mV |
| | | $I_F = 0.5 \text{ A}; t_p \le 300 \text{ μs}; \delta \le 0.02;$ $T_j = 25 \text{ °C}$ | - | 640 | 710 | mV |
| | | $I_F = 0.7$ A; $t_p \le 300$ μs; δ ≤ 0.02 ; $T_j = 25$ °C | - | 675 | 740 | mV |
| | | I_F = 1 A; $t_p \le 300$ μs; δ ≤ 0.02; T_j = 25 °C | - | 710 | 770 | mV |
| | | I_F = 1 A; $t_p \le 300$ μs; δ ≤ 0.02; T_j = 125 °C | - | 575 | 680 | mV |
| I _R | reverse current | V_R = 10 V; $t_p \le 300 \ \mu s$; $\delta \le 0.02$; T_j = 25 °C | - | 4 | - | nA |
| | | V_R = 60 V; $t_p \le 300 \ \mu s$; $\delta \le 0.02$; T_j = 25 °C | - | 12 | - | nA |
| | | V_R = 100 V; $t_p \le 300 \ \mu s; \ \delta \le 0.02;$ T_j = 25 °C | - | 40 | 150 | nA |
| | | V_R = 100 V; t _p ≤ 300 μs; δ ≤ 0.02; T _j = 125 °C | - | 70 | 500 | μA |
| C _d | diode capacitance | V _R = 1 V; f = 1 MHz; T _j = 25 °C | - | 70 | - | pF |
| | | V _R = 4 V; f = 1 MHz; T _j = 25 °C | - | 42 | - | pF |
| | | V _R = 10 V; f = 1 MHz; T _j = 25 °C | - | 28 | - | pF |
| trr | reverse recovery time | $I_F = 0.5 \text{ A}; I_R = 1 \text{ A}; I_{R(meas)} = 0.25 \text{ A};$ $T_j = 25 \text{ °C}$ | - | 3.7 | - | ns |
| V _{FRM} | peak forward recovery voltage | $I_F = 0.5 \text{ A}; \text{ d}I_F/\text{d}t = 20 \text{ A}/\mu\text{s}; \text{ T}_j = 25 ^\circ\text{C}$ | - | 690 | - | V |

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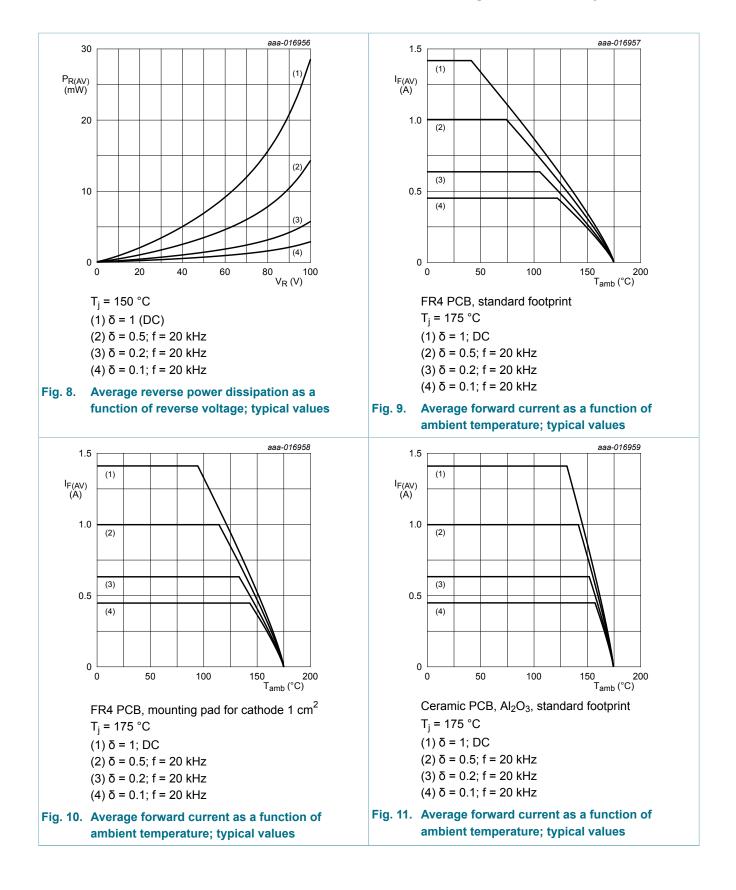
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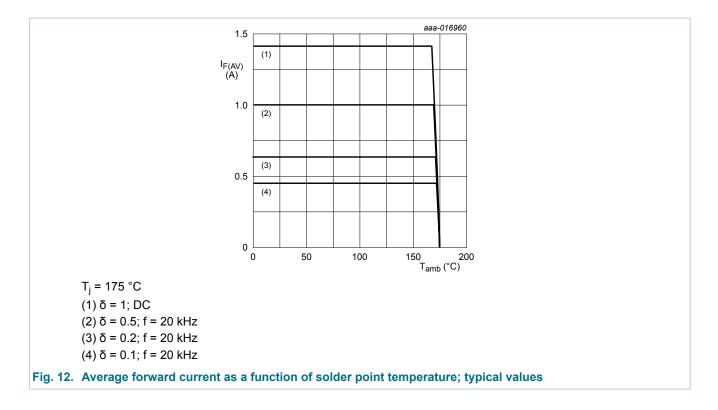
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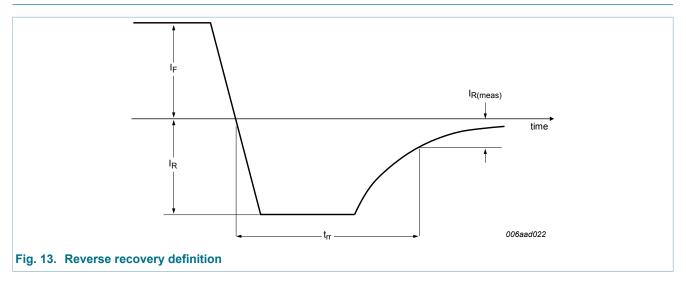


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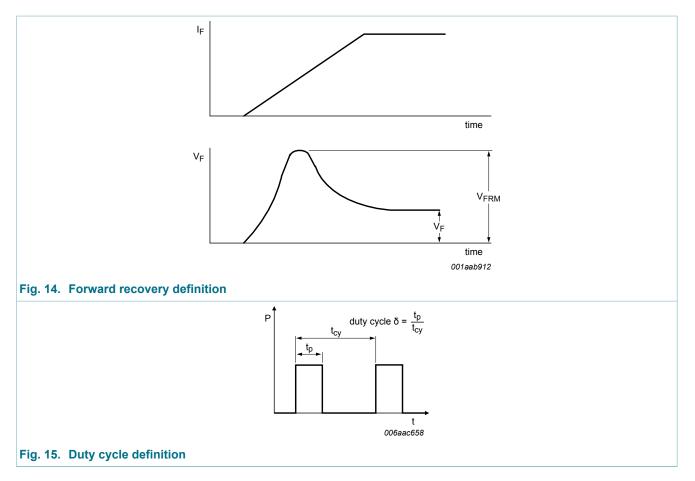


11. Test information



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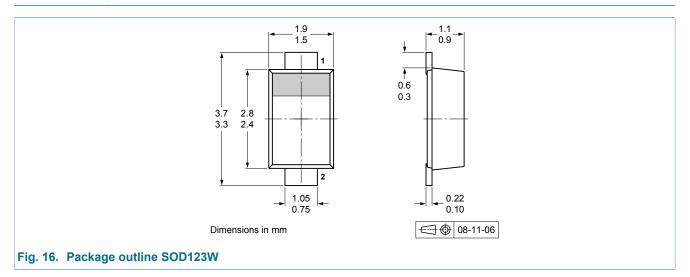
The current ratings for the typical waveforms are calculated according to the equations: $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current, $I_{RMS} = I_{F(AV)}$ at DC, and $I_{RMS} = I_M \times \sqrt{\delta}$ with I_{RMS} defined as RMS current.

11.1 Quality information

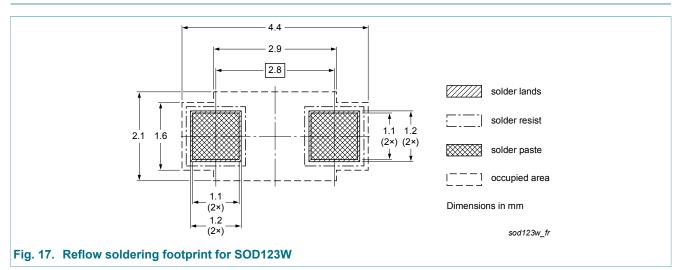
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

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12. Package outline



13. Soldering



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14. Revision history

| Table 8. Revision his | story | | | |
|-----------------------|---------------------|------------------------|---------------|------------------|
| Data sheet ID | Release date | Data sheet status | Change notice | Supersedes |
| PMEG10010ELR v.2 | 20150507 | Product data sheet | - | PMEG10010ELR v.1 |
| Modifications: | Product status char | iged | · | |
| PMEG10010ELR v.1 | 20150220 | Preliminary data sheet | - | - |

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15. Legal information

15.1 Data sheet status

| Document status [1][2] | Product status [<u>3]</u> | Definition |
|--------------------------------------|-------------------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
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