

20 V, 1 A very low V<sub>F</sub> MEGA Schottky barrier rectifier in<br/>SOD323F packageRev. 03 — 15 January 2010Product date

Product data sheet

#### **Product profile** 1.

#### 1.1 General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD323F (SC-90) very small and flat lead Surface Mounted Device (SMD) plastic package.

#### 1.2 Features

- Forward current: ≤1 A
- Reverse voltage: ≤ 20 V
- Very low forward voltage
- Very small and flat lead SMD plastic package

#### 1.3 Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Inverse polarity protection
- Low power consumption applications

#### 1.4 Quick reference data

Table 1.	Quick reference data					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>F</sub>	forward current	$T_{sp} \le 55 \ ^{\circ}C$	-	-	1	А
V <sub>R</sub>	reverse voltage		-	-	20	V
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 1000 mA	<u>[1]</u> _	480	550	mV
-						

[1] Pulse test:  $t_p \le 300 \ \mu s$ ;  $\delta \le 0.02$ .



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## 2. Pinning information

Pin	Description	Simplified outline	Symbol
1	cathode	[1]	
2	anode		1 🕂 2
_			s

[1] The marking bar indicates the cathode.

## 3. Ordering information

Table 3. Ord	ering information	on	
Type number	Package		
	Name	Description	Version
PMEG2010AEJ	SC-90	plastic surface mounted package; 2 leads	SOD323F

### 4. Marking

Table 4. Marking codes	
Type number	Marking code
PMEG2010AEJ	EM

### 5. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>R</sub>	reverse voltage			-	20	V
I <sub>F</sub>	forward current	$T_{sp} \le 55 \ ^{\circ}C$		-	1	А
I <sub>FRM</sub>	repetitive peak forward current	$t_p \leq 1 \text{ ms; } \delta \leq 0.25$		-	5.5	А
I <sub>FSM</sub>	non-repetitive peak forward current	square wave; t <sub>p</sub> = 8 ms		-	10	А
P <sub>tot</sub>	total power dissipation	$T_{amb} \leq 25 ~^{\circ}C$	[1]	-	360	mW
			[2]	-	830	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	+150	°C
T <sub>stg</sub>	storage temperature			-65	+150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

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### 6. Thermal characteristics

Table 6.	Thermal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
uii(j-a)	thermal resistance from junction to ambient	in to ambient	<u>[1][2]</u>	-	-	350	K/W
			[1][3]	-	-	150	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		<u>[4]</u> .	-	-	55	K/W

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses  $P_R$  are a significant part of the total power losses. Nomograms for determining the reverse power losses  $P_R$  and  $I_{F(AV)}$  rating are available on request.

- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.
- [4] Solder point of cathode tab.

### 7. Characteristics

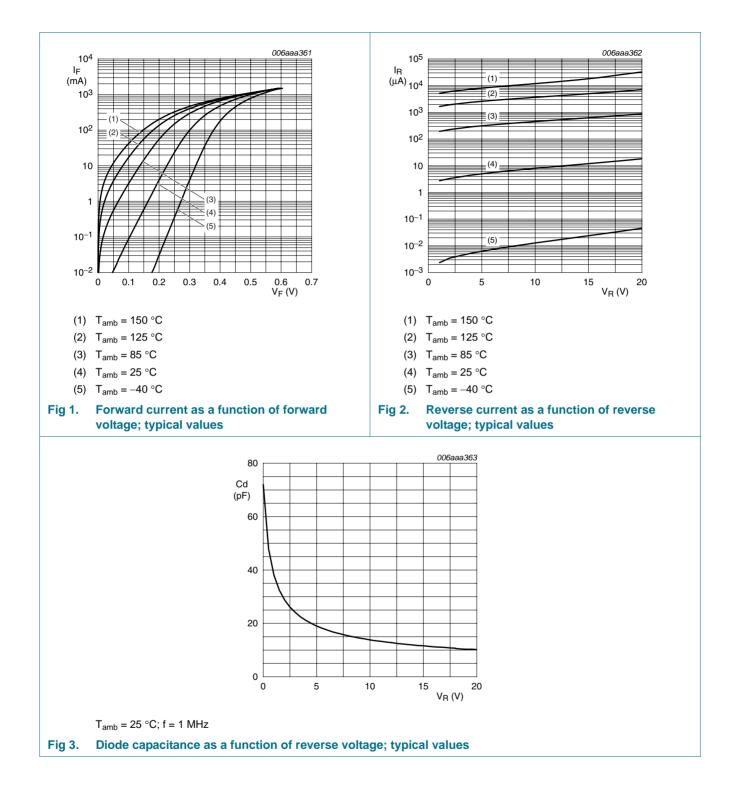
#### Table 7. Characteristics

 $T_{amb} = 25 \ ^{\circ}C$  unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>F</sub>	forward voltage		<u>[1]</u>			
		I <sub>F</sub> = 10 mA	-	240	270	mV
	I <sub>F</sub> = 100 mA	-	300	350	mV	
	I <sub>F</sub> = 500 mA	-	400	460	mV	
	I <sub>F</sub> = 1000 mA	-	480	550	mV	
I <sub>R</sub> reverse	reverse current	V <sub>R</sub> = 5 V	-	5	10	μΑ
		V <sub>R</sub> = 8 V	-	7	20	μΑ
		V <sub>R</sub> = 10 V	-	8	30	μΑ
		V <sub>R</sub> = 15 V	-	10	50	μΑ
		V <sub>R</sub> = 20 V	-	15	70	μΑ
C <sub>d</sub>	diode capacitance	V <sub>R</sub> = 1 V; f = 1 MHz	-	40	50	pF

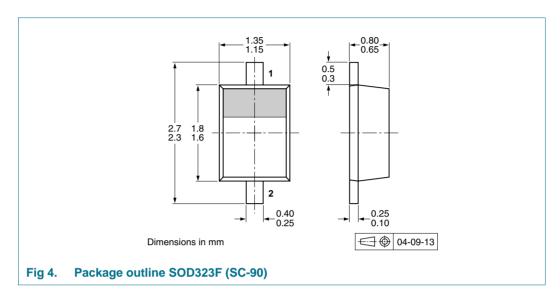
[1] Pulse test:  $t_p \le 300 \ \mu s; \delta \le 0.02$ .

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#### 8. Package outline



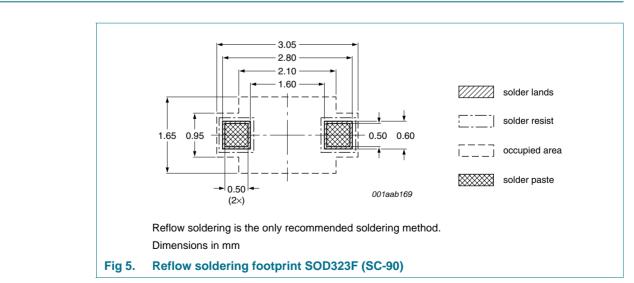
## 9. Packing information

#### Table 8. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Type number	Package	Description	Packing	quantity
			3000	10000
PMEG2010AEJ	SOD323F	4 mm pitch, 8 mm tape and reel	-115	-135

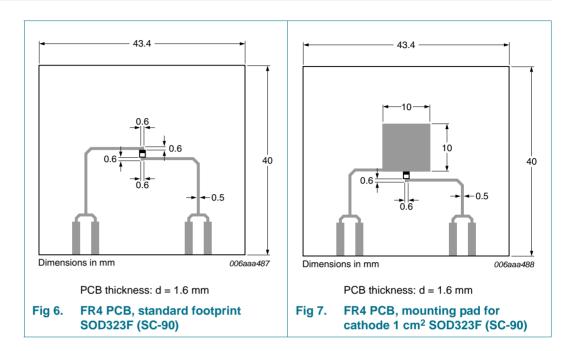
[1] For further information and the availability of packing methods, see Section 14.



## **10. Soldering**

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## 11. Mounting



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## 12. Revision history

Table 9. Revision his	story			
Document ID	Release date	Data sheet status	Change notice	Supersedes
PMEG2010AEJ_3	20100115	Product data sheet	-	PMEG2010AEJ_2
Modifications:		eet was changed to reflect w legal definitions and disc		
PMEG2010AEJ_2	20051014	Product data sheet	-	PMEG2010AEJ_1
PMEG2010AEJ_1	20050302	Product data sheet	-	-

## 13. Legal information

#### **13.1 Data sheet status**

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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