

PMEG4005CT

500 mA low V_F dual MEGA Schottky barrier rectifier Rev. 2 — 20 September 2010 Product

Product data sheet

Product profile

1.1 General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier in common cathode configuration with an integrated guard ring for stress protection, encapsulated in a SOT23 (TO-236AB) small Surface-Mounted Device (SMD) plastic package.

1.2 Features and benefits

- Average forward current: I_{F(AV)} ≤ 0.5 A
 AEC-Q101 qualified
- Reverse voltage: V_R ≤ 40 V
- Low forward voltage

- Small SMD plastic package

1.3 Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch Mode Power Supply (SMPS)
- Reverse polarity protection
- High-speed switching
- Low power consumption applications

1.4 Quick reference data

Table 1. Quick reference data $T_i = 25$ °C unless otherwise specified.

J	<u>'</u>					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per diode						
I _{F(AV)}	average forward current	square wave; δ = 0.5; f = 20 kHz				
		$T_{amb} \le 85 ^{\circ}C$	[1] -	-	0.5	Α
		$T_{sp} \le 130 ^{\circ}C$	-	-	0.5	Α
V_R	reverse voltage		-	-	40	V
V_{F}	forward voltage	$I_F = 0.5 A$	-	410	470	mV
I _R	reverse current	$V_R = 40 V$	-	27	100	μΑ

^[1] Device mounted on a ceramic Printed-Circuit Board (PCB), Al₂O₃, standard footprint.



2. Pinning information

Table 2. Pinning

Table 2.	Finning		
Pin	Description	Simplified outline	Graphic symbol
1	anode (diode 1)		
2	anode (diode 2)	3	3
3	common cathode	1 2	1 2 006aaa438

3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PMEG4005CT	-	plastic surface-mounted package; 3 leads	SOT23

4. Marking

Table 4. Marking codes

Type number	Marking code ^[1]
PMEG4005CT	PA*

^{[1] * = -:} made in Hong Kong

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter Conditions		Min	Max	Unit
Per diode					
V_R	reverse voltage	T _j = 25 °C	-	40	V
I _{F(AV)}	average forward current	square wave; δ = 0.5; f = 20 kHz			
		$T_{amb} \le 85 ^{\circ}C$	<u>[1]</u> _	0.5	Α
		$T_{sp} \le 130 ^{\circ}C$	-	0.5	Α
I _{FRM}	repetitive peak forward current	$\begin{array}{l} t_p \leq 1 \text{ ms;} \\ \delta \leq 0.25 \end{array}$	-	3.9	Α
I _{FSM}	non-repetitive peak forward current	square wave; t _p = 8 ms	[2] -	10	А
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PMEG4005CT

^{* =} p: made in Hong Kong

^{* =} t: made in Malaysia

^{* =} W: made in China

 Table 5.
 Limiting values ...continued

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
Per device;	one diode loaded				
P _{tot}	total power dissipation	$T_{amb} \le 25 ^{\circ}C$	[3] _	330	mW
			[4] _	400	mW
			[1] -	460	mW
Tj	junction temperature		-	150	°C
T _{amb}	ambient temperature		-55	+150	°C
T _{stg}	storage temperature		-65	+150	°C

^[1] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.

6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per diode;	one diode loaded					
· · · · · · · · · · · · · · · · · · ·	thermal resistance from	in free air	<u>[1]</u>			
	junction to ambient		[2] _	-	375	K/W
			[3]	-	310	K/W
			[4] -	-	270	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point		<u>[5]</u> _	-	60	K/W

^[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

^[2] $T_j = 25$ °C prior to surge.

^{3]} Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

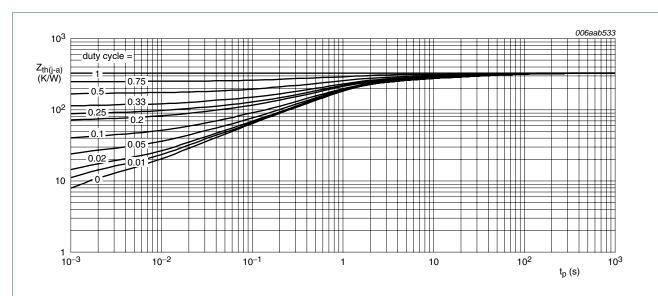
^[4] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

^[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

^[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

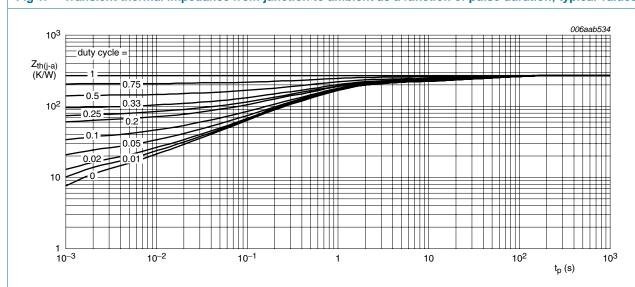
^[4] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.

^[5] Soldering point of cathode tab.



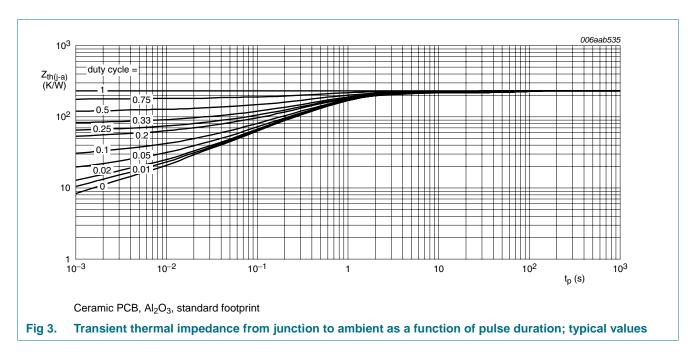
FR4 PCB, standard footprint

Fig 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for cathode 1 cm²

Fig 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



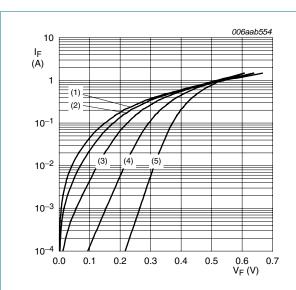
7. Characteristics

Table 7. Characteristics

 $T_i = 25$ °C unless otherwise specified.

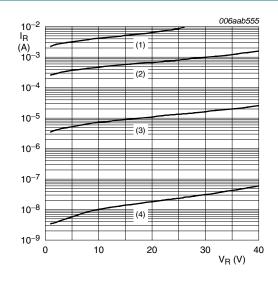
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per diode						
V _F forv	forward voltage	$I_F = 0.1 \text{ mA}$	-	95	130	mV
		I _F = 1 mA	-	155	210	mV
		I _F = 10 mA	-	220	270	mV
		I _F = 100 mA	-	295	350	mV
		I _F = 500 mA	-	410	470	mV
I _R	reverse current	$V_R = 10 \text{ V}$	-	7	20	μΑ
		V _R = 40 V	-	27	100	μΑ
C _d	diode capacitance	$V_R = 1 V$; $f = 1 MHz$	-	43	50	рF
t _{rr}	reverse recovery time	;	[1] -	13	-	ns

^[1] When switched from I_F = 10 mA to I_R = 10 mA; R_L = 100 Ω ; measured at I_R = 1 mA.



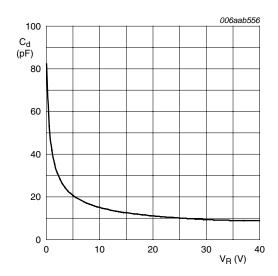
- (1) $T_j = 150 \,^{\circ}\text{C}$
- (2) $T_i = 125 \, ^{\circ}C$
- (3) $T_j = 85 \,^{\circ}C$
- (4) $T_j = 25 \, ^{\circ}C$
- (5) $T_i = -40 \, ^{\circ}\text{C}$

Fig 4. Forward current as a function of forward voltage; typical values



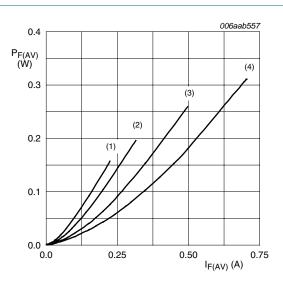
- (1) $T_j = 125 \, ^{\circ}C$
- (2) $T_j = 85 \, ^{\circ}C$
- (3) $T_j = 25 \, ^{\circ}C$
- (4) $T_j = -40 \, ^{\circ}C$

Fig 5. Reverse current as a function of reverse voltage; typical values



f = 1 MHz; T_{amb} = 25 °C

Fig 6. Diode capacitance as a function of reverse voltage; typical values



T_i = 150 °C

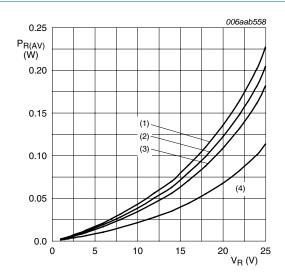
(1)
$$\delta = 0.1$$

(2)
$$\delta = 0.2$$

(3)
$$\delta = 0.5$$

(4)
$$\delta = 1$$

Fig 7. Average forward power dissipation as a function of average forward current; typical values



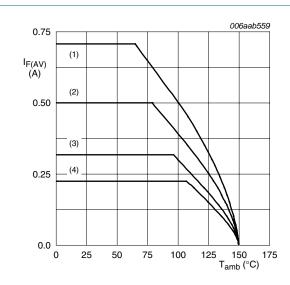
(1)
$$\delta = 1$$

(2)
$$\delta = 0.9$$

(3)
$$\delta = 0.8$$

(4)
$$\delta = 0.5$$

Fig 8. Average reverse power dissipation as a function of reverse voltage; typical values



FR4 PCB, standard footprint

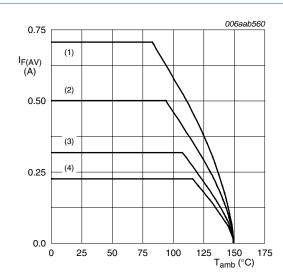
(1) $\delta = 1$; DC

(2) $\delta = 0.5$; f = 20 kHz

(3) $\delta = 0.2$; f = 20 kHz

(4) $\delta = 0.1$; f = 20 kHz

Fig 9. Average forward current as a function of ambient temperature; typical values



FR4 PCB, mounting pad for cathode 1 cm²

(1) $\delta = 1$; DC

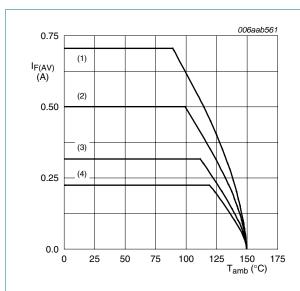
(2) $\delta = 0.5$; f = 20 kHz

(3) $\delta = 0.2$; f = 20 kHz

(4) $\delta = 0.1$; f = 20 kHz

Fig 10. Average forward current as a function of ambient temperature; typical values

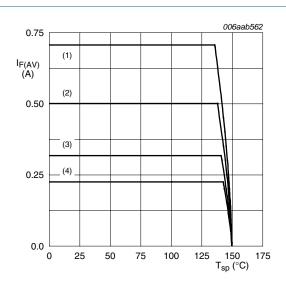
Product data sheet



Ceramic PCB, Al₂O₃, standard footprint

- (1) $\delta = 1$; DC
- (2) $\delta = 0.5$; f = 20 kHz
- (3) $\delta = 0.2$; f = 20 kHz
- (4) $\delta = 0.1$; f = 20 kHz

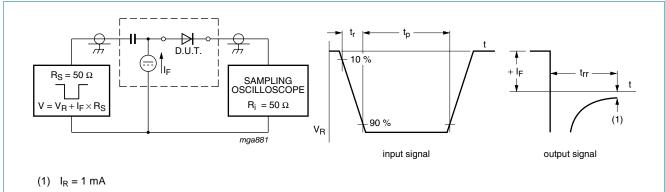
Fig 11. Average forward current as a function of ambient temperature; typical values



- (1) $\delta = 1$; DC
- (2) $\delta = 0.5$; f = 20 kHz
- (3) $\delta = 0.2$; f = 20 kHz
- (4) $\delta = 0.1$; f = 20 kHz

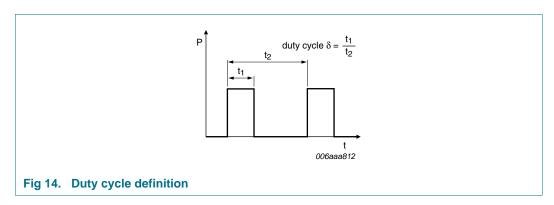
Fig 12. Average forward current as a function of solder point temperature; typical values

8. Test information



Input signal: reverse pulse rise time t_r = 0.6 ns; reverse voltage pulse duration t_p = 100 ns; duty cycle δ = 0.05 Oscilloscope: rise time t_r = 0.35 ns

Fig 13. Reverse recovery time test circuit and waveforms



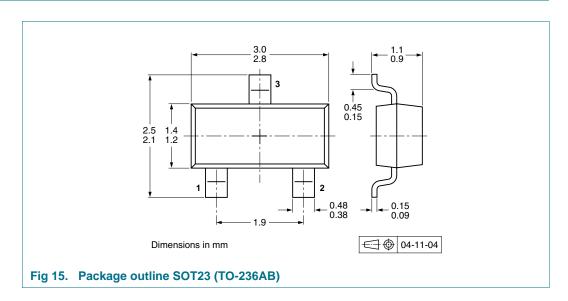
The current ratings for the typical waveforms as shown in Figure 9, 10, 11 and 12 are calculated according to the equations: $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current,

 $I_{RMS} = I_{F(AV)}$ at DC, and $I_{RMS} = I_M \times \sqrt{\delta}$ with I_{RMS} defined as RMS current.

8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

9. Package outline



10. Packing information

Table 8. Packing methods

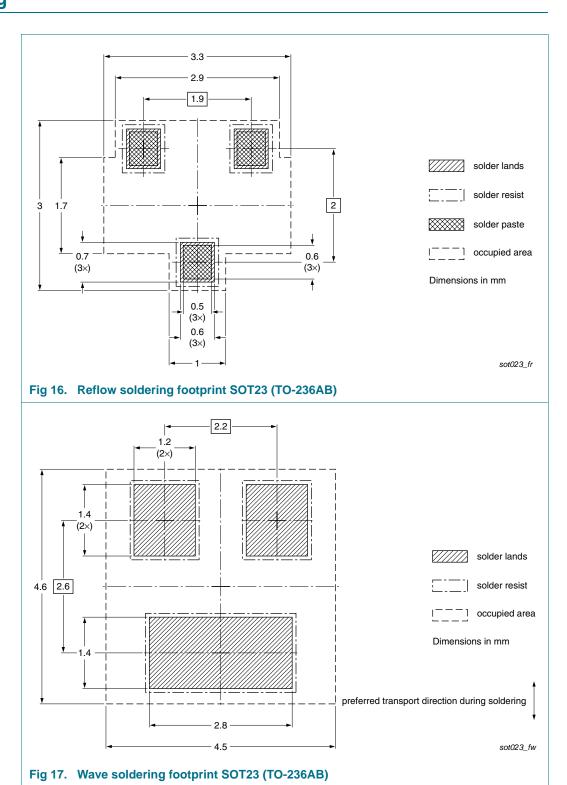
The indicated -xxx are the last three digits of the 12NC ordering code.[1]

		•	0		
Type number	Package	Description		Packing q	uantity
				3000	10000
PMEG4005CT	SOT23	4 mm pitch, 8 mm tape and reel		-215	-235

PMEG4005CT

[1] For further information and the availability of packing methods, see Section 14.

11. Soldering





12. Revision history

Table 9. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
PMEG4005CT v.2	20100920	Product data sheet	-	PMEG4005CT_1
Modifications:	• Table 2 "Pinr	ning": Graphic symbol amende	d	
	 Section 13 "L 	<u>egal information"</u> : updated		
PMEG4005CT_1	20090605	Product data sheet	-	-

13. Legal information

13.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
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NXP Semiconductors PMEG4005CT

500 mA low V_F dual MEGA Schottky barrier rectifier

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500 mA low V_F dual MEGA Schottky barrier rectifier

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